

LMI-based Robust MPC Design

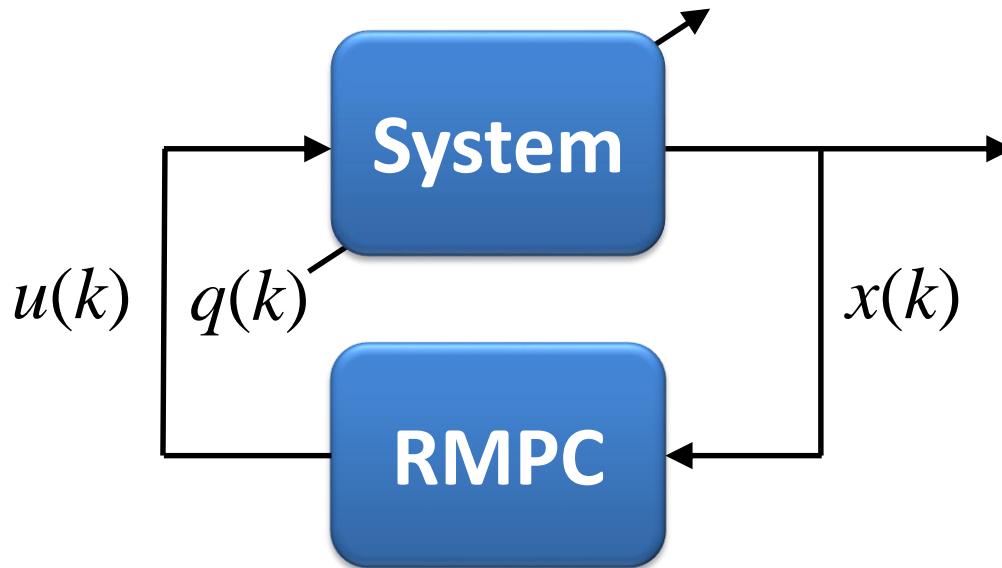
Toolbox MUP

Juraj Oravec – Monika Bakošová

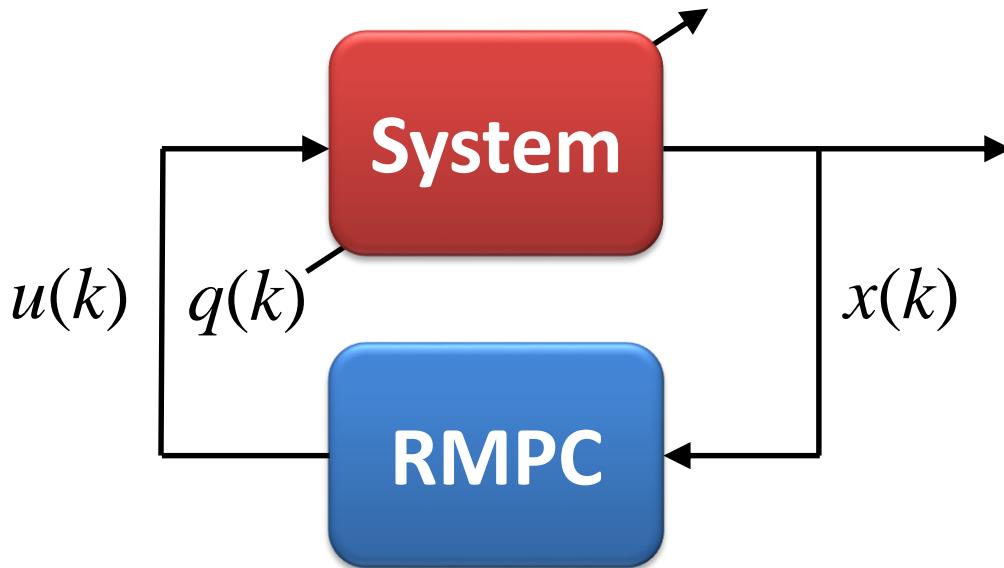
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Slovak University of Technology in Bratislava

Problems to solve

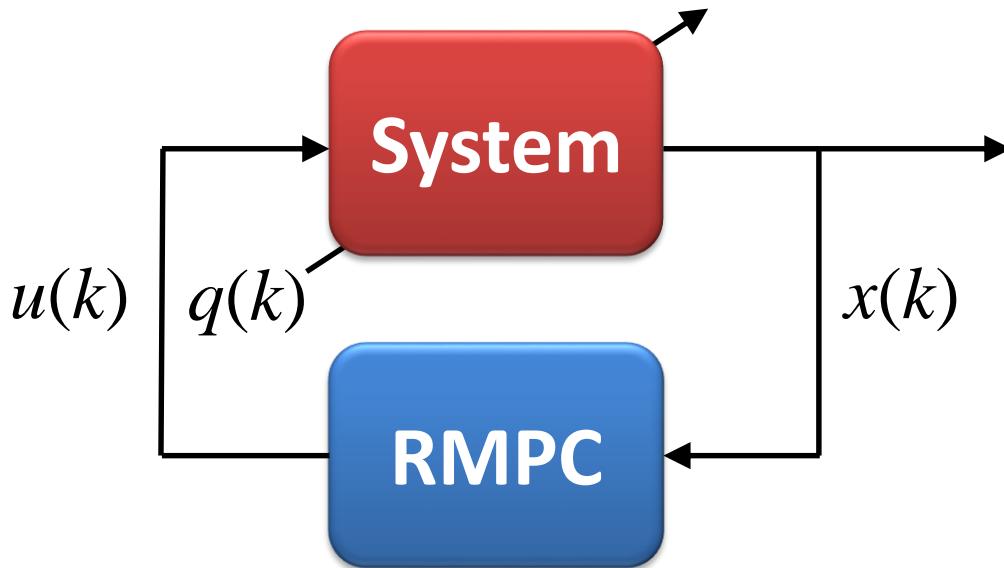


Problems to solve



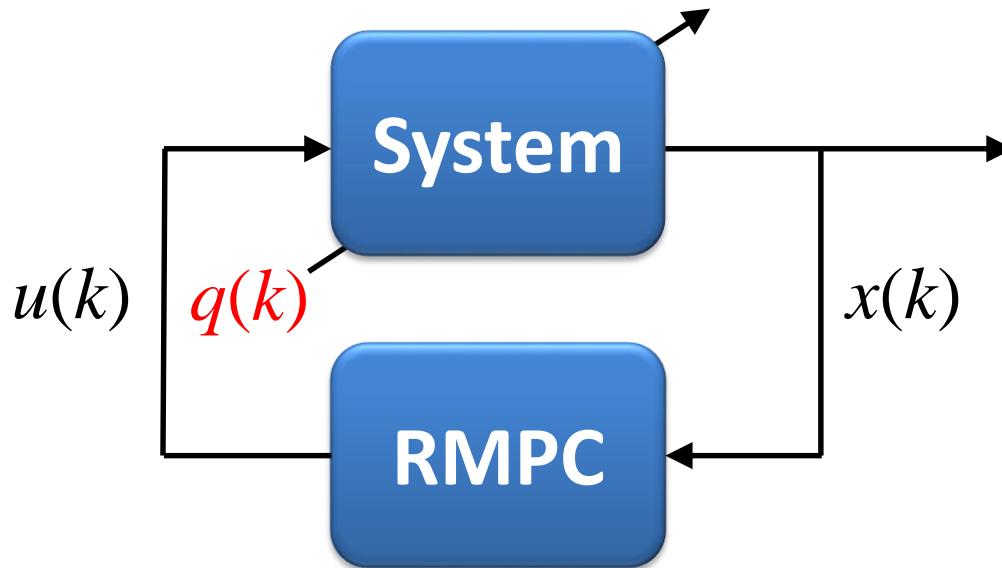
$$x(k+1) = A x(k) + B u(k), \quad x(0) = x_0,$$
$$y(k) = C x(k),$$

Problems to solve



$$x(k+1) = A x(k) + B u(k), \quad x(0) = x_0,$$
$$y(k) = C x(k), \quad u \in \mathbb{U}, \quad y \in \mathbb{Y},$$

Problems to solve

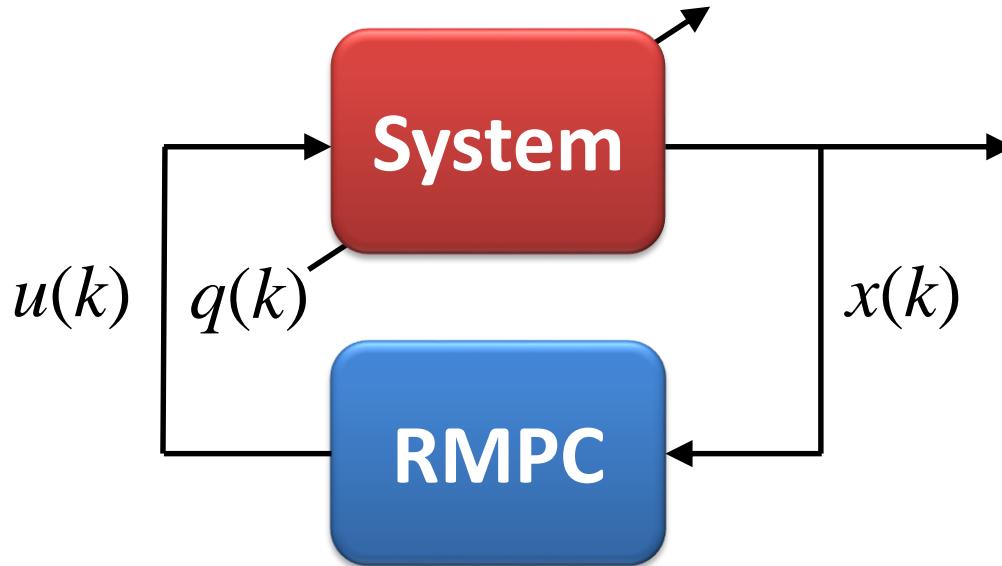


$$x(k+1) = A x(k) + B u(k), \quad x(0) = x_0,$$

$$y(k) = C x(k), \quad u \in \mathbb{U}, \quad y \in \mathbb{Y},$$

$$q \in \mathbb{Q}.$$

Problems to solve

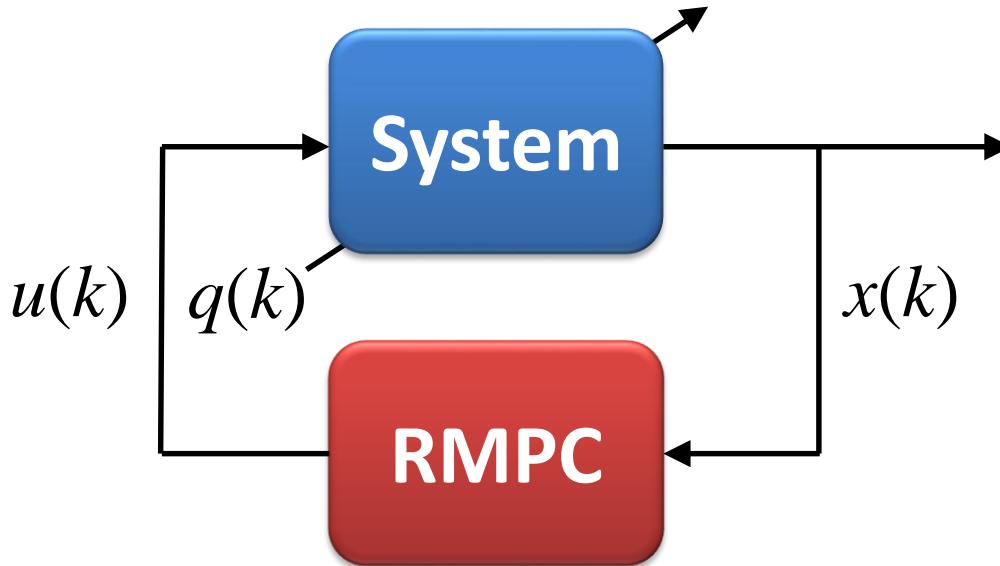


$$x(k+1) = A x(k) + B u(k), \quad x(0) = x_0,$$

$$y(k) = C x(k), \quad u \in \mathbb{U}, \quad y \in \mathbb{Y},$$

$$[A, B] \in \text{convhull}\{[A_v, B_v]\}, \quad q \in \mathbb{Q}.$$

Analysis

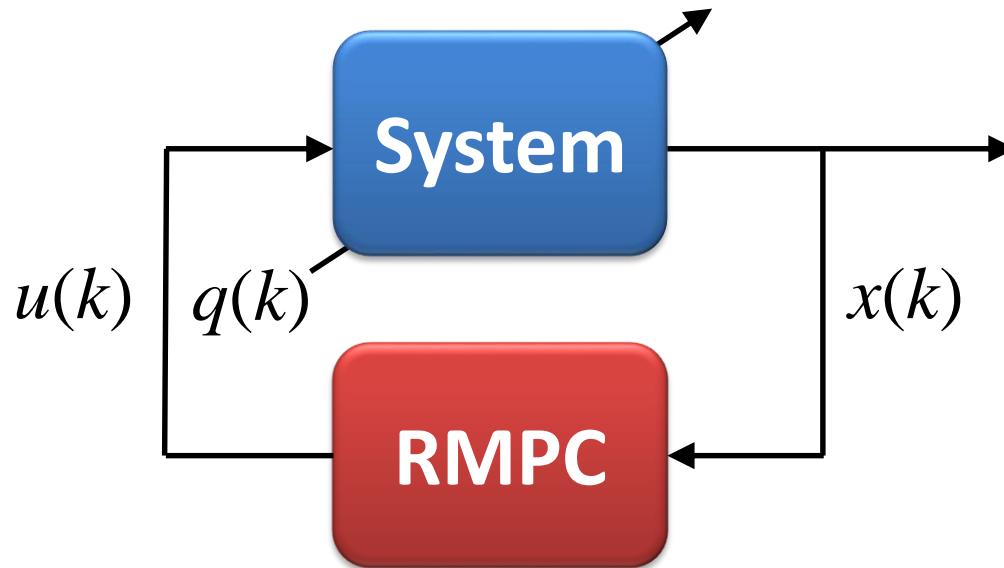


SDP:

$$\min c^\top x$$

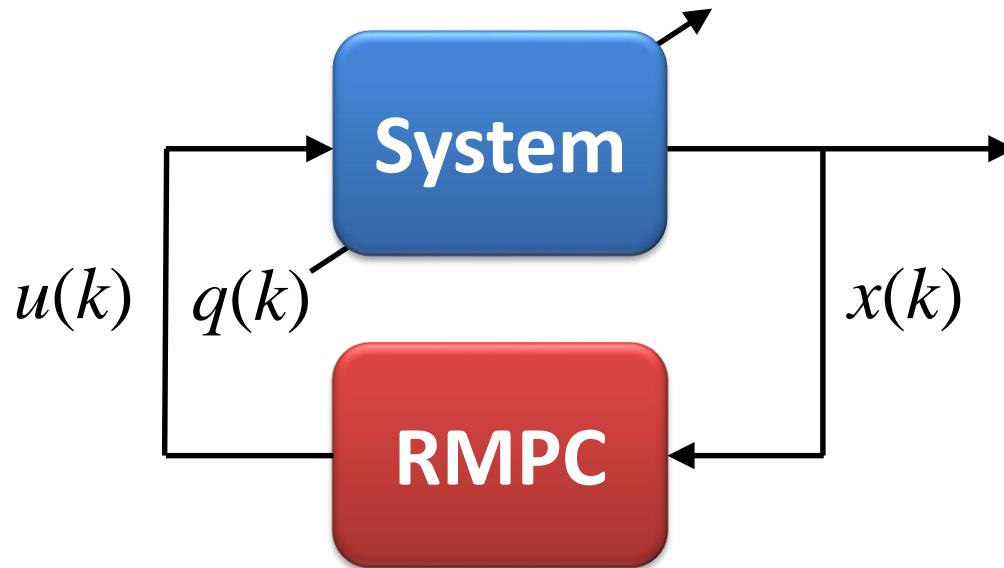
$$\text{s.t.: } x \preceq_{\mathcal{K}} 0 \Rightarrow M(x) \prec 0$$

Synthesis



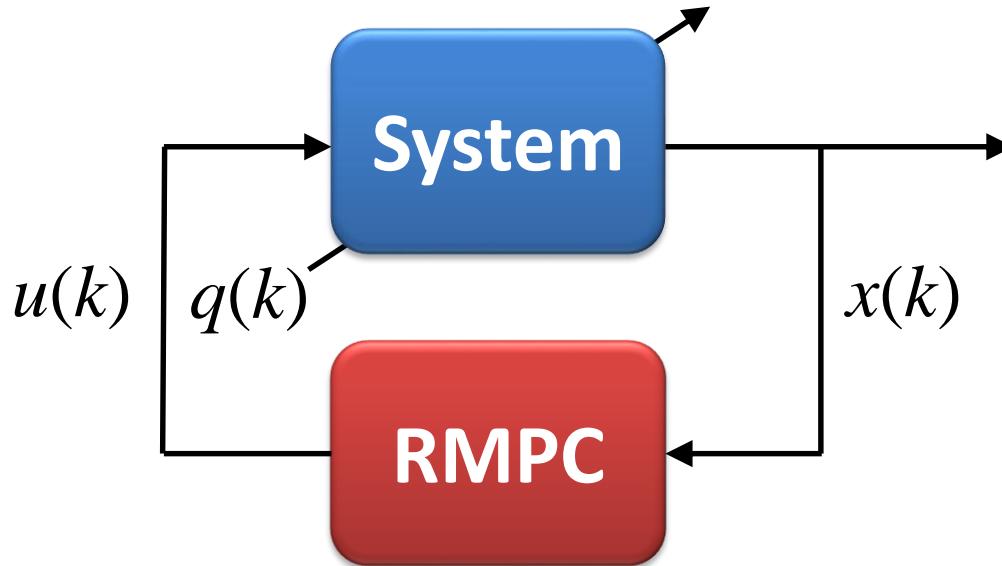
Control law: $u(k) = F(x(k)) x(k)$

Synthesis



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Synthesis

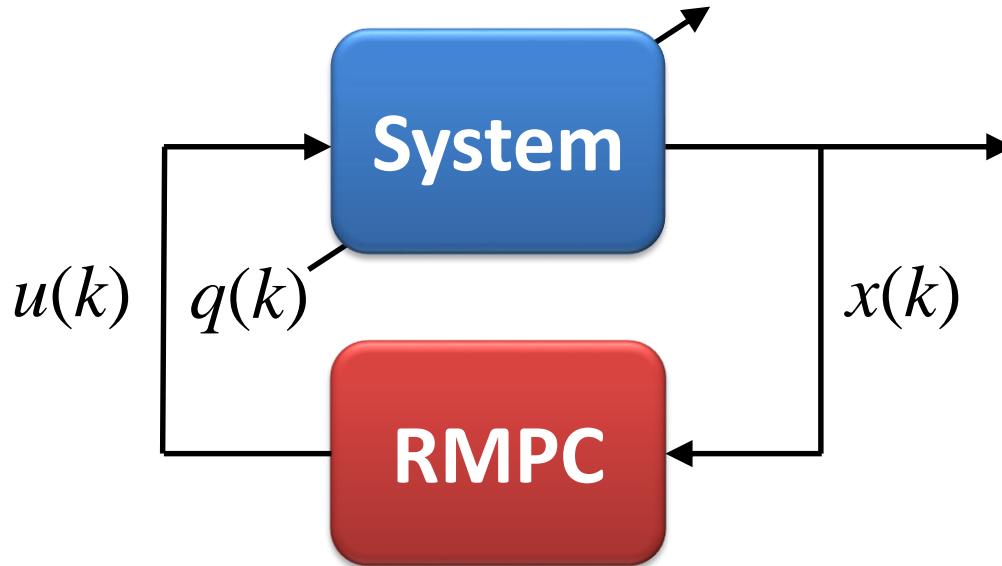


Control law: $u(k) = F(x(k)) x(k)$

Quality criterion:

$$J(k) = \sum_{i=0}^{\infty} (x(k+i)^\top Q x(k+i) + u(k+i)^\top R u(k+i))$$

Synthesis

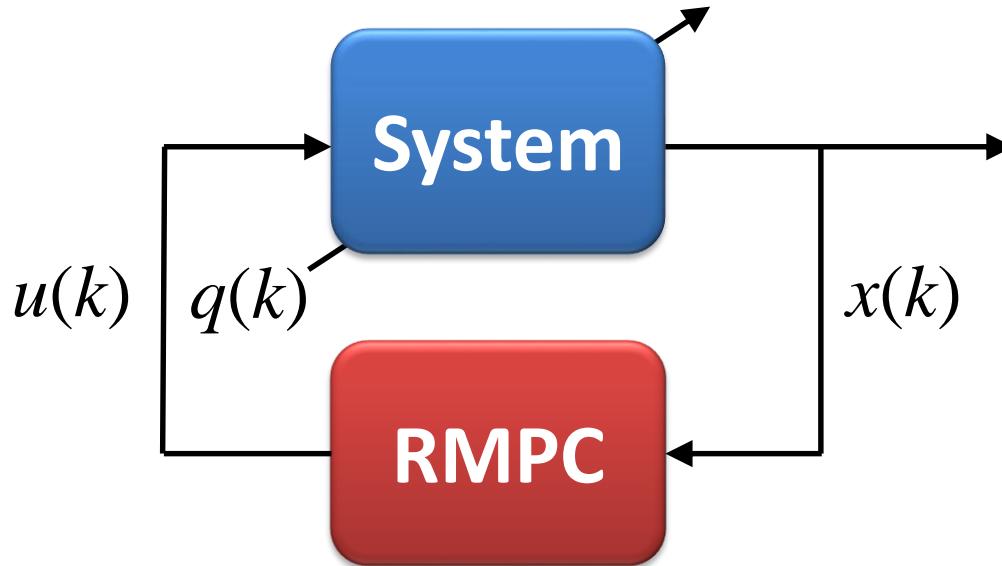


Control law: $u(k) = F(x(k)) x(k)$

Quality criterion:

$$J(k) = \sum_{i=0}^{\infty} (x(k+i)^T Q x(k+i) + u(k+i)^T R u(k+i))$$

Synthesis

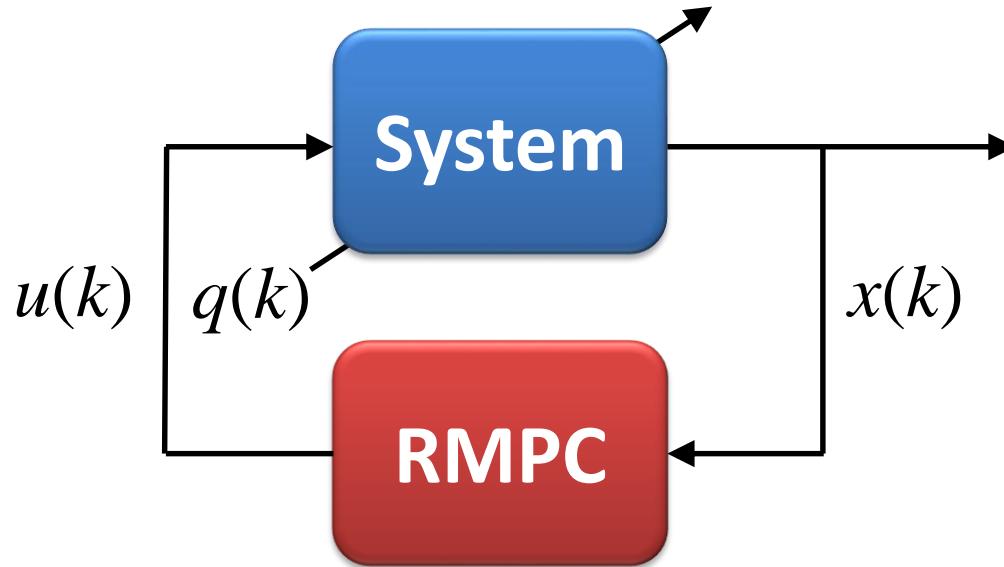


Control law: $u(k) = F(x(k)) x(k)$

Quality criterion:

$$J(k) = \sum_{i=0}^{\infty} (x(k+i)^T \mathbf{W_x} x(k+i) + u(k+i)^T \mathbf{W_u} u(k+i))$$

Synthesis

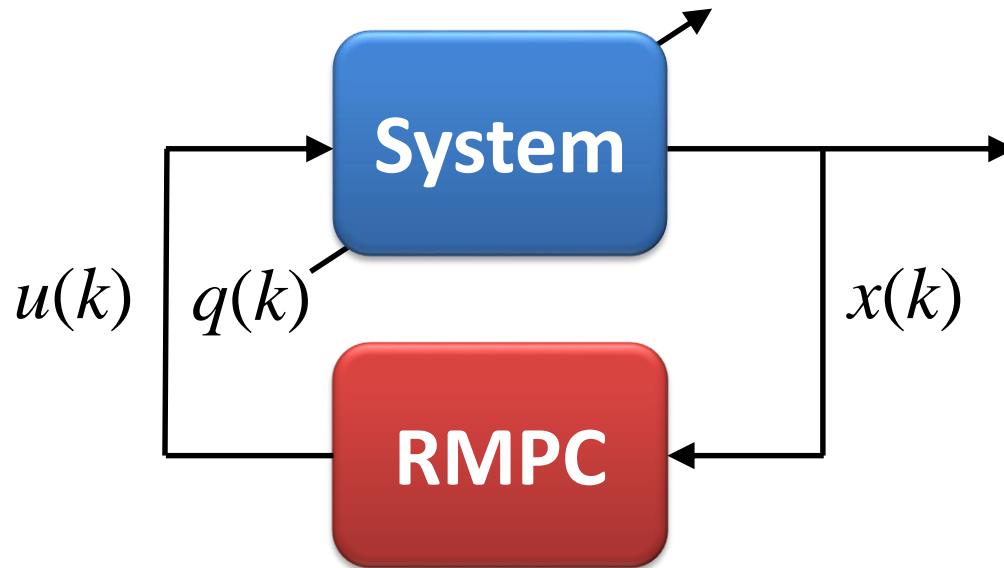


Control law: $u(k) = F(x(k)) x(k)$

Quality criterion:

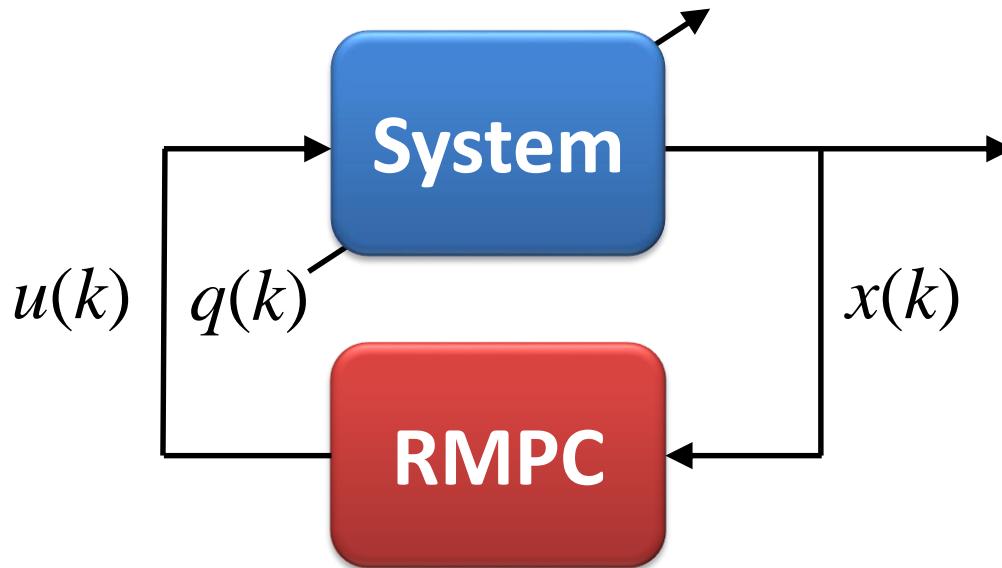
$$J(k) = \sum_{i=0}^{\infty} \ell(x(k+i), u(k+i))$$

Toolbox



MATLAB toolbox for on-line RMPC design by LMIs

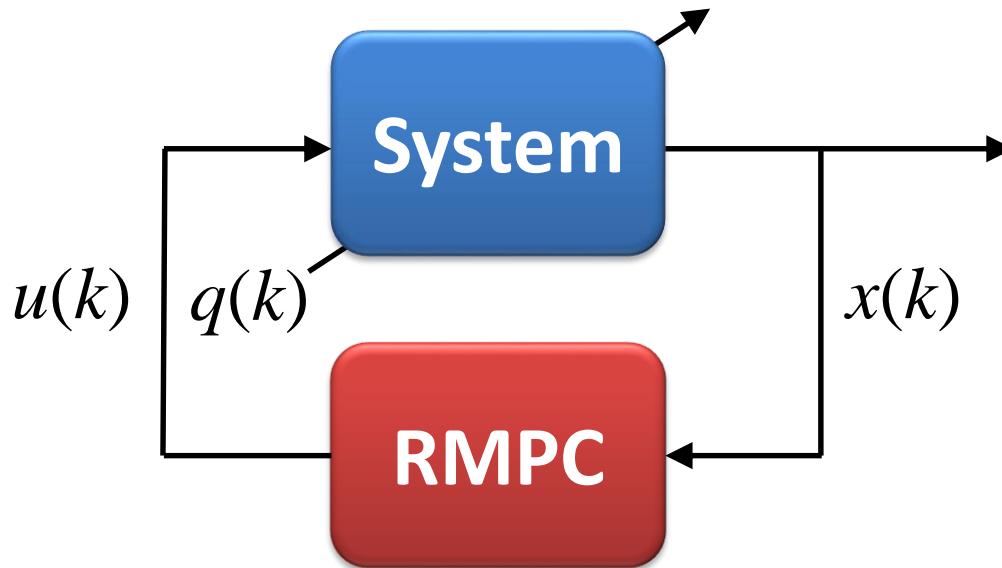
Toolbox



MATLAB toolbox for on-line RMPC design by LMIs

- speed up RMPC design and tuning
- advanced RMPC design analysis
- user-friendly tool

Toolbox

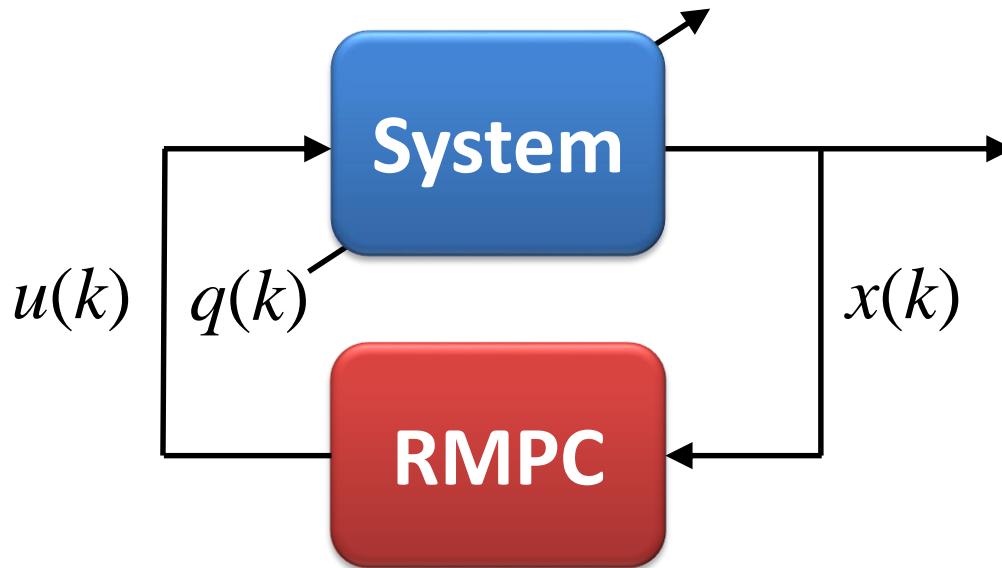


MATLAB toolbox for on-line RMPC design by LMIs

- freely available*
- support* (demo projects, wiki, issues)
- under development (*new release coming soon*)

* bitbucket.org/oravec/mup

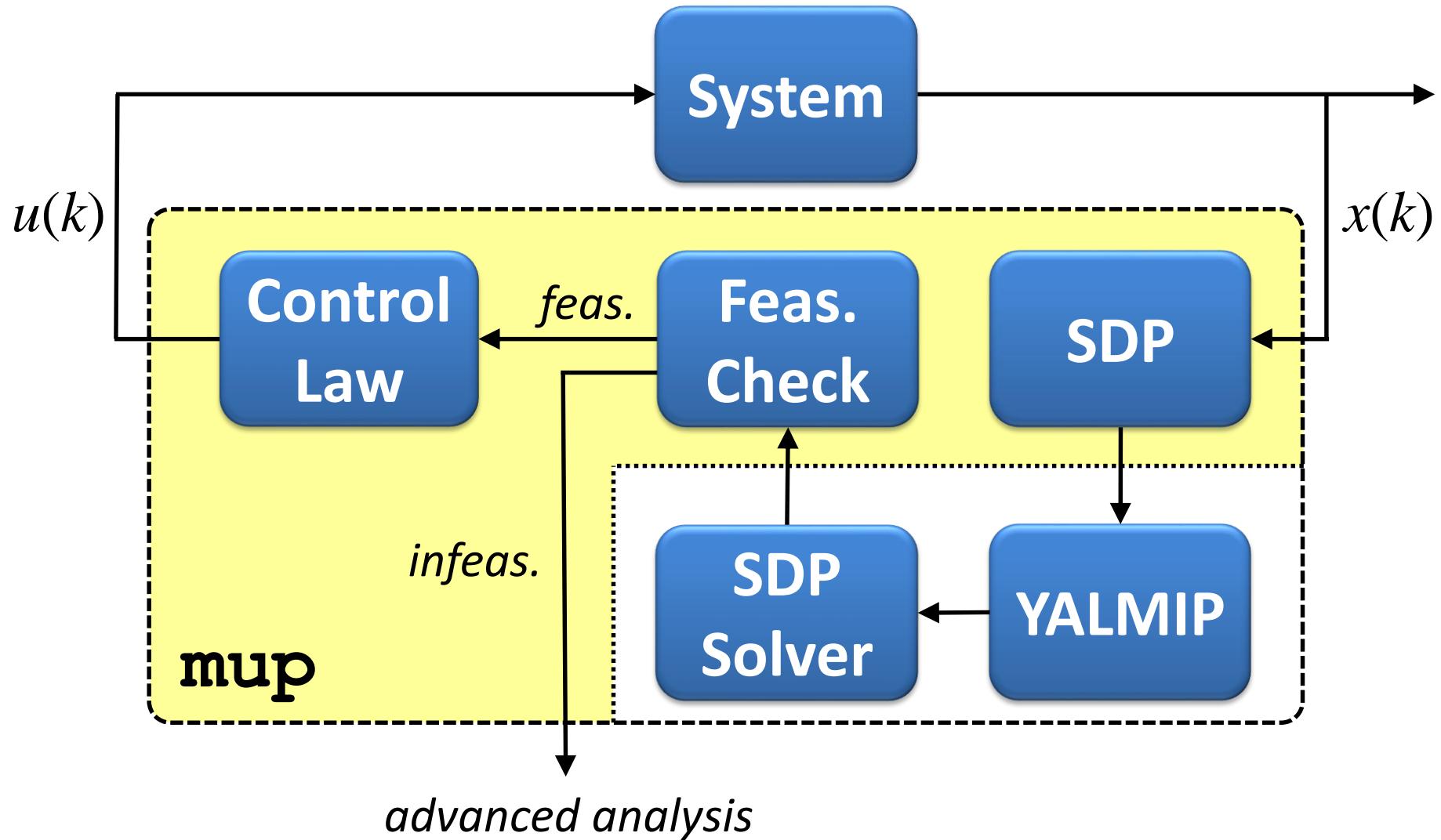
Toolbox



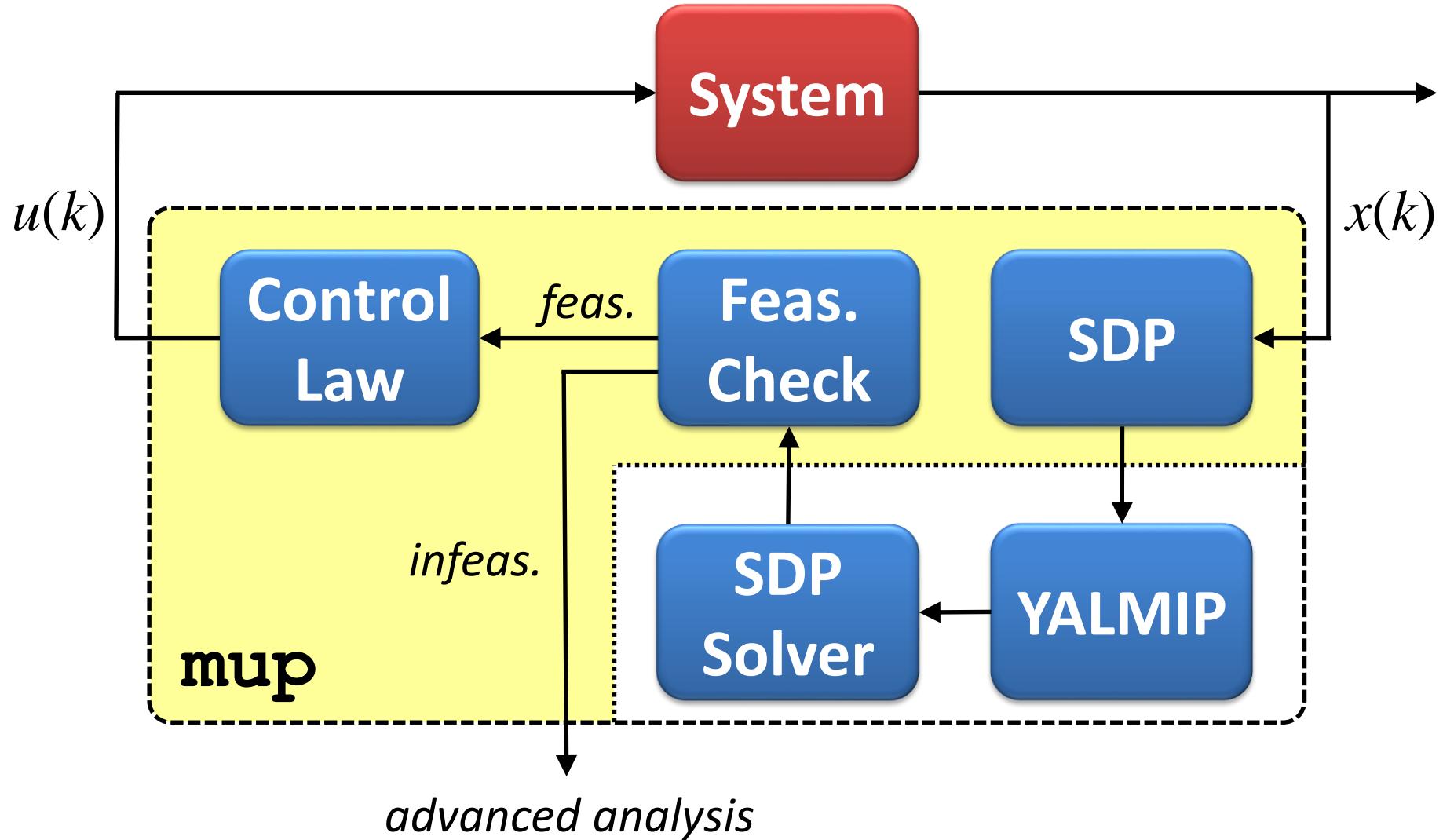
Necessary theoretical backgrounds:

- LQR/MPC basic (*required*)
- RMPC (*optional*)
- SDP (*optional*)

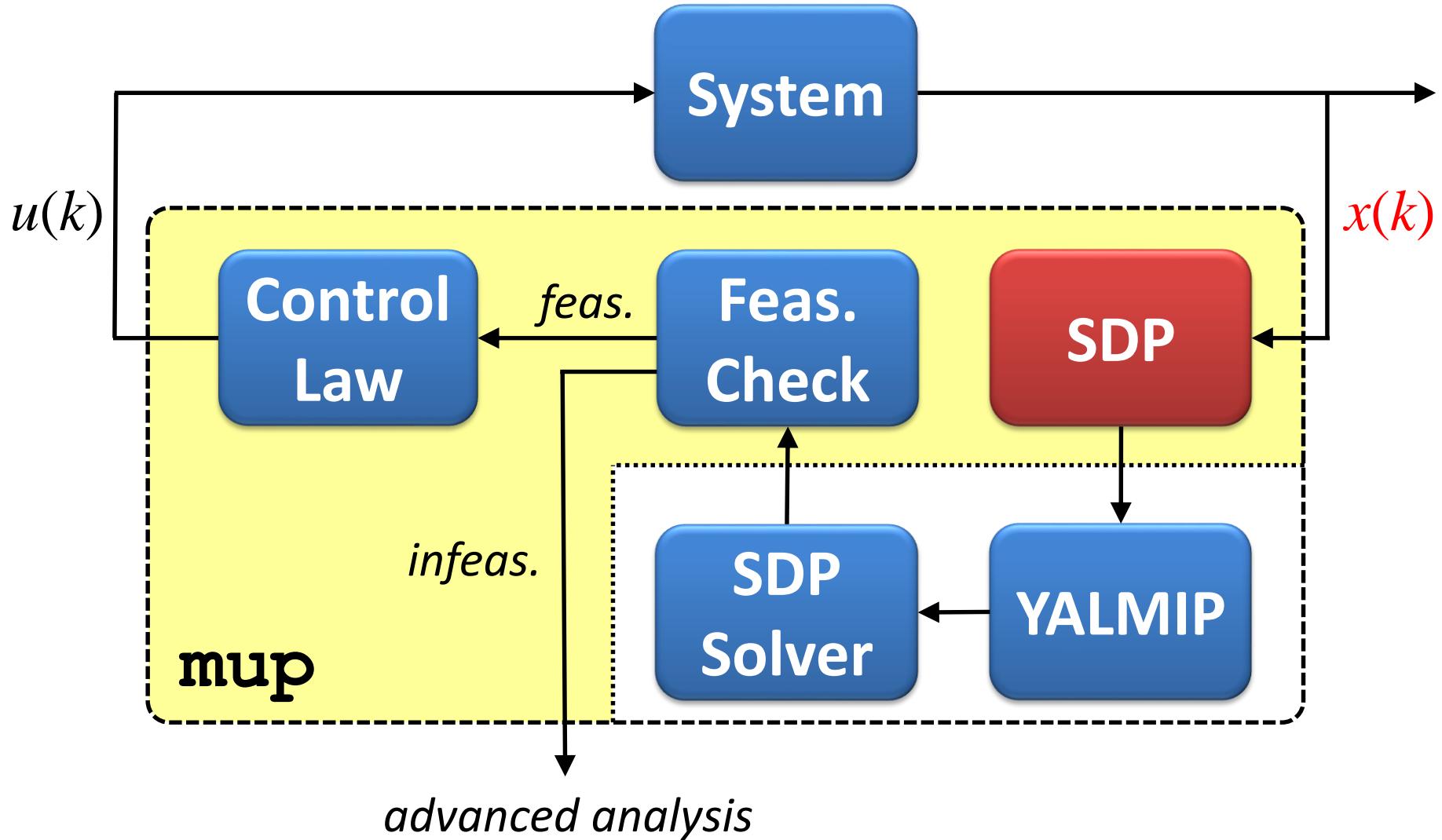
Framework



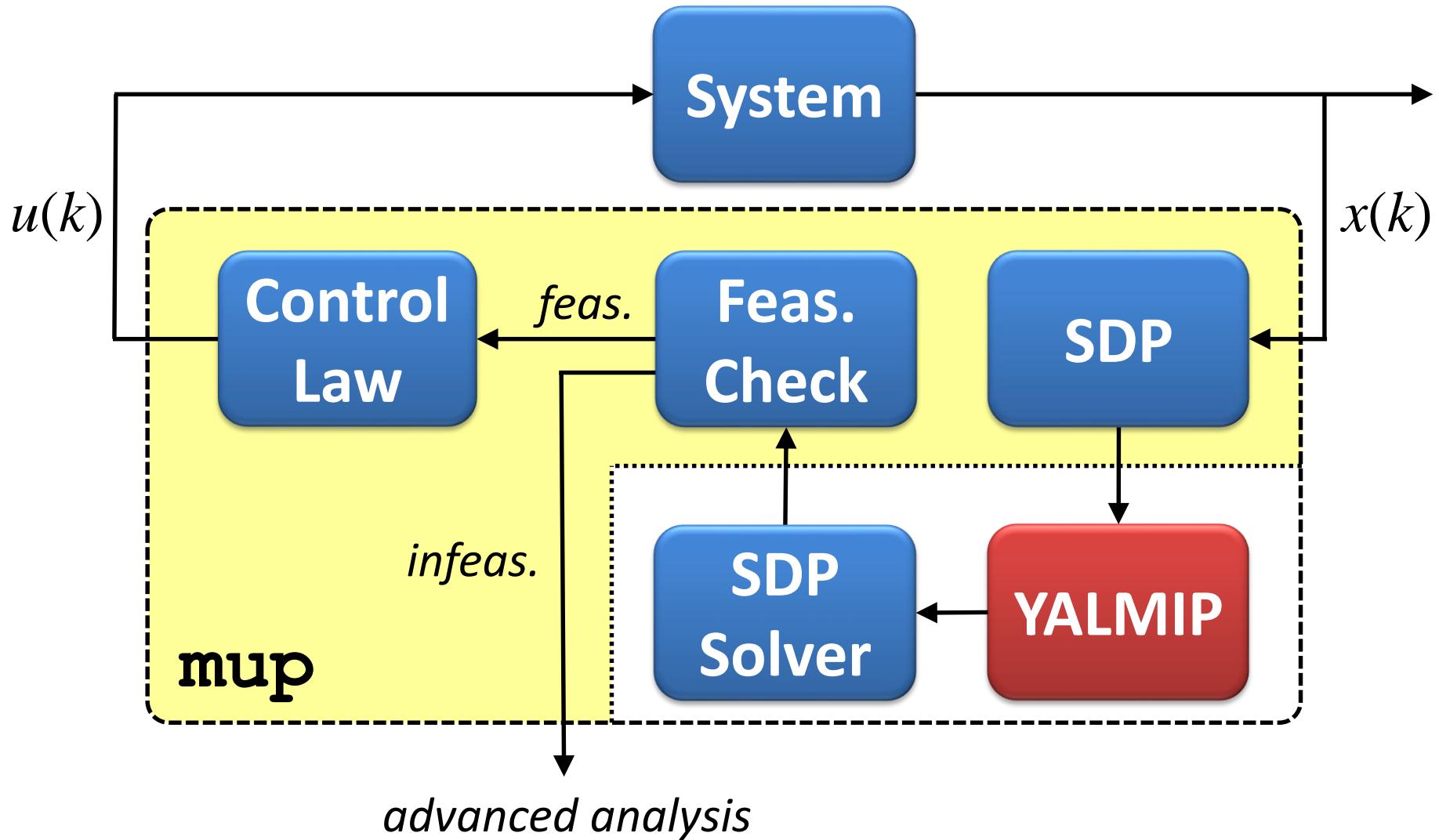
Framework



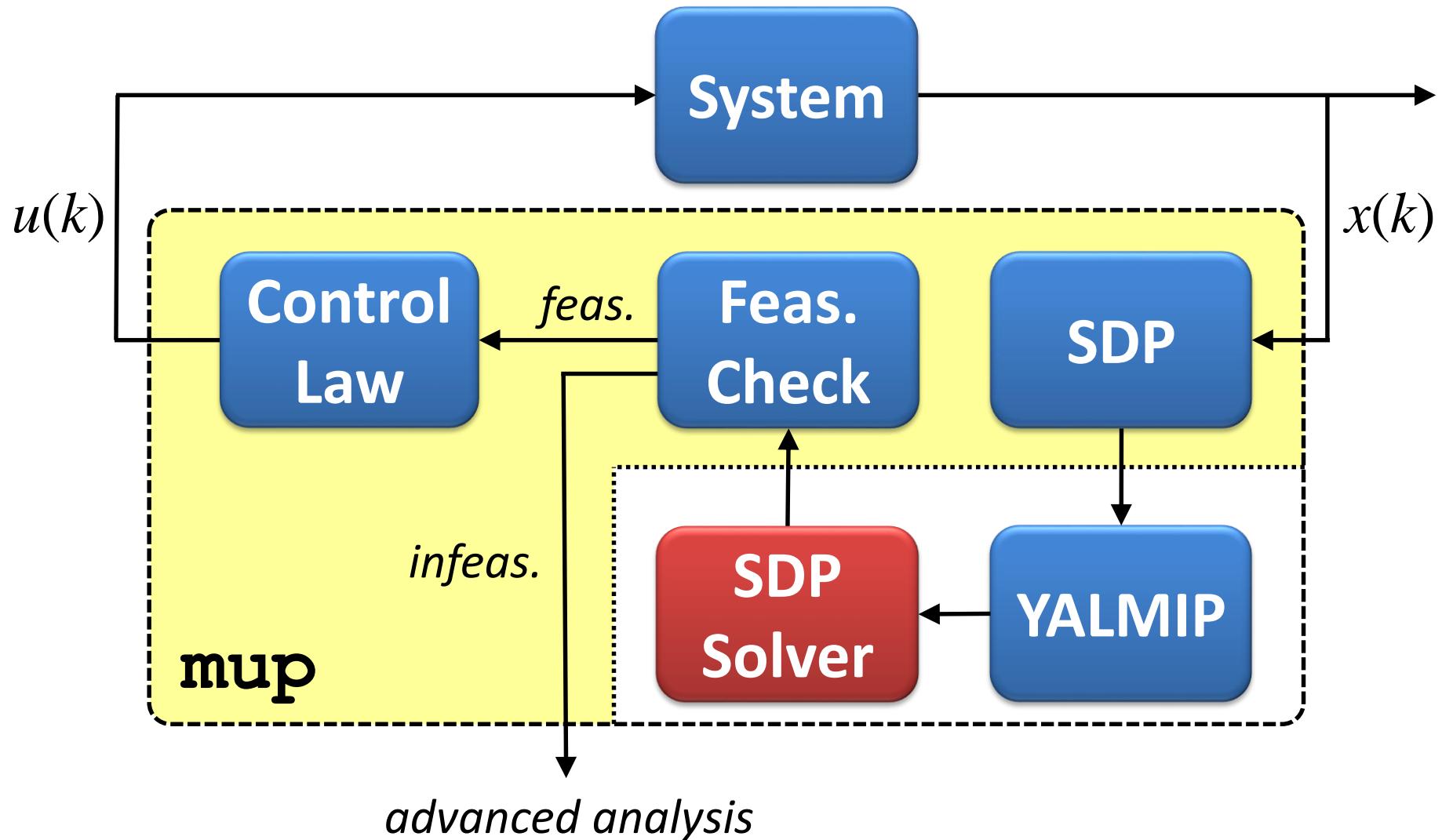
Framework



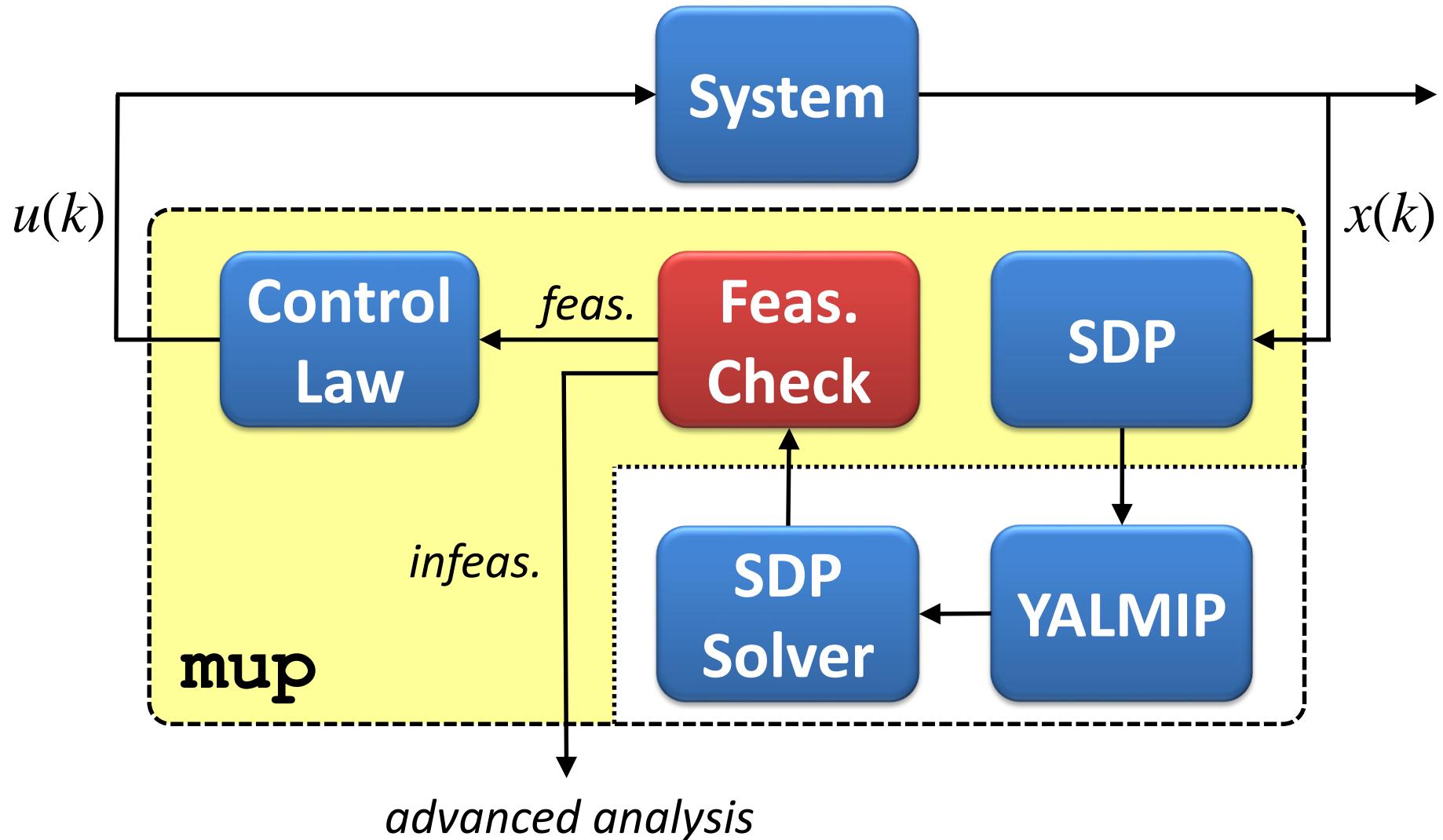
Framework



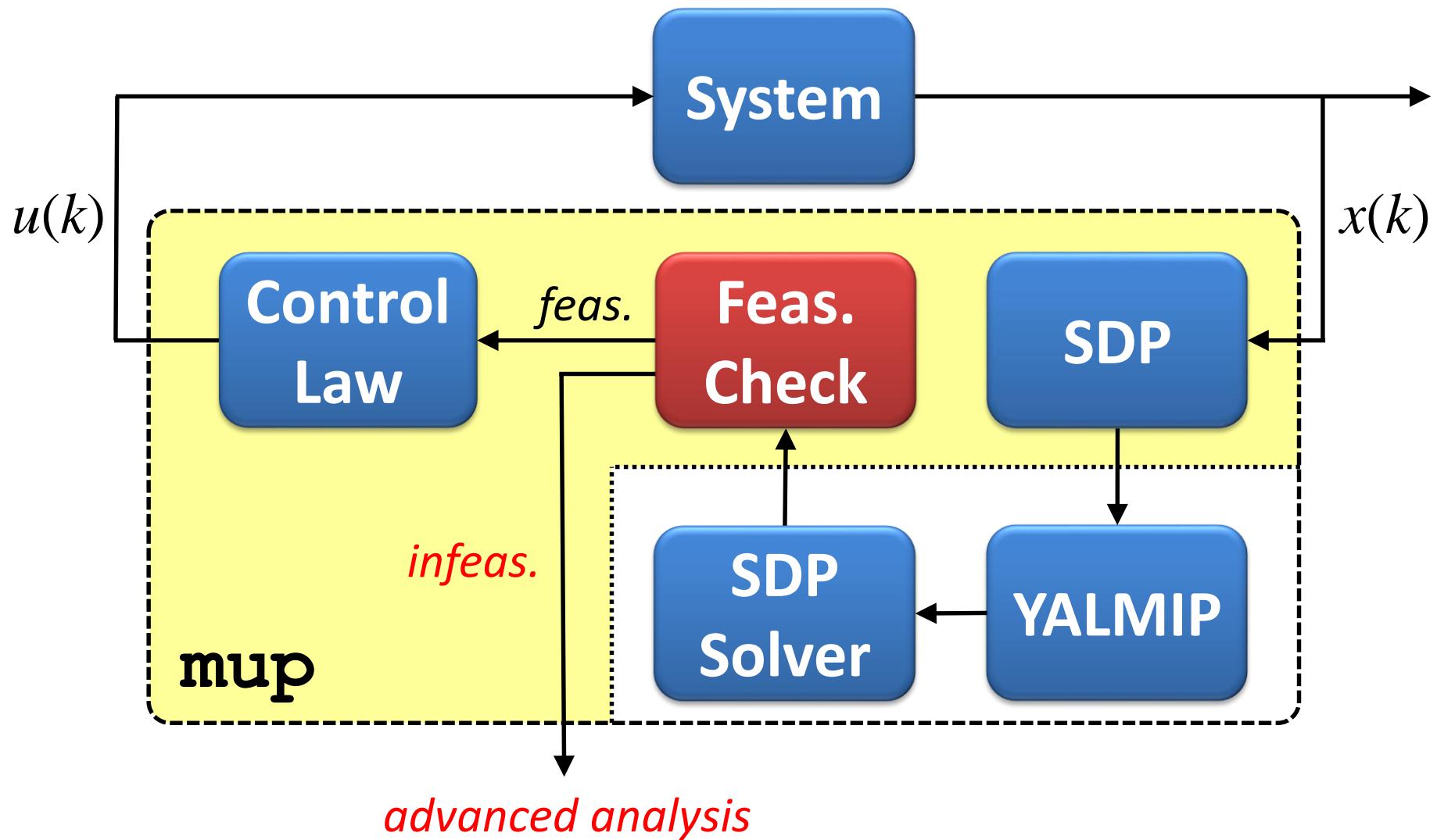
Framework



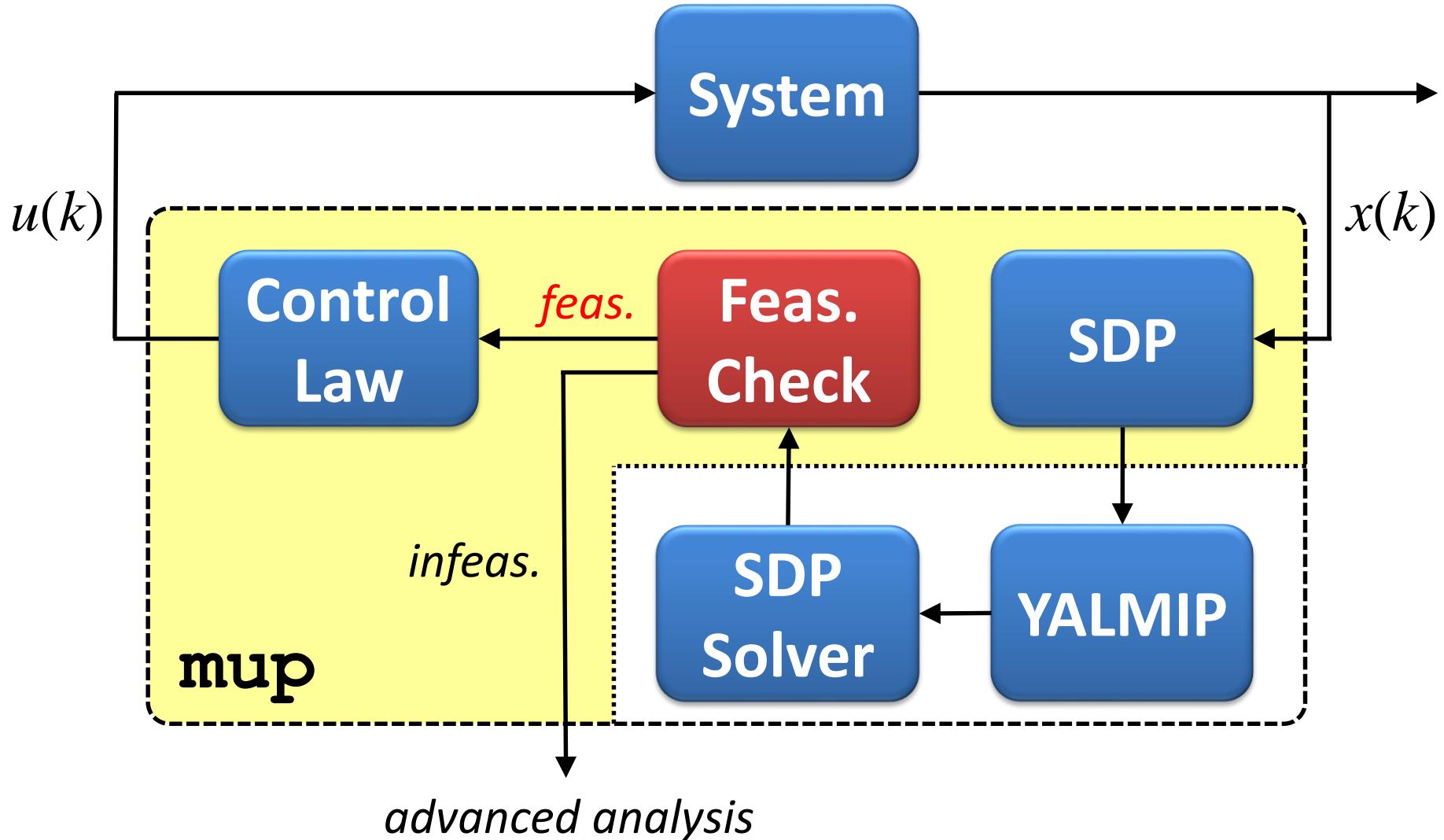
Framework



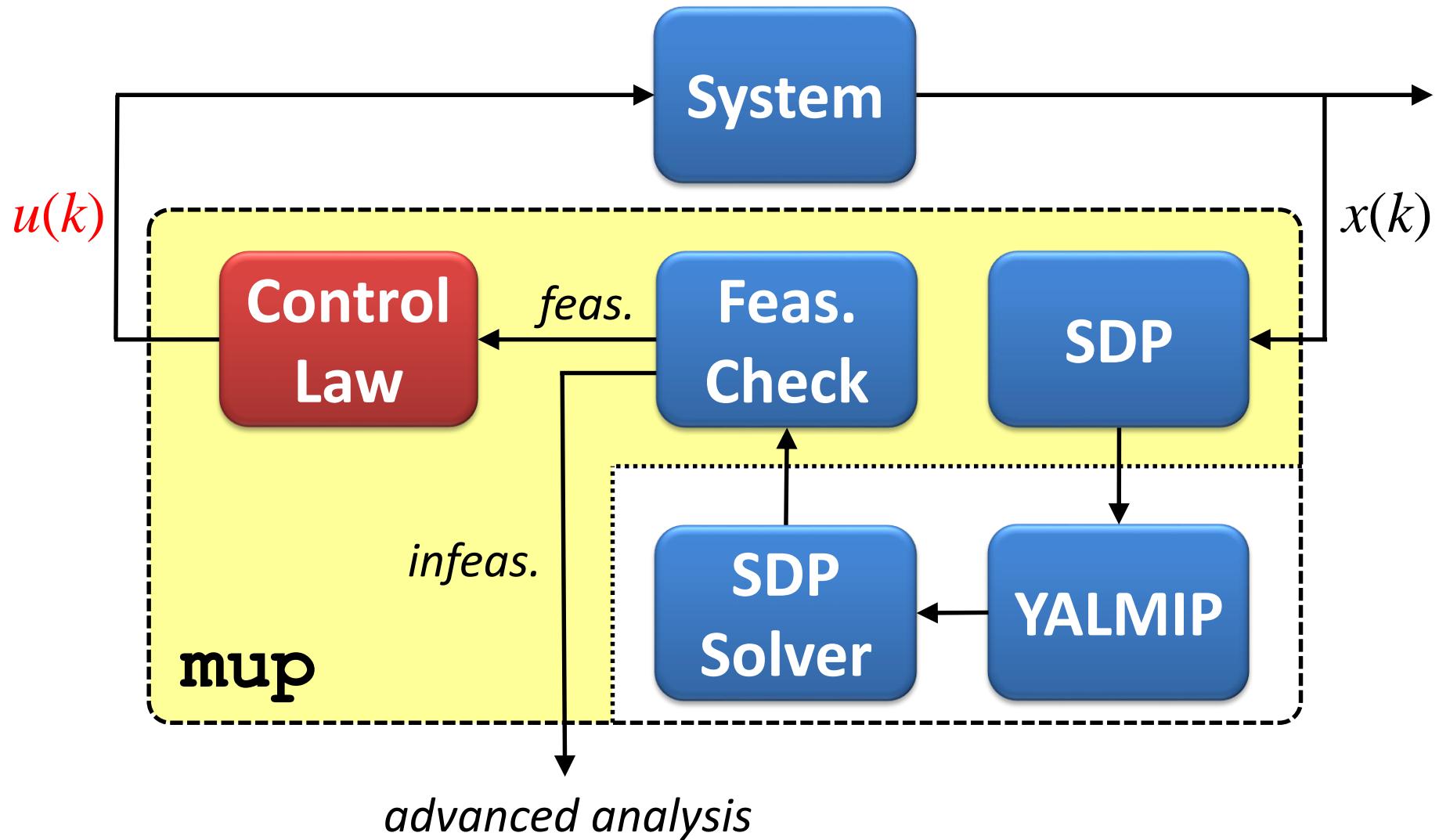
Framework



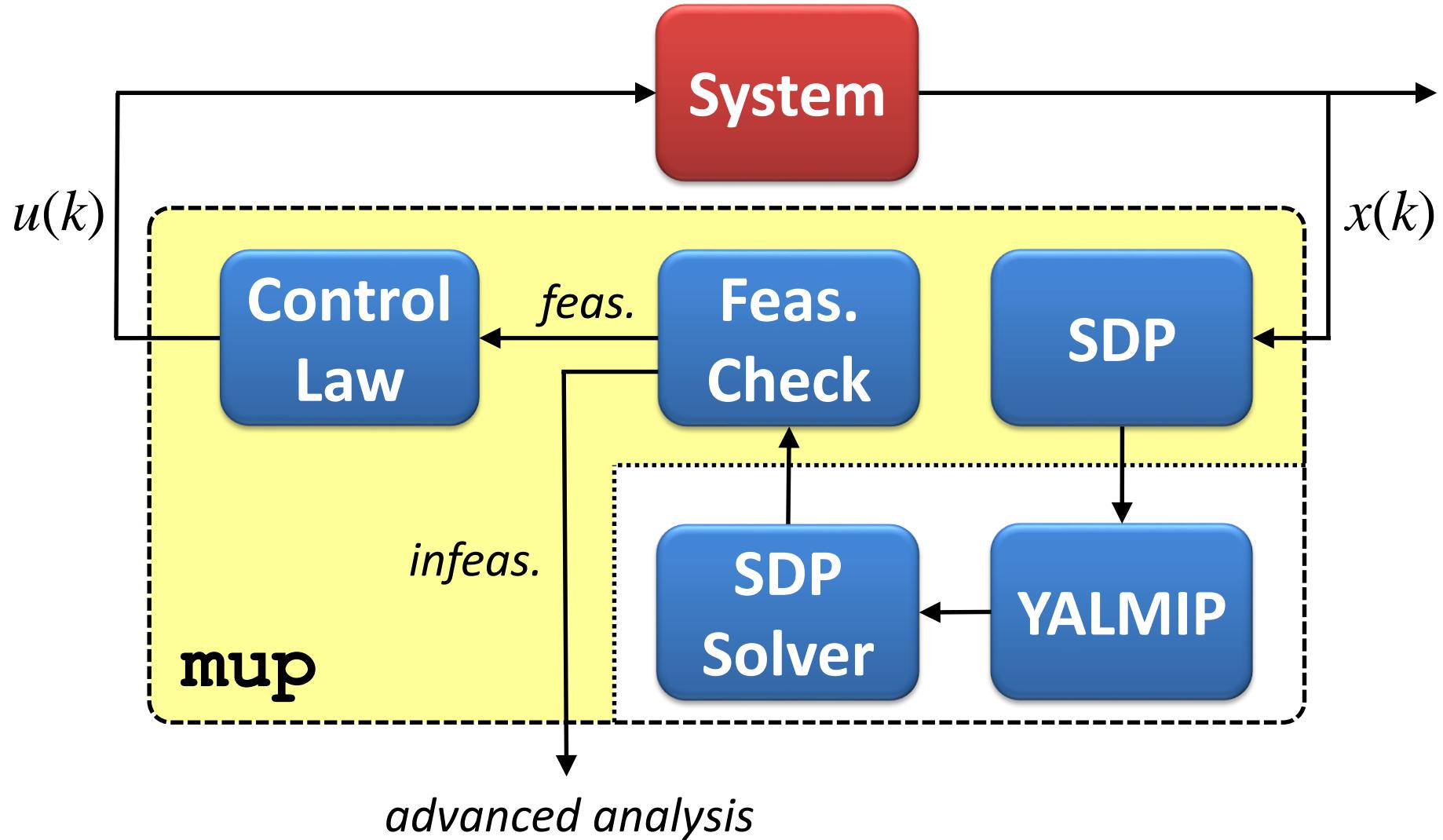
Framework



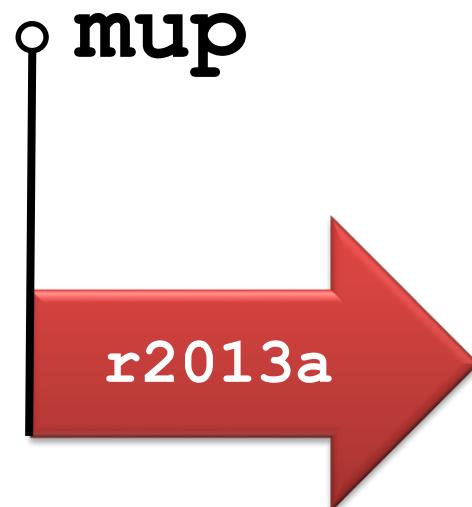
Framework



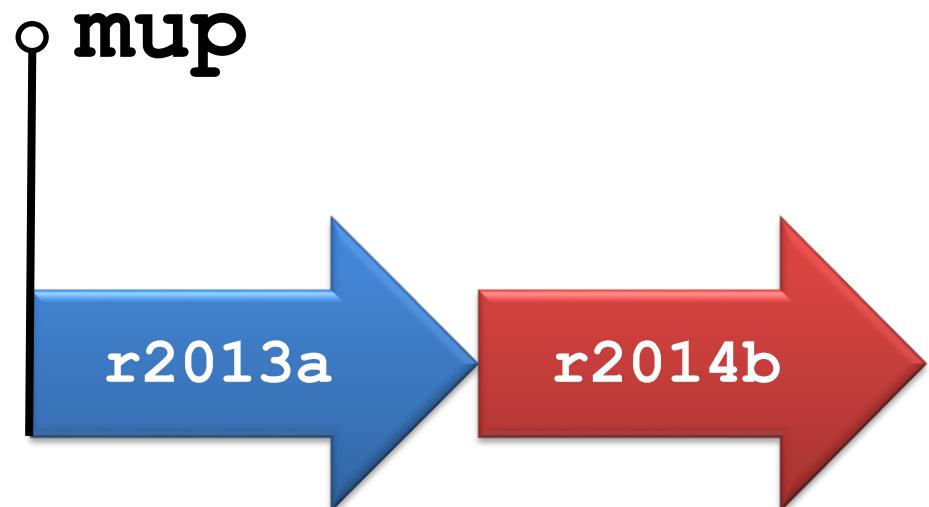
Framework



MATLAB Compatibility

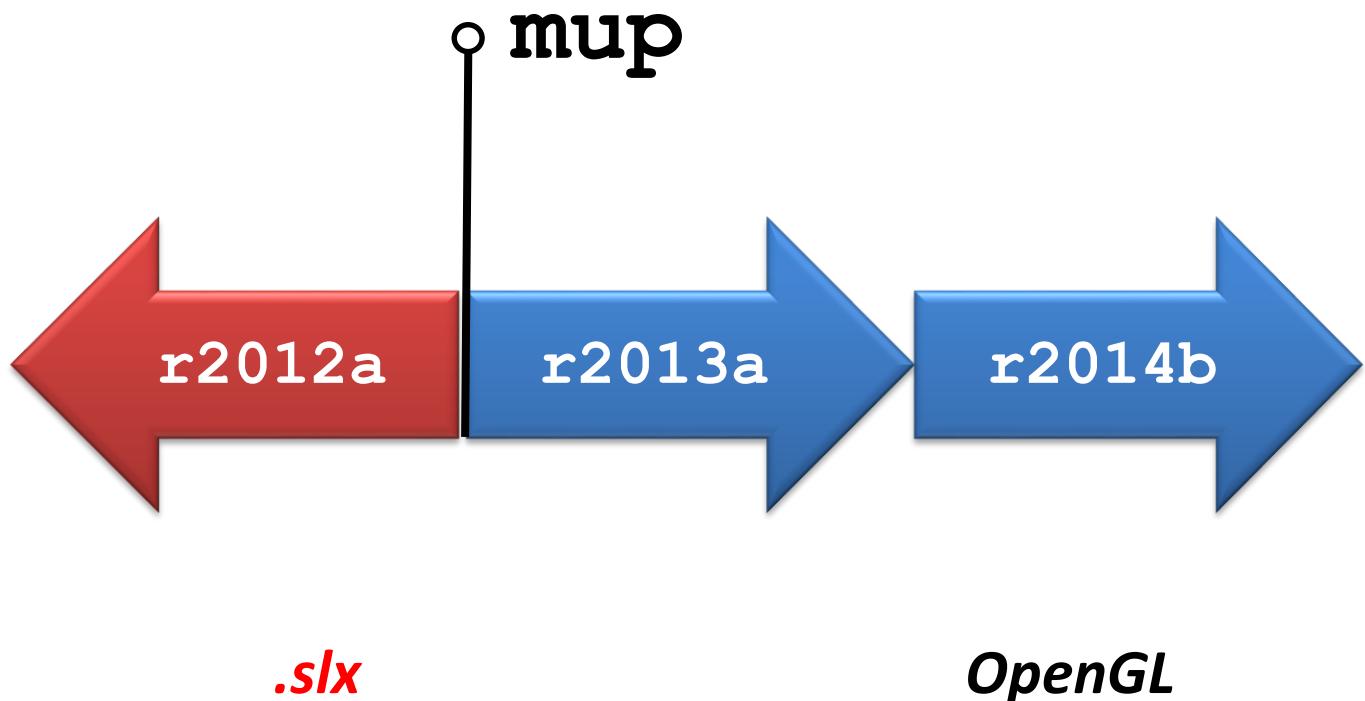


MATLAB Compatibility

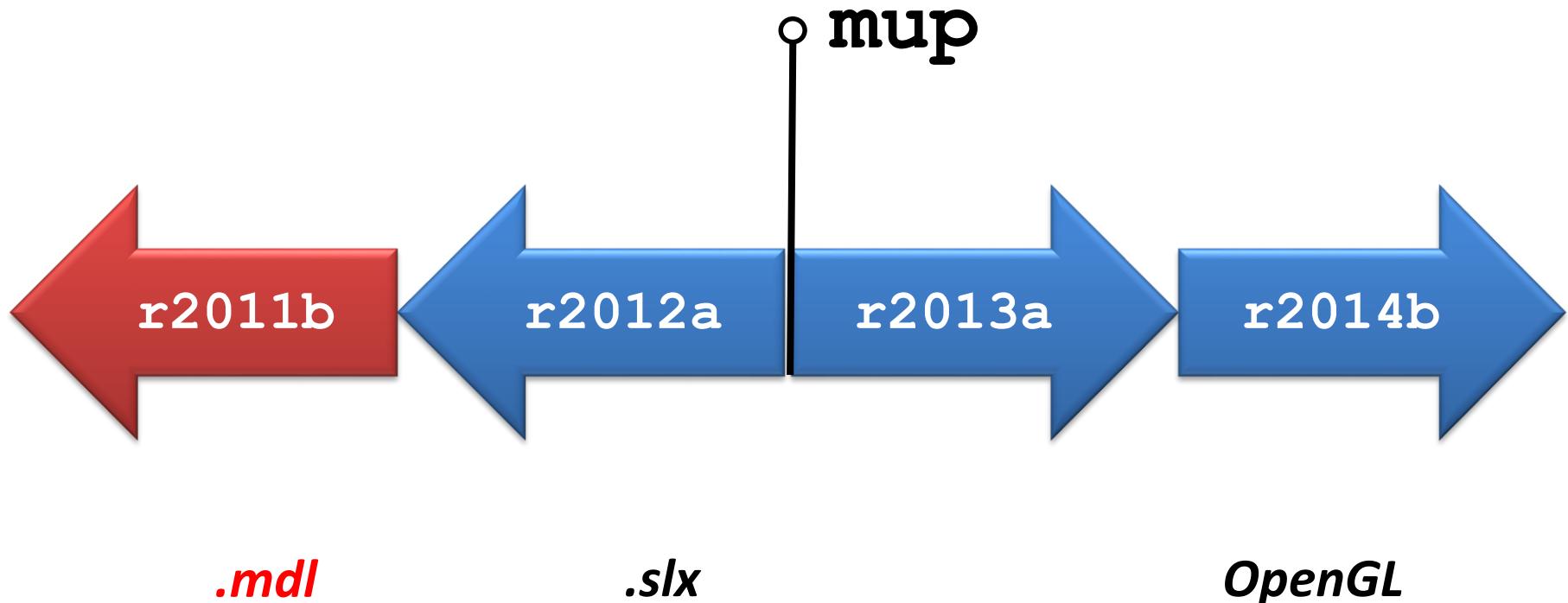


OpenGL

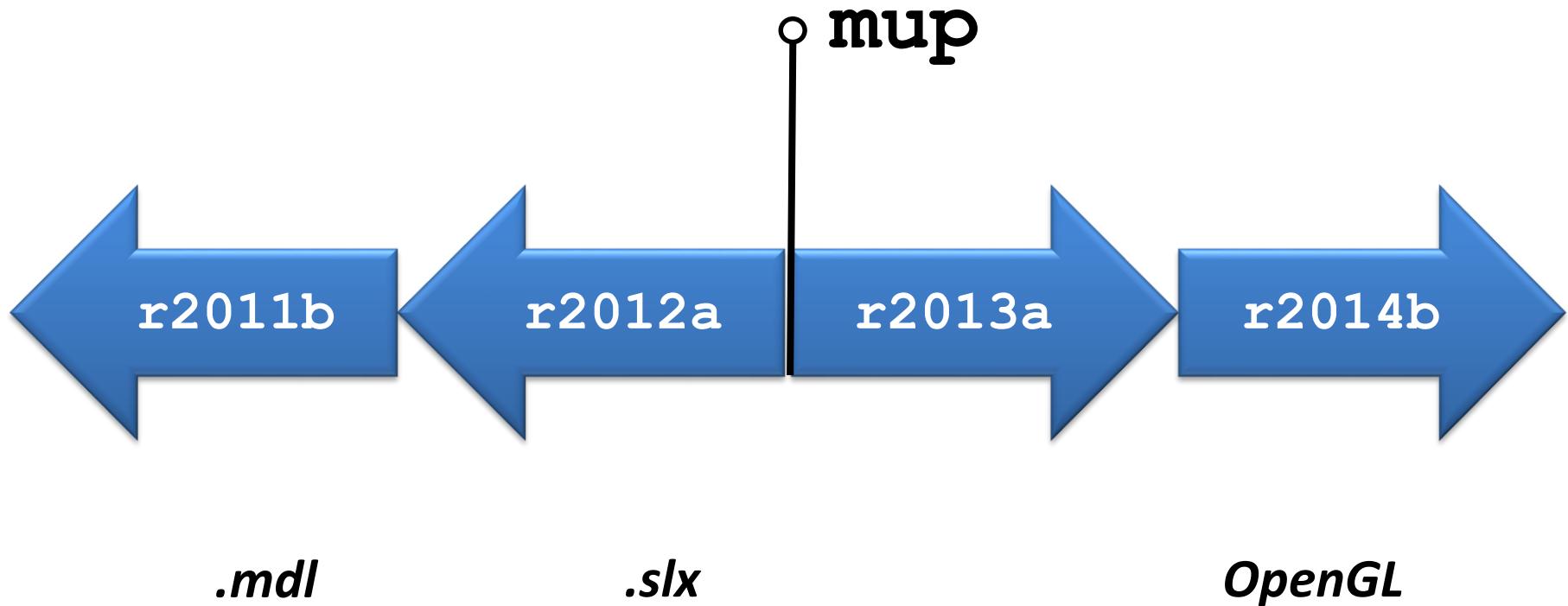
MATLAB Compatibility



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MATLAB Compatibility



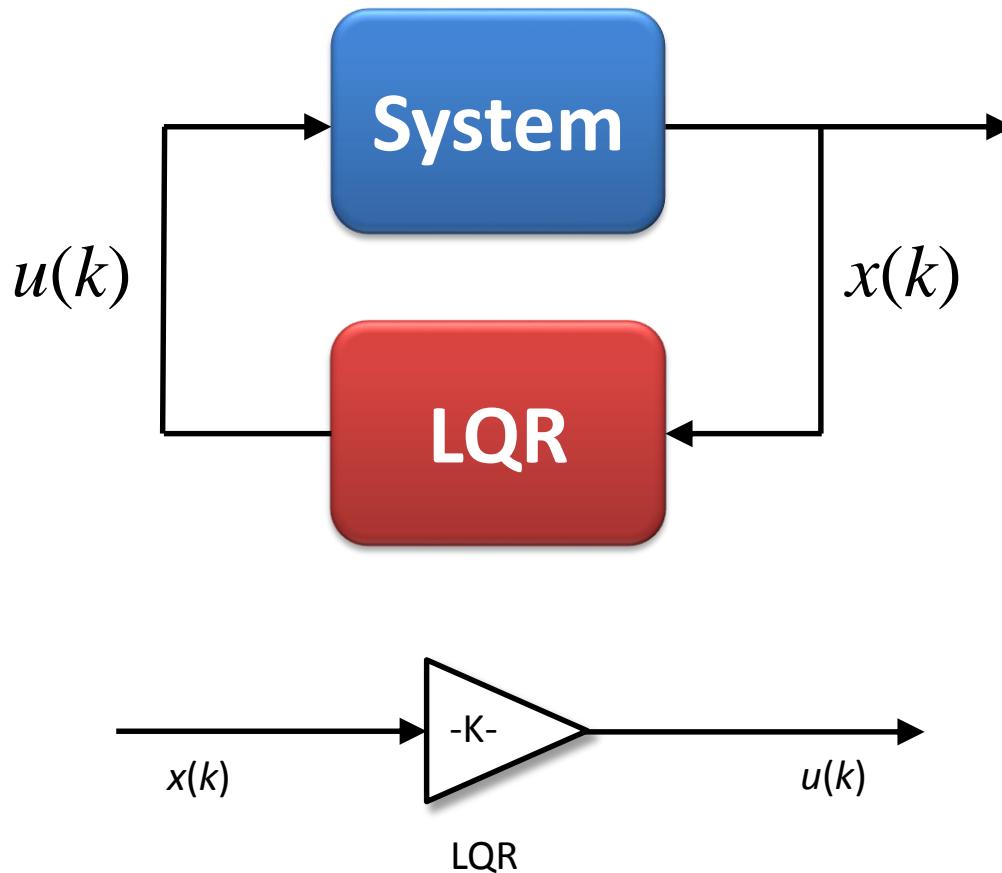
How to use it?

- MATLAB/Simulink: `rmpc_block` (*beginner*)
- MATLAB: CLI (*advanced*)

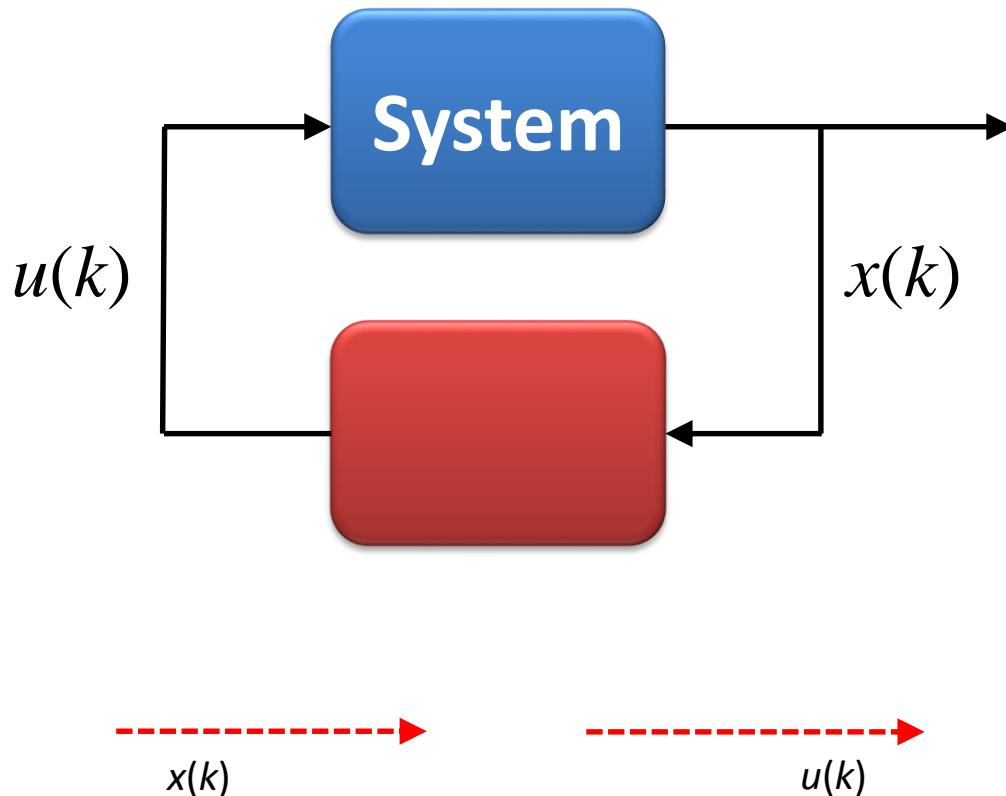
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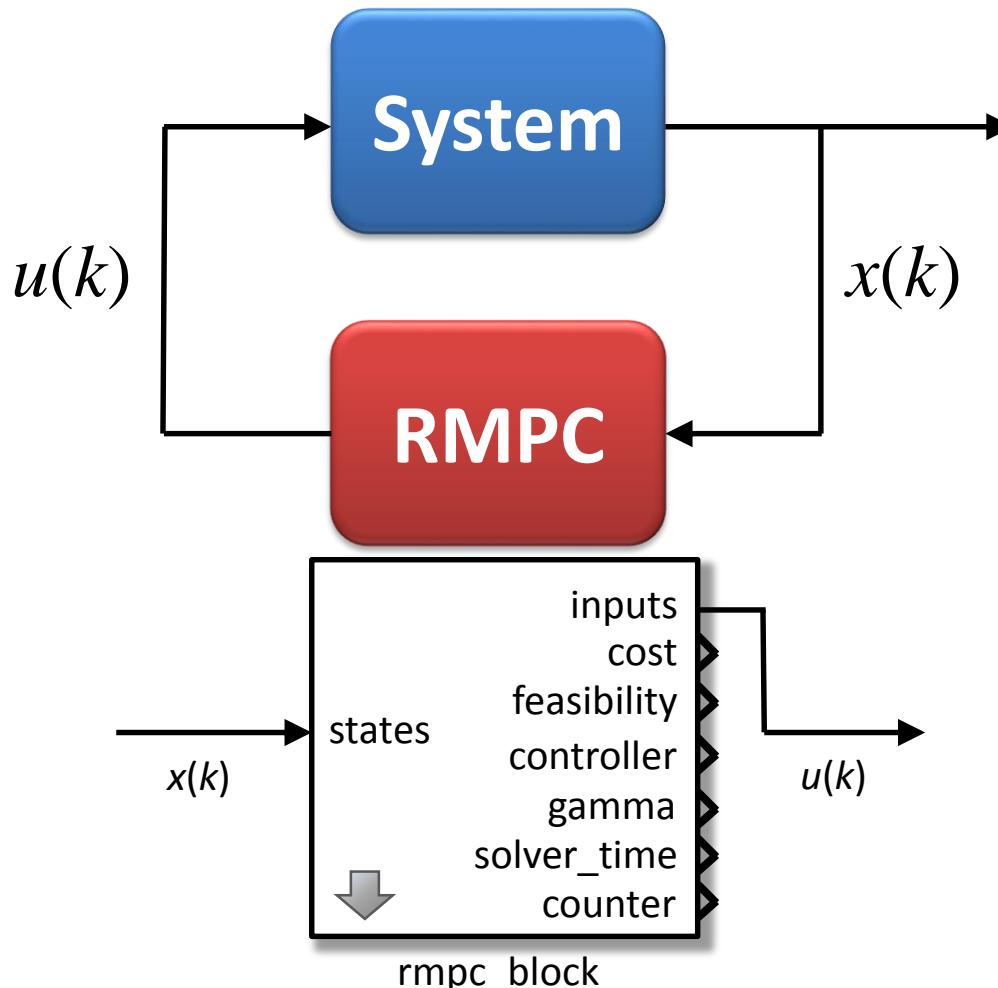
Simulink Block

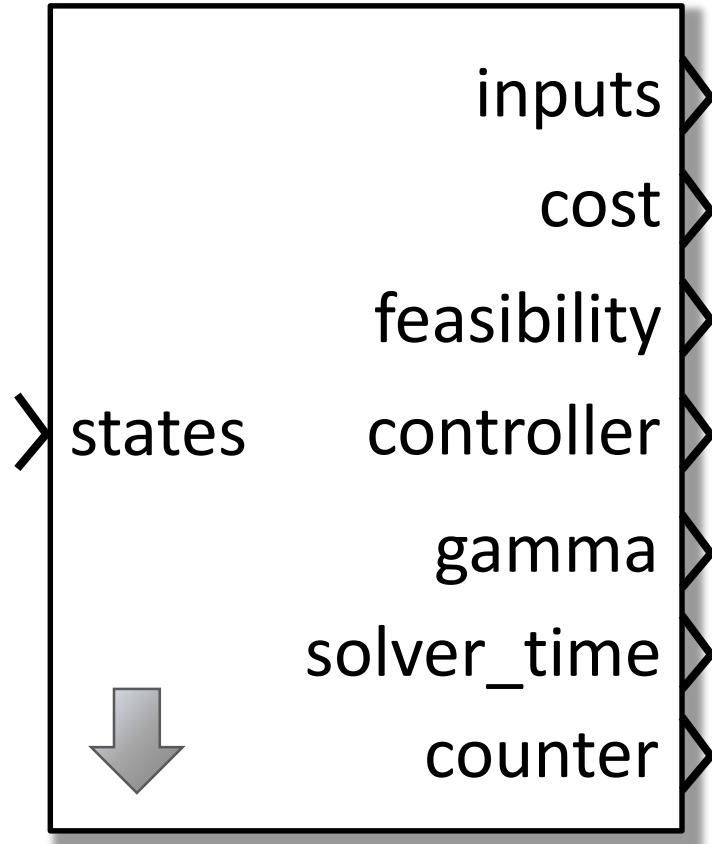


Simulink Block

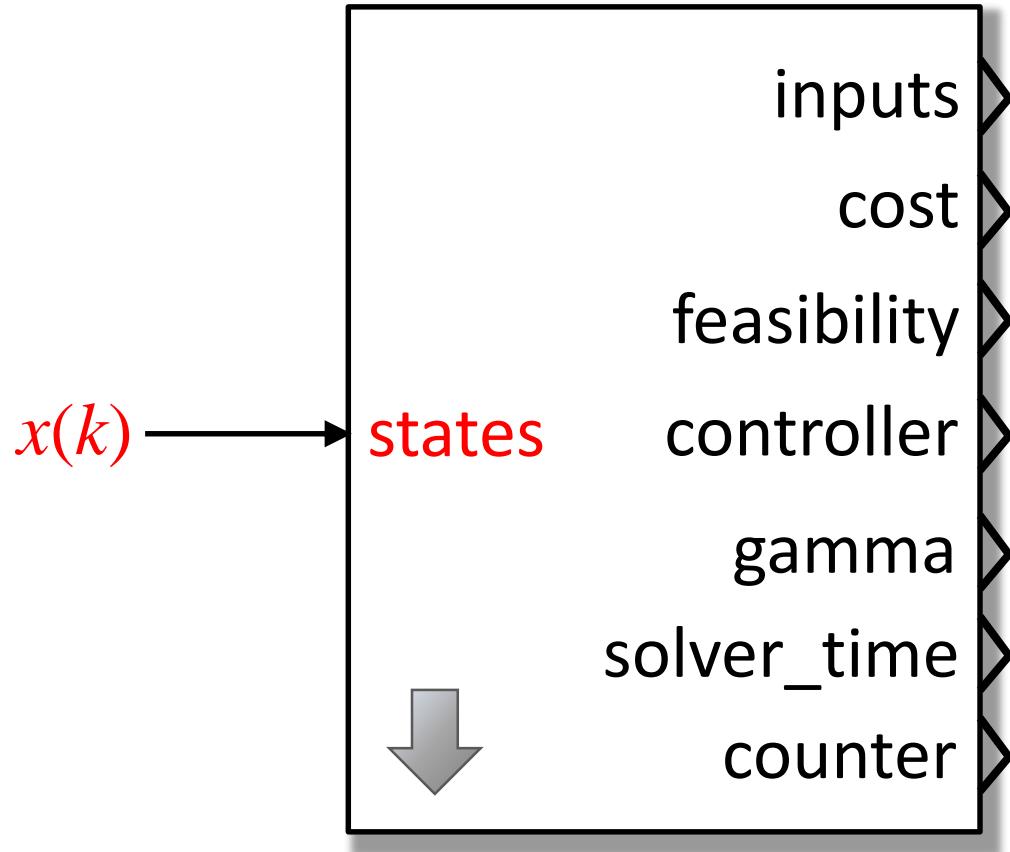


Simulink Block

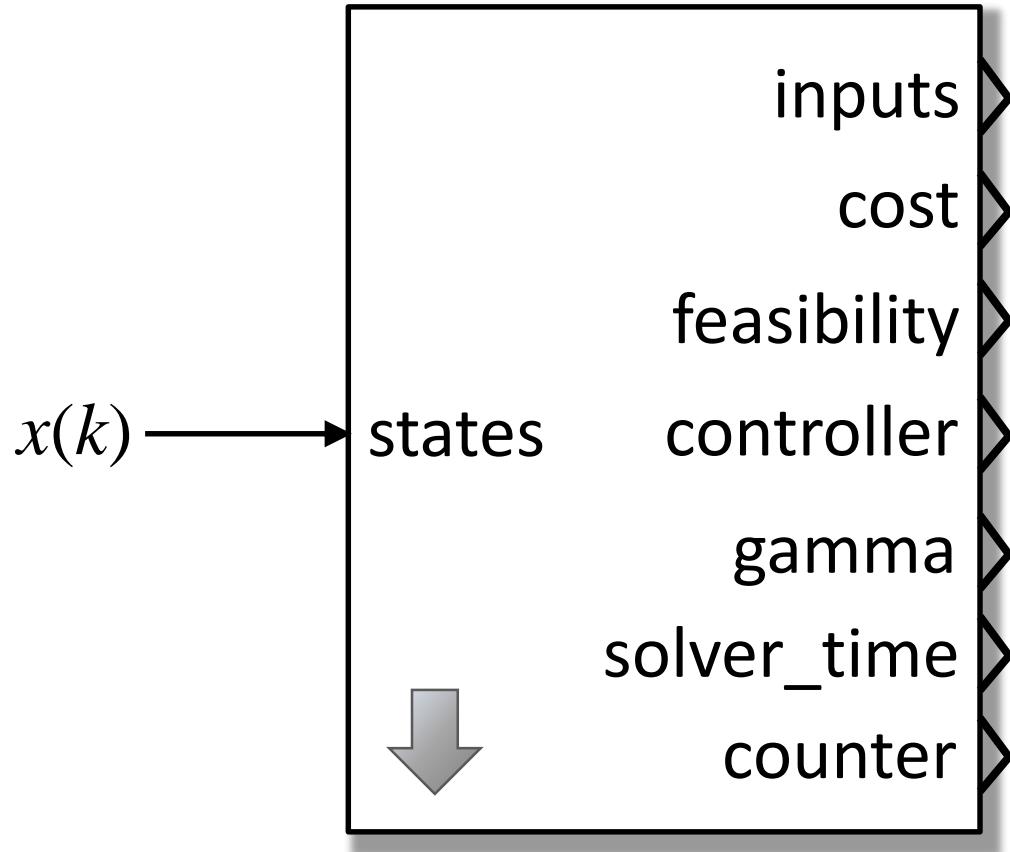




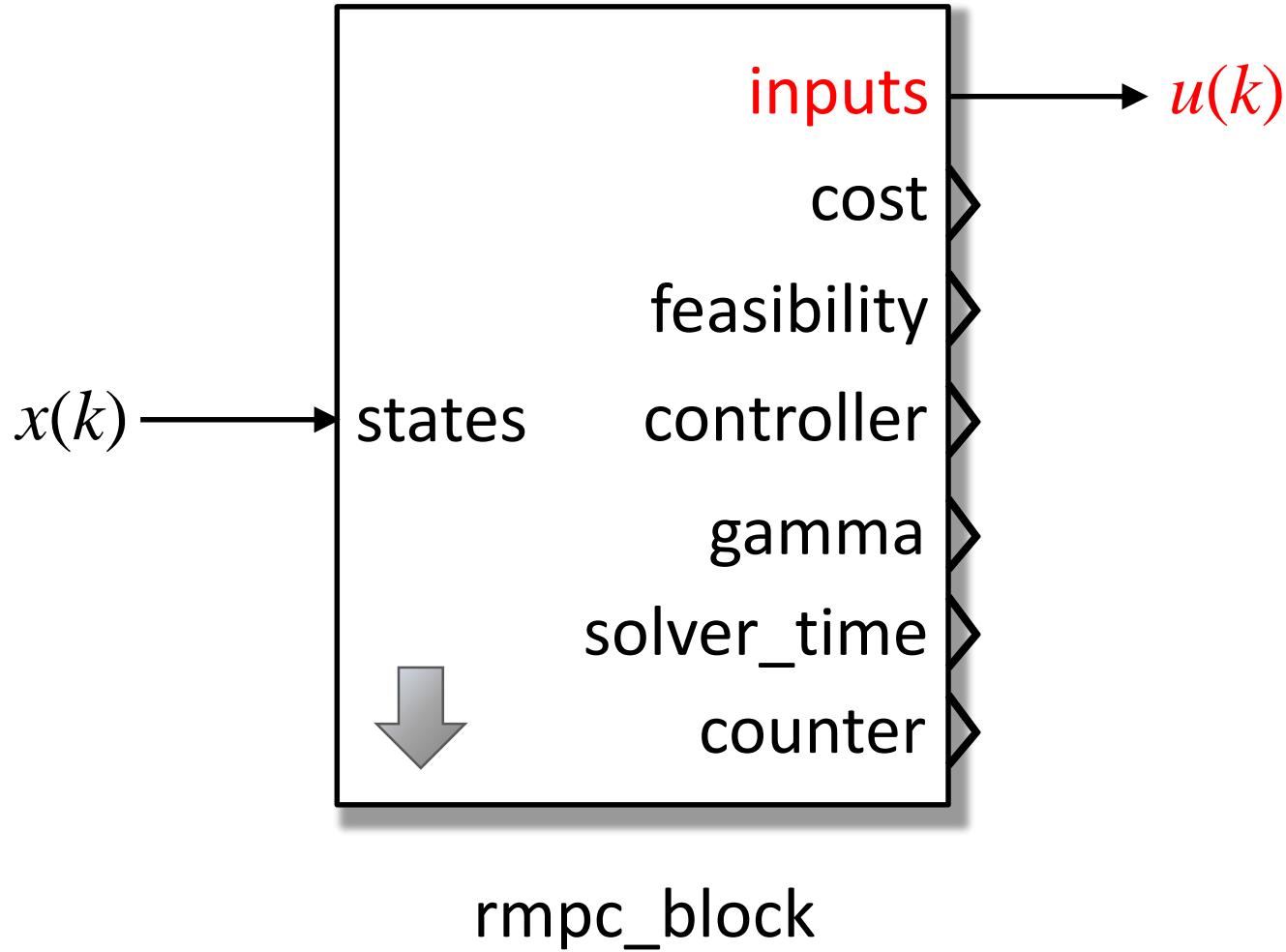
`rmpc_block`



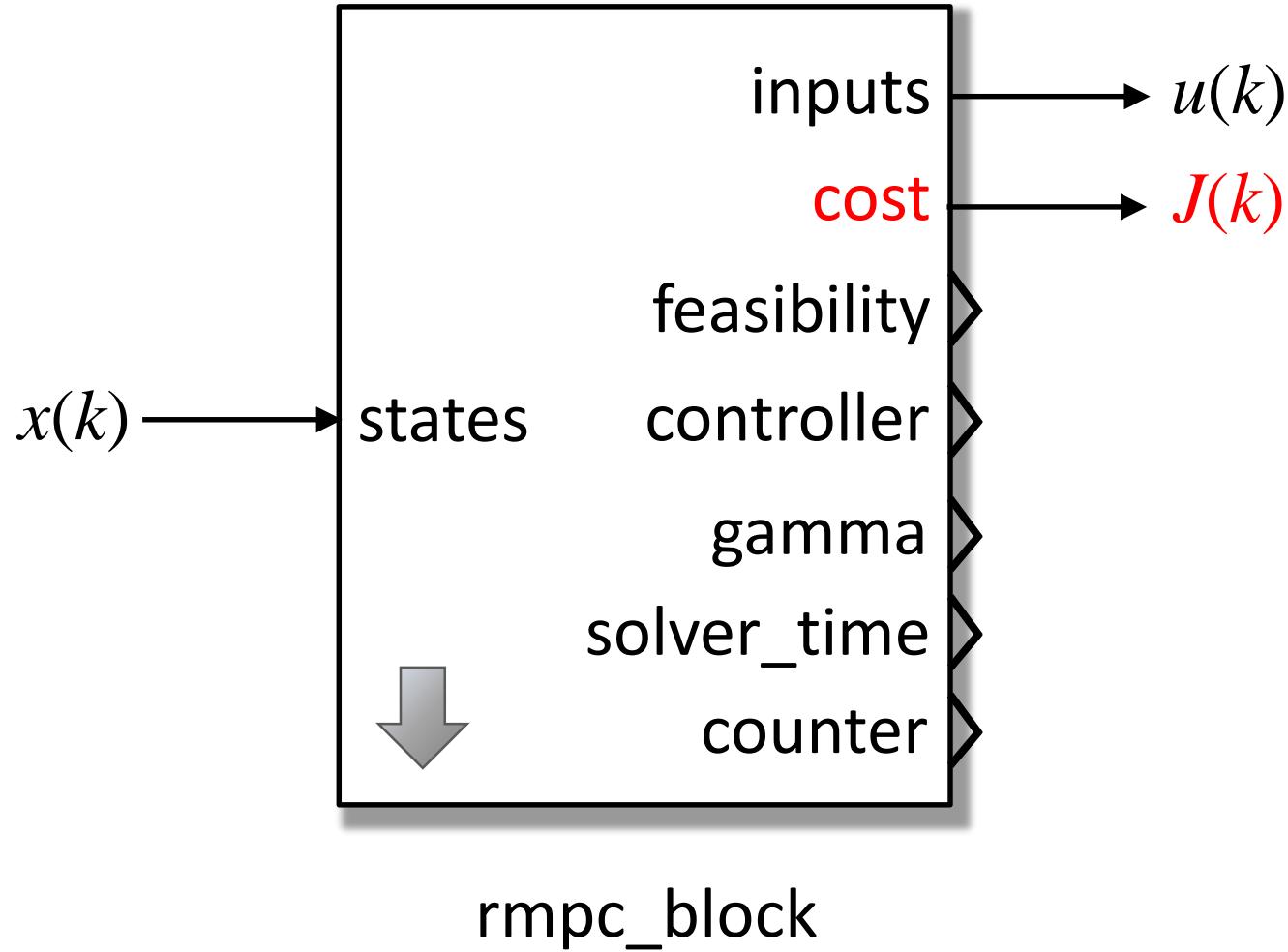
rmpc_block



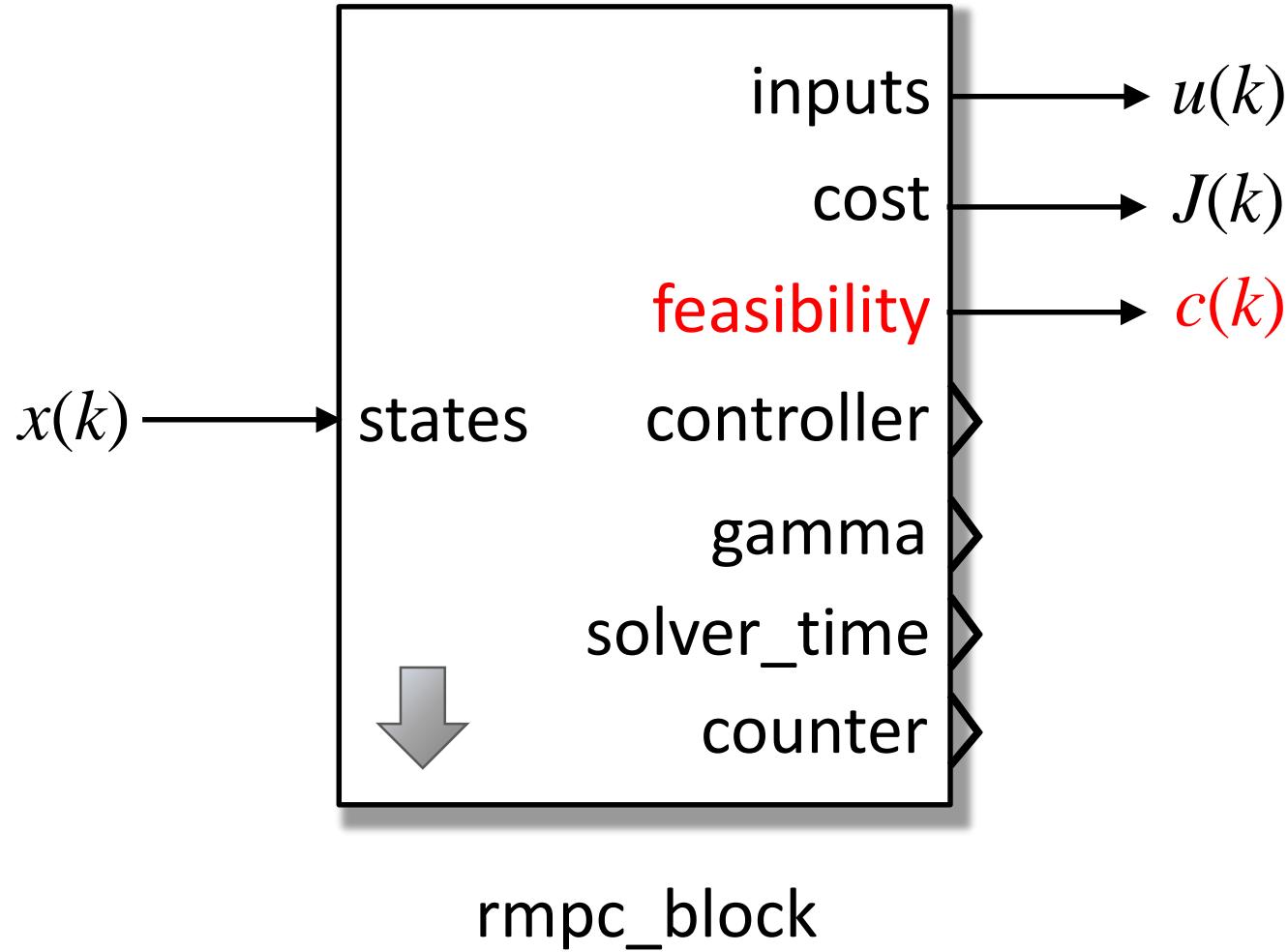
`rmpc_block`

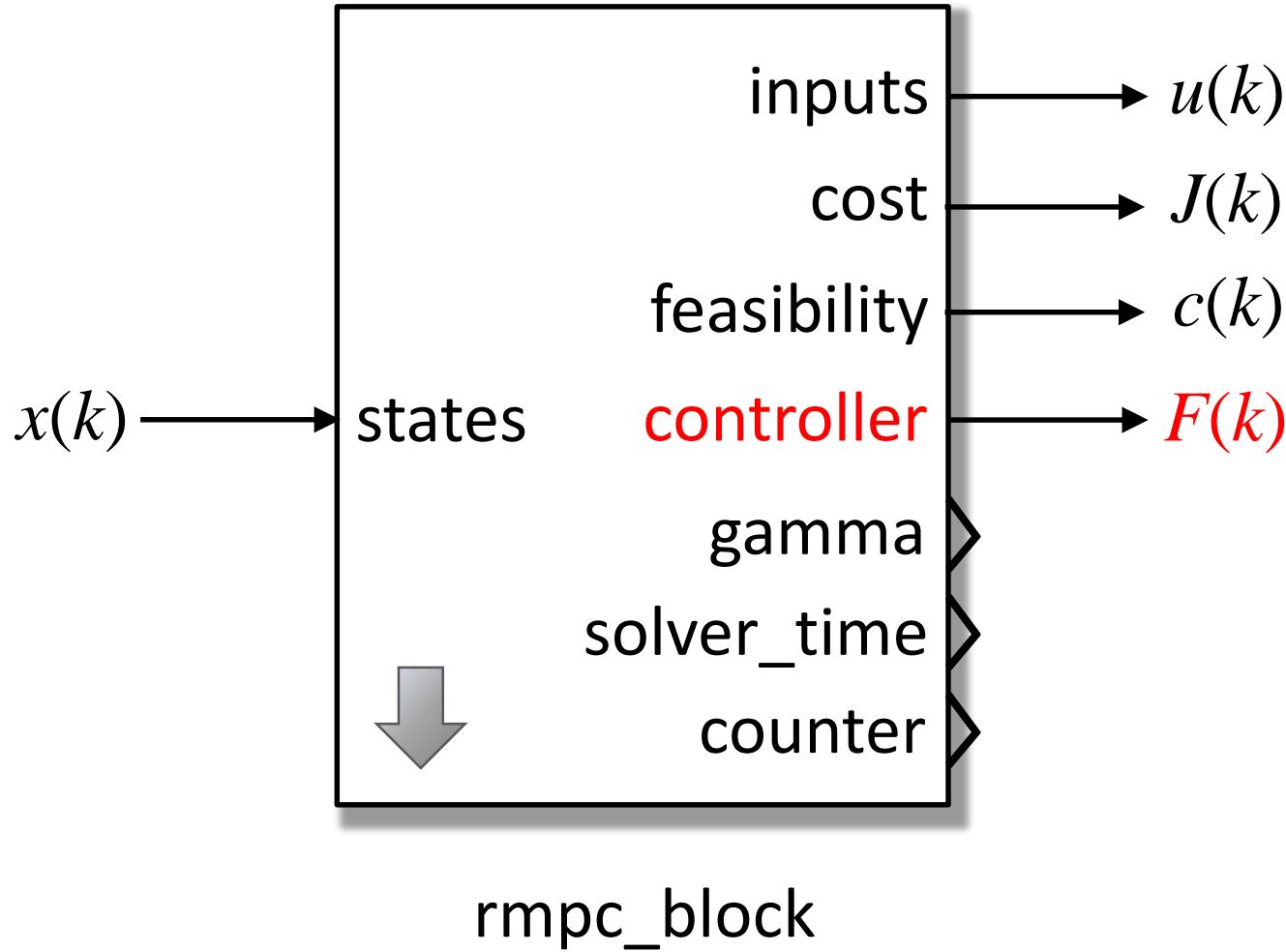


Control law: $u(k) = F(x(k)) x(k)$

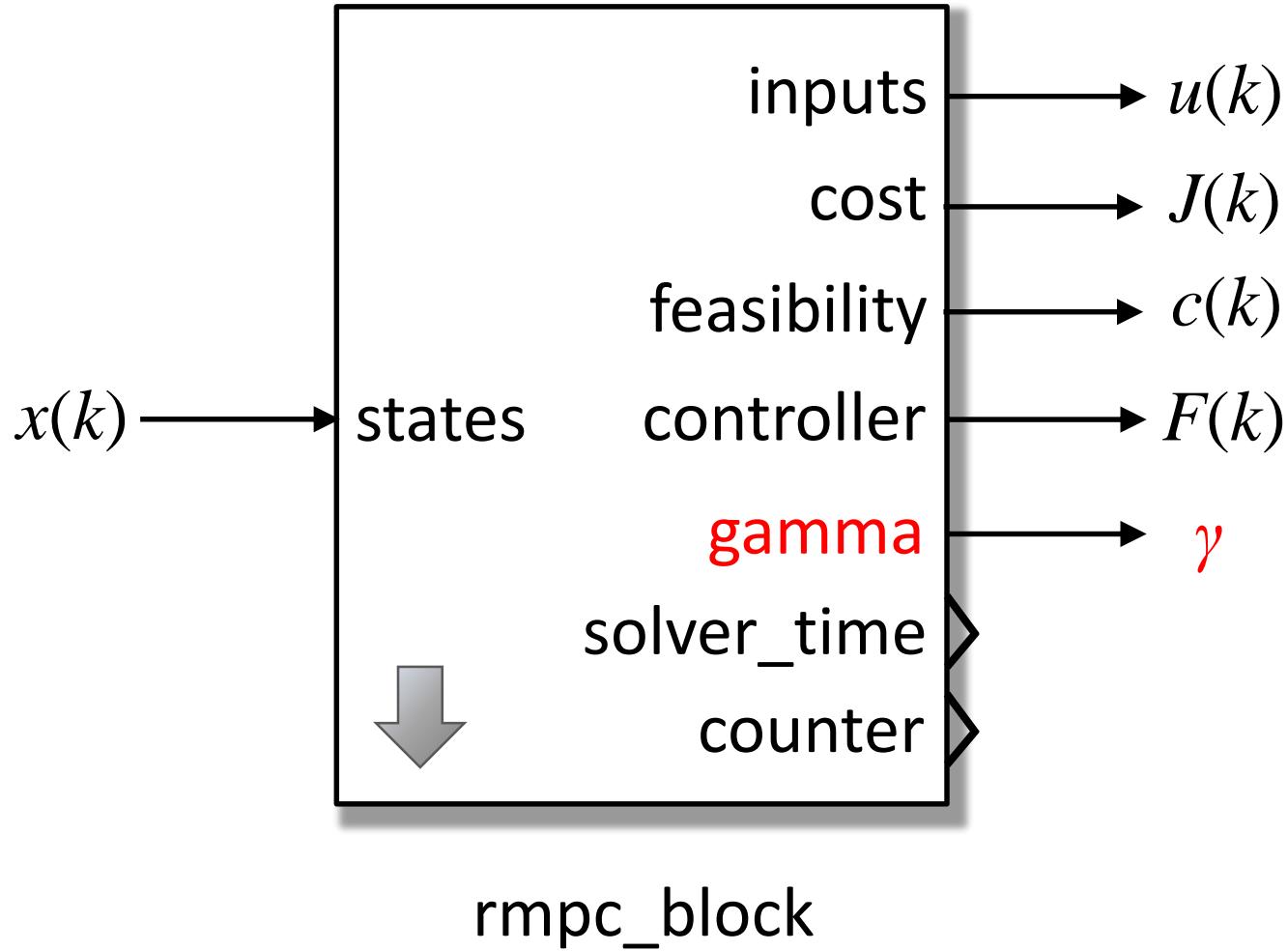


$$J(k) = \sum_{i=0}^{\infty} (x(k+i)^T \mathbf{W_x} x(k+i) + u(k+i)^T \mathbf{W_u} u(k+i))$$

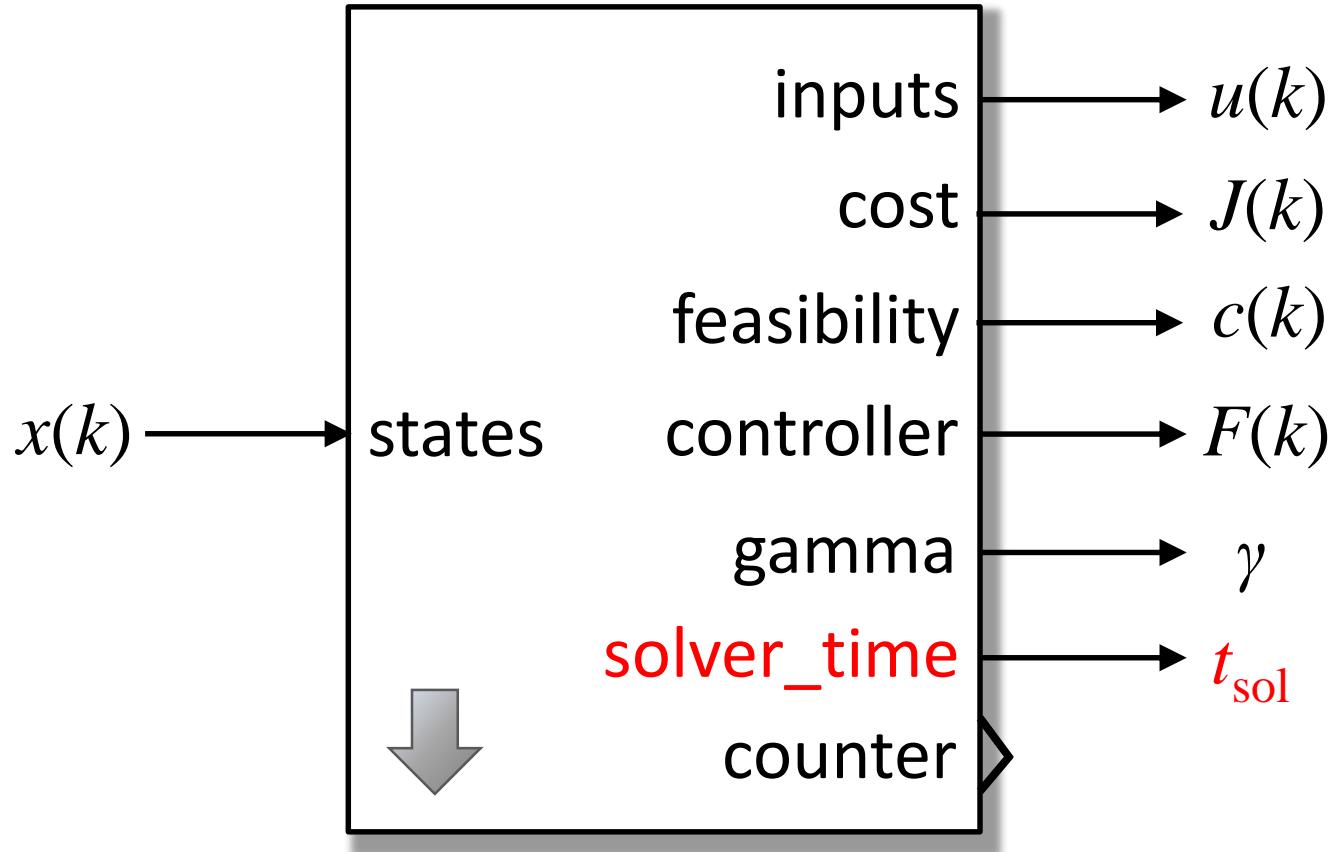




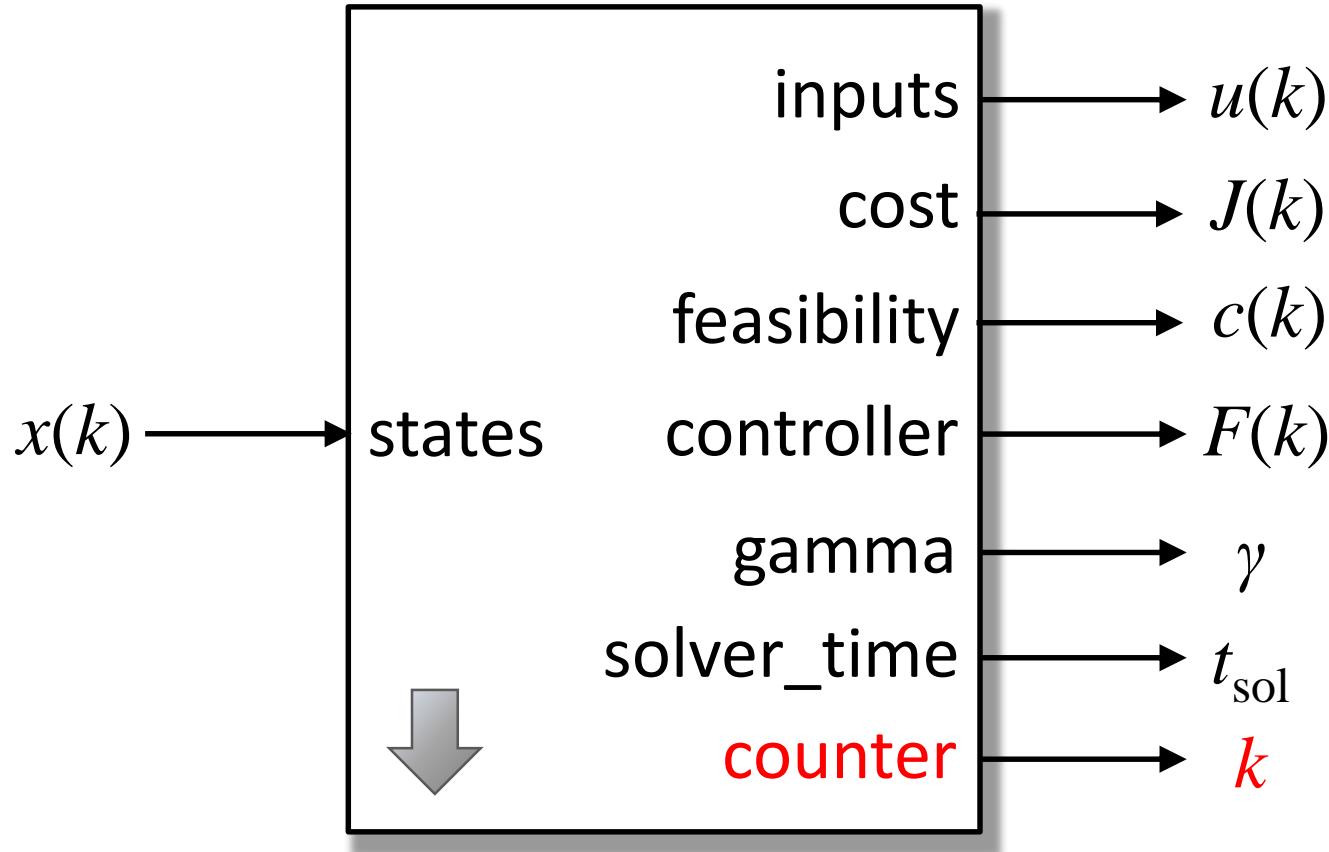
Control law: $u(k) = F(x(k)) x(k)$



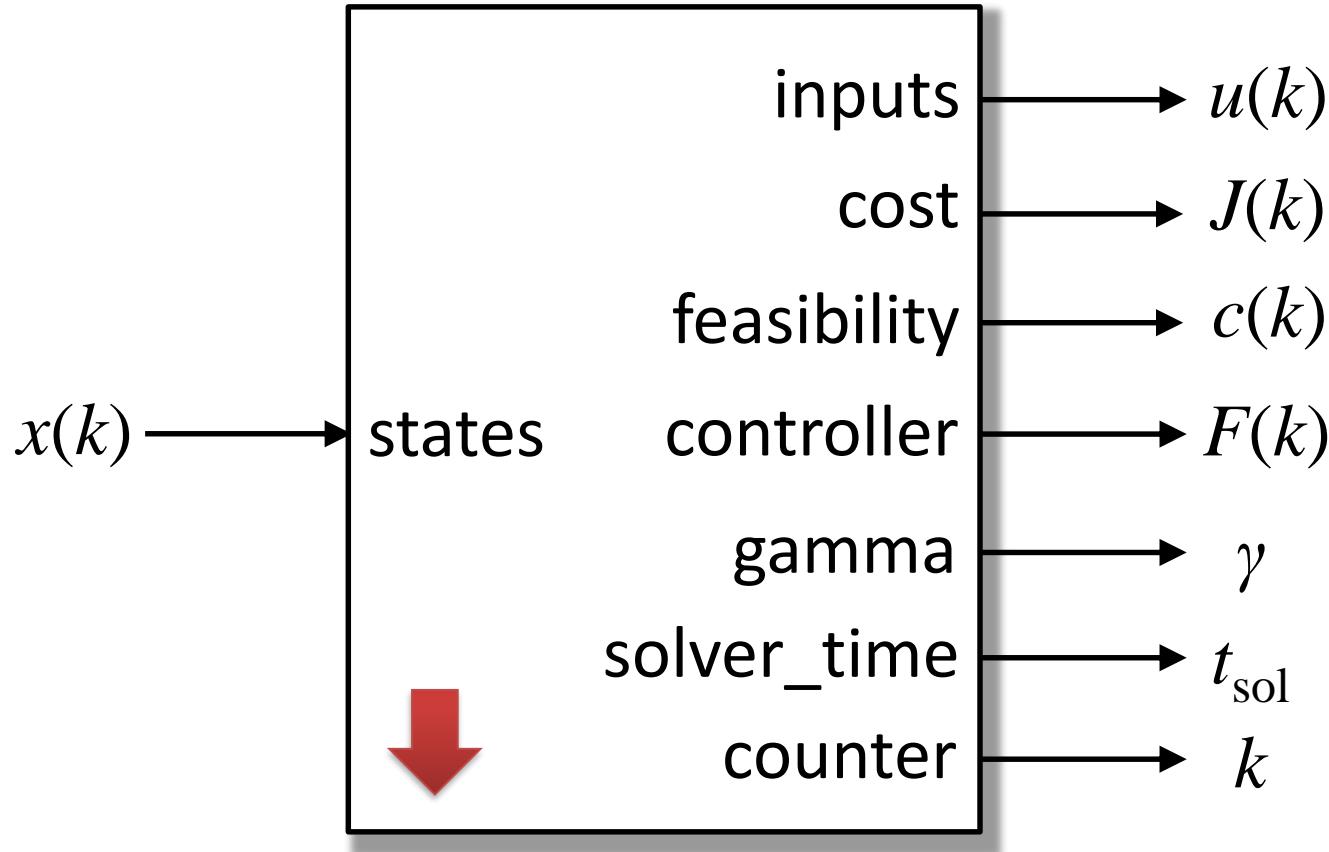
SDP: $\min \gamma, \quad \text{s.t.: } x \preceq_{\mathcal{K}} 0 \Rightarrow M(x) \prec 0$



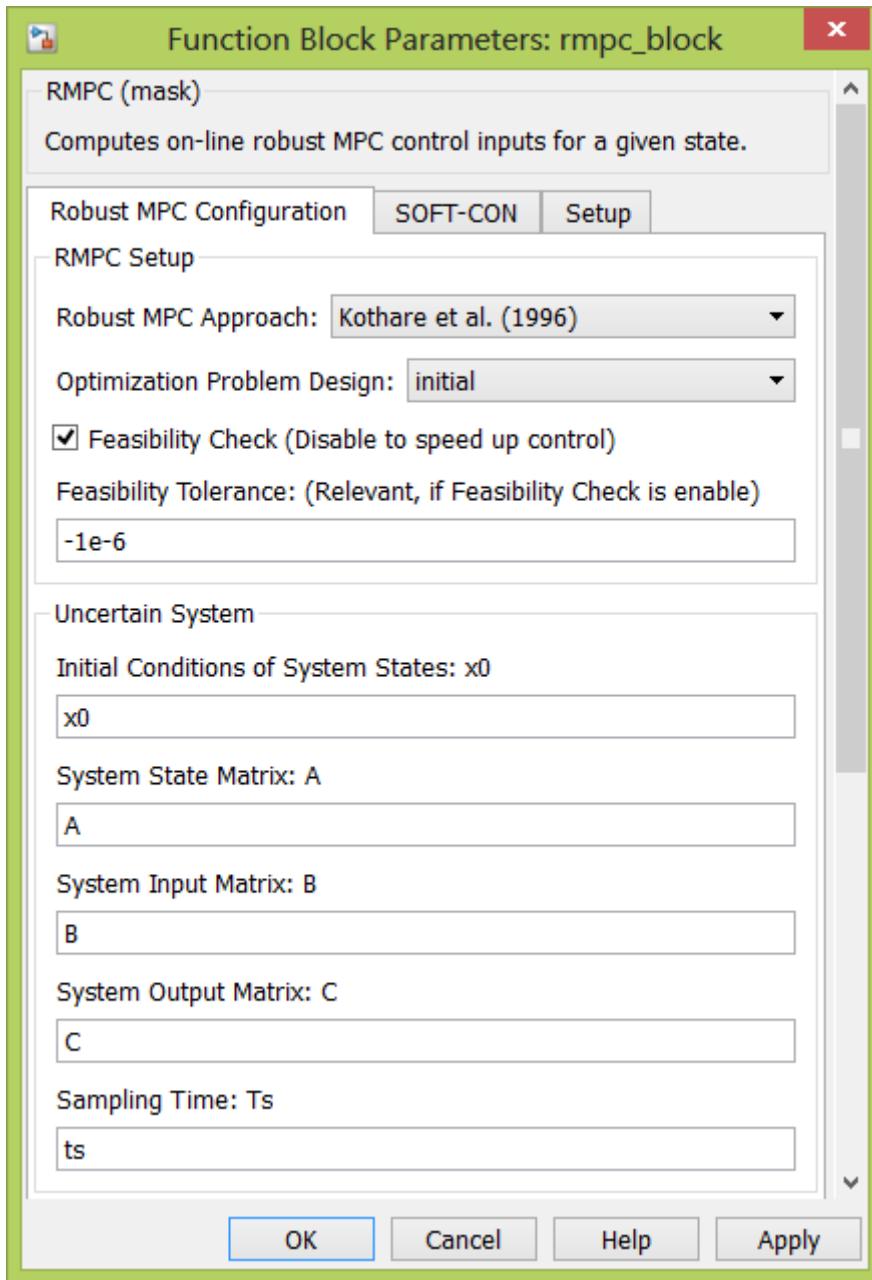
`rmpc_block`



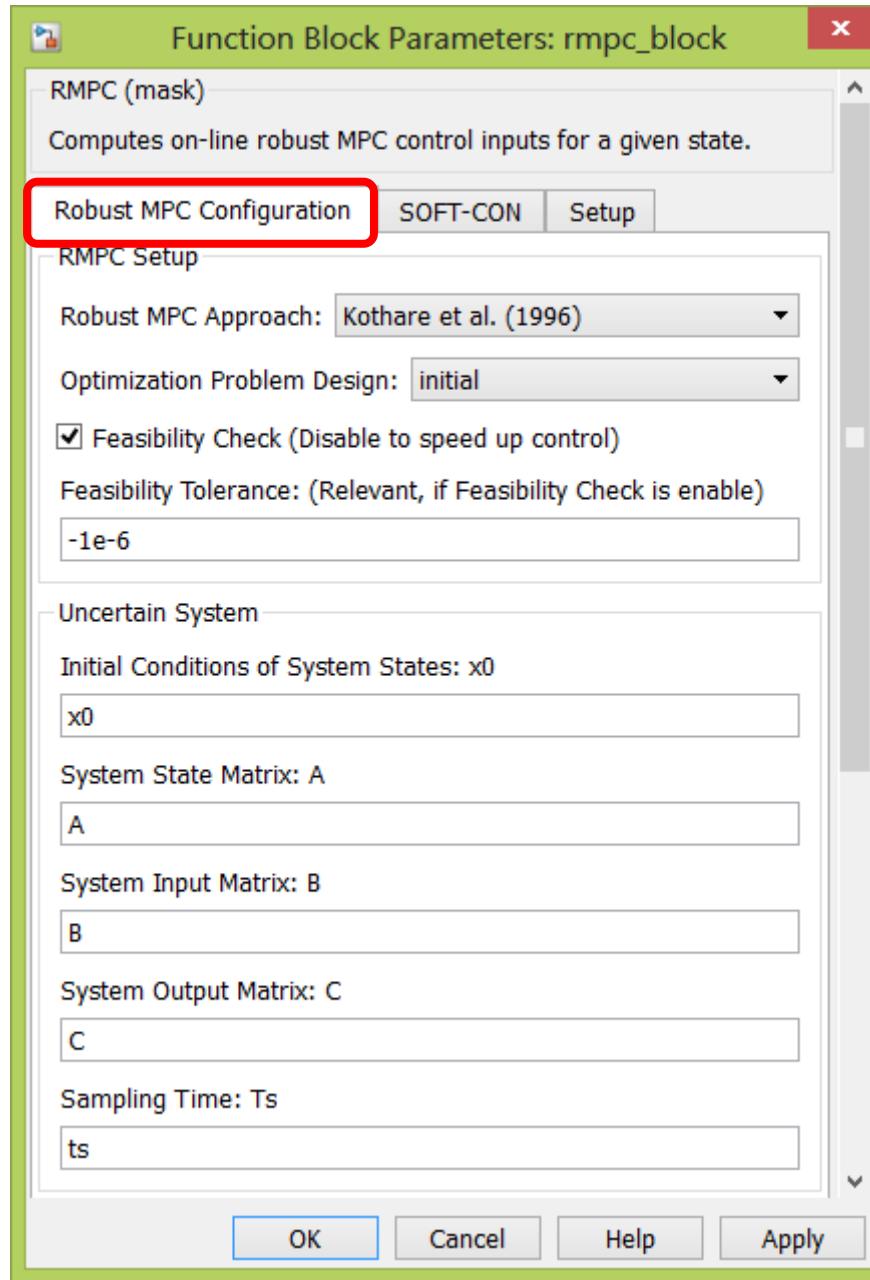
`rmpc_block`



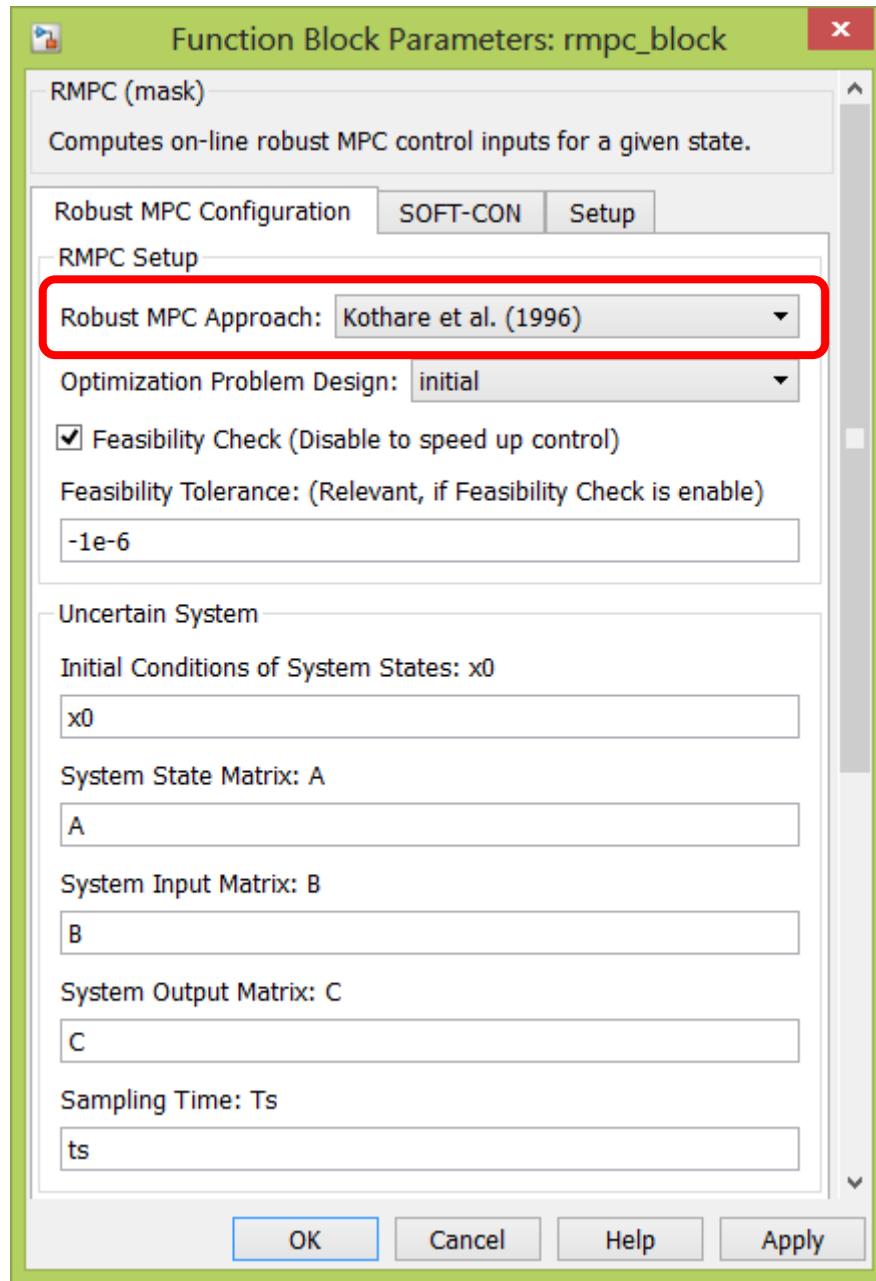
`rmpc_block`

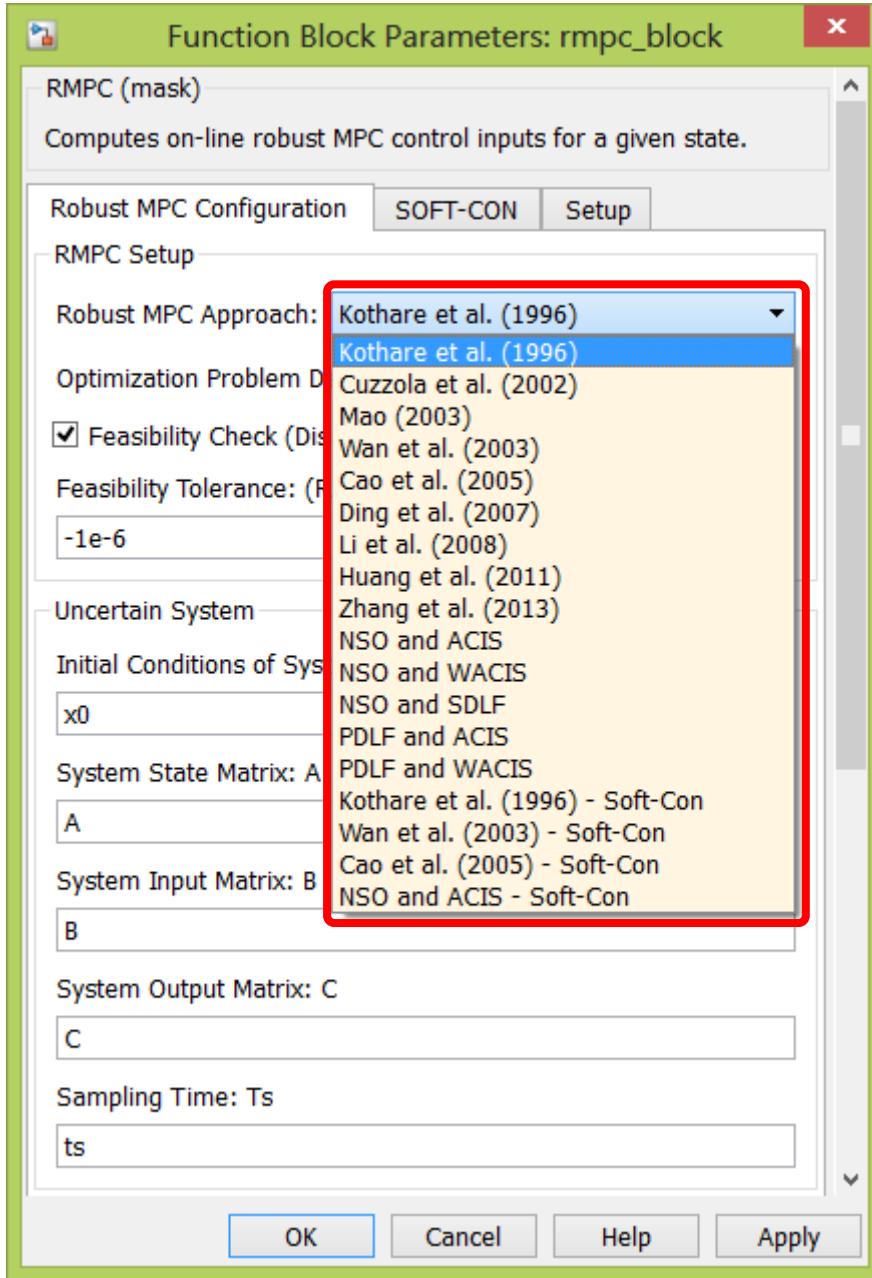


RMPC Configuration



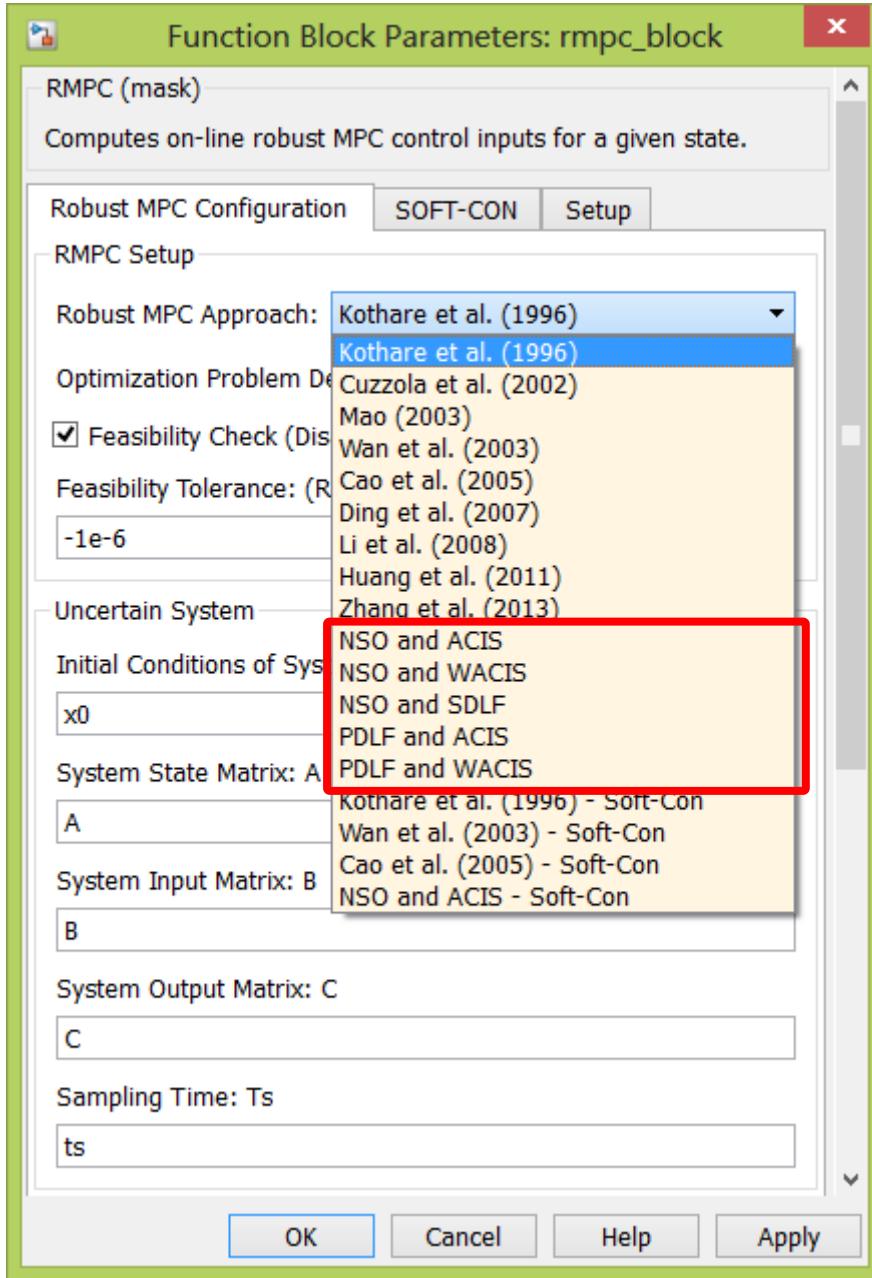
RMPC Approaches





RMPC Approaches

Kothare et al. (1996)
Cuzzola et al. (2002)
Mao (2003)
Wan et al. (2003)
Ding et al. (2007)
Li et al. (2008)
Cao et Li (2005)
Huang et al. (2011)
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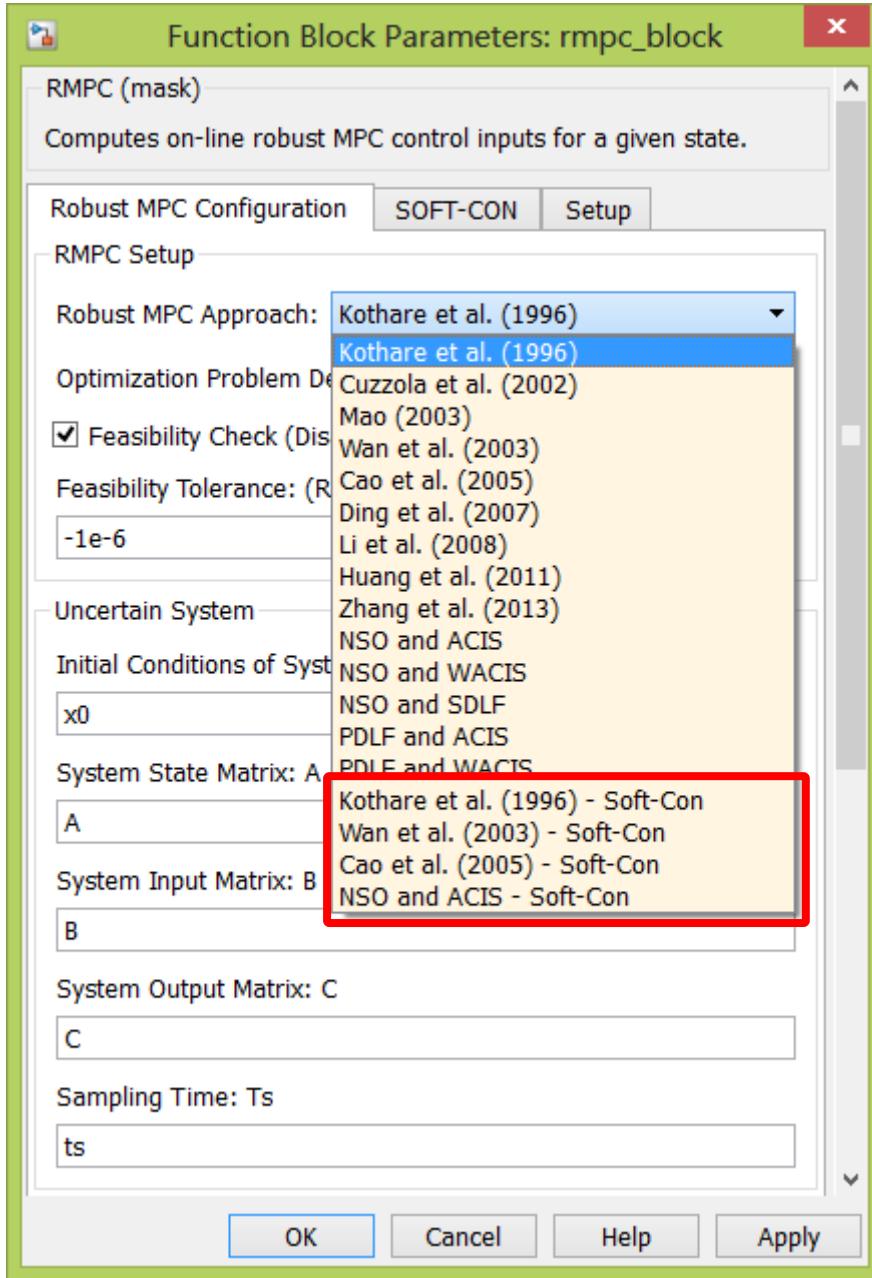
NSO and ACIS

NSO and WACIS

NSO and SDFL

PDLF and ACIS

PDLF and WACIS



RMPC Approaches

Kothare et al. (1996)

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NSO and ACIS

NSO and WACIS

NSO and SDFL

PDLF and ACIS

PDLF and WACIS

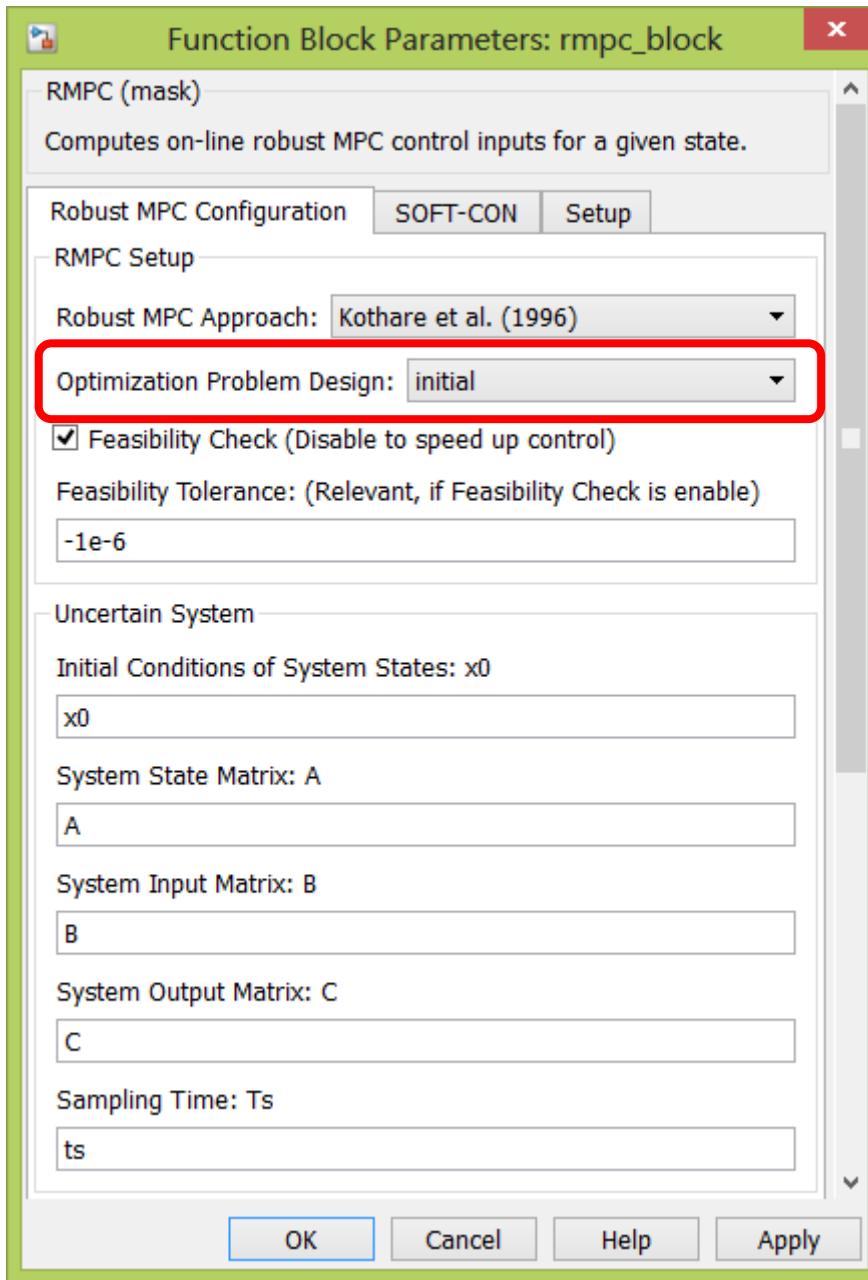
Kothare et al. (1996) – Soft-Con

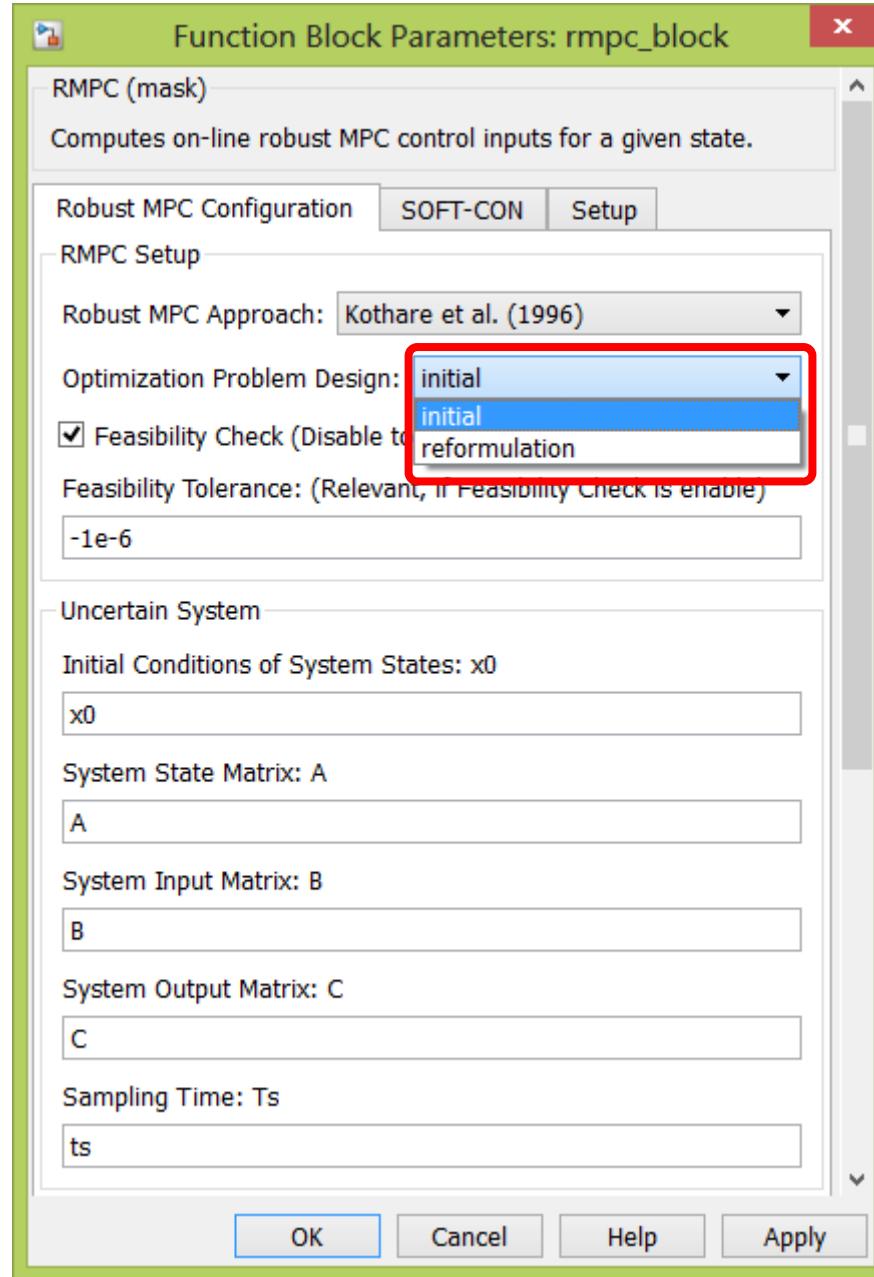
Wan et al. (2003) – Soft-Con

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NSO and ACIS – Soft-Con

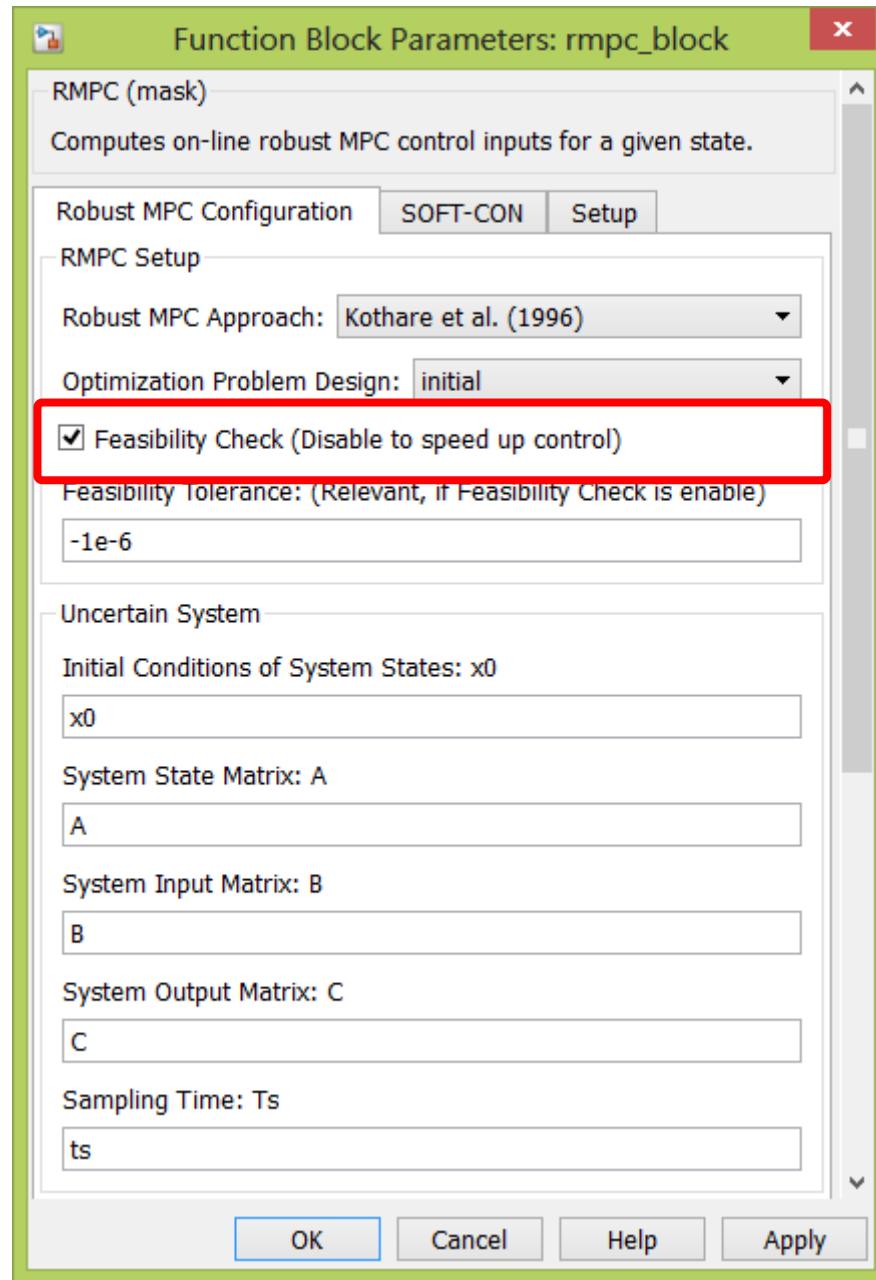
Opt. Problem Design





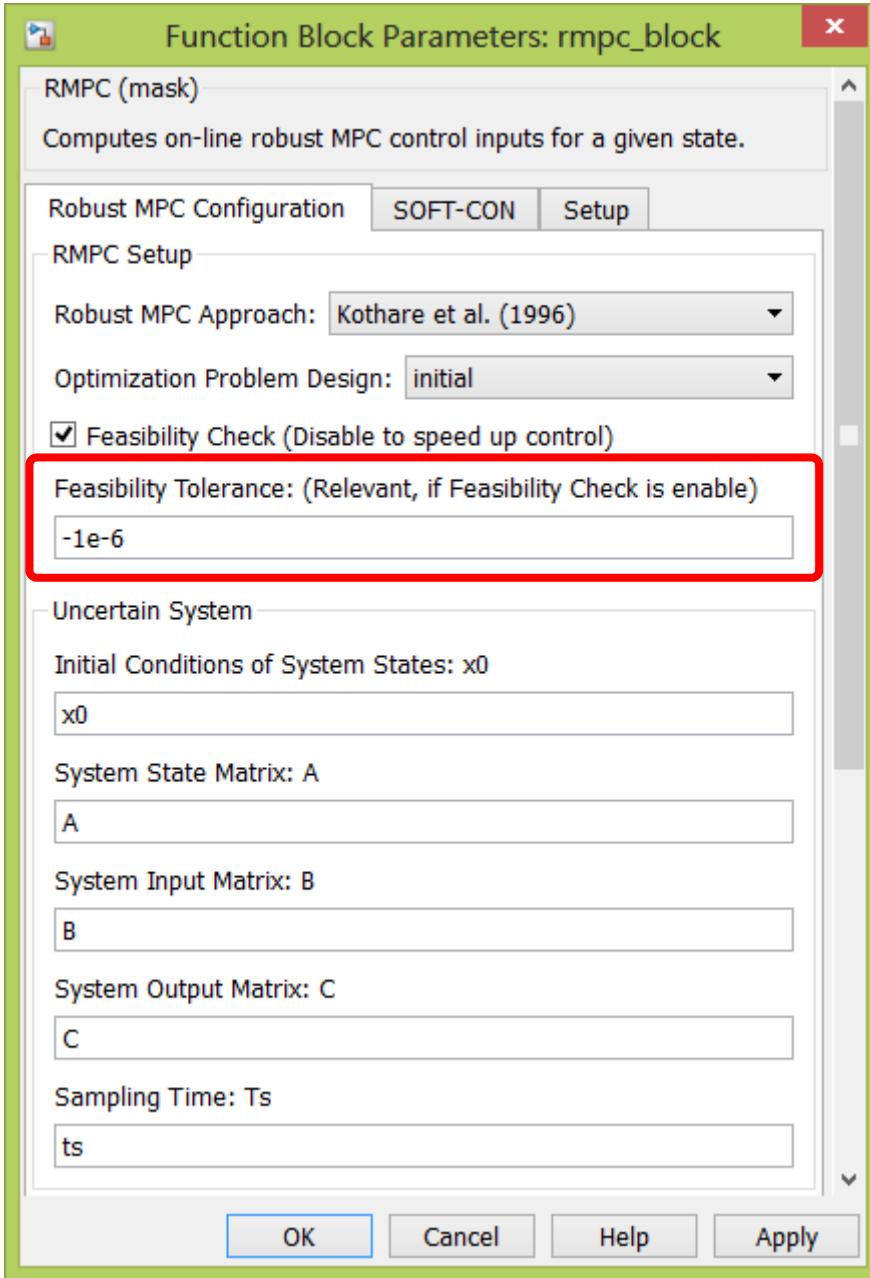
Opt. Problem Design

- initial: *speed up* (YALMIP/optimizer)
- reformulation: *analysis* (YALMIP/optimize)



Feasibility Check

enable



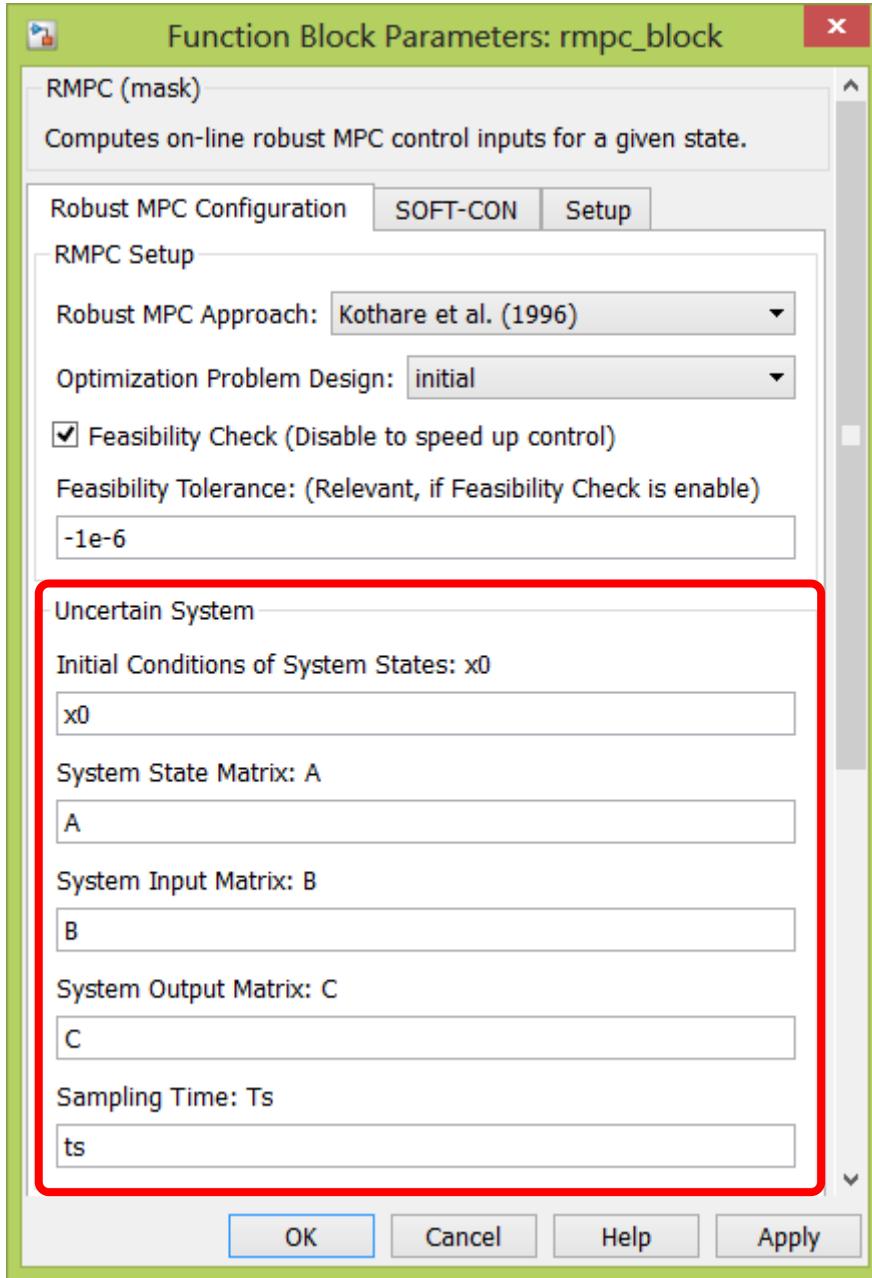
Feasibility Tolerance

- *default:* $\alpha = -1 \times 10^{-6}$

- SDP:

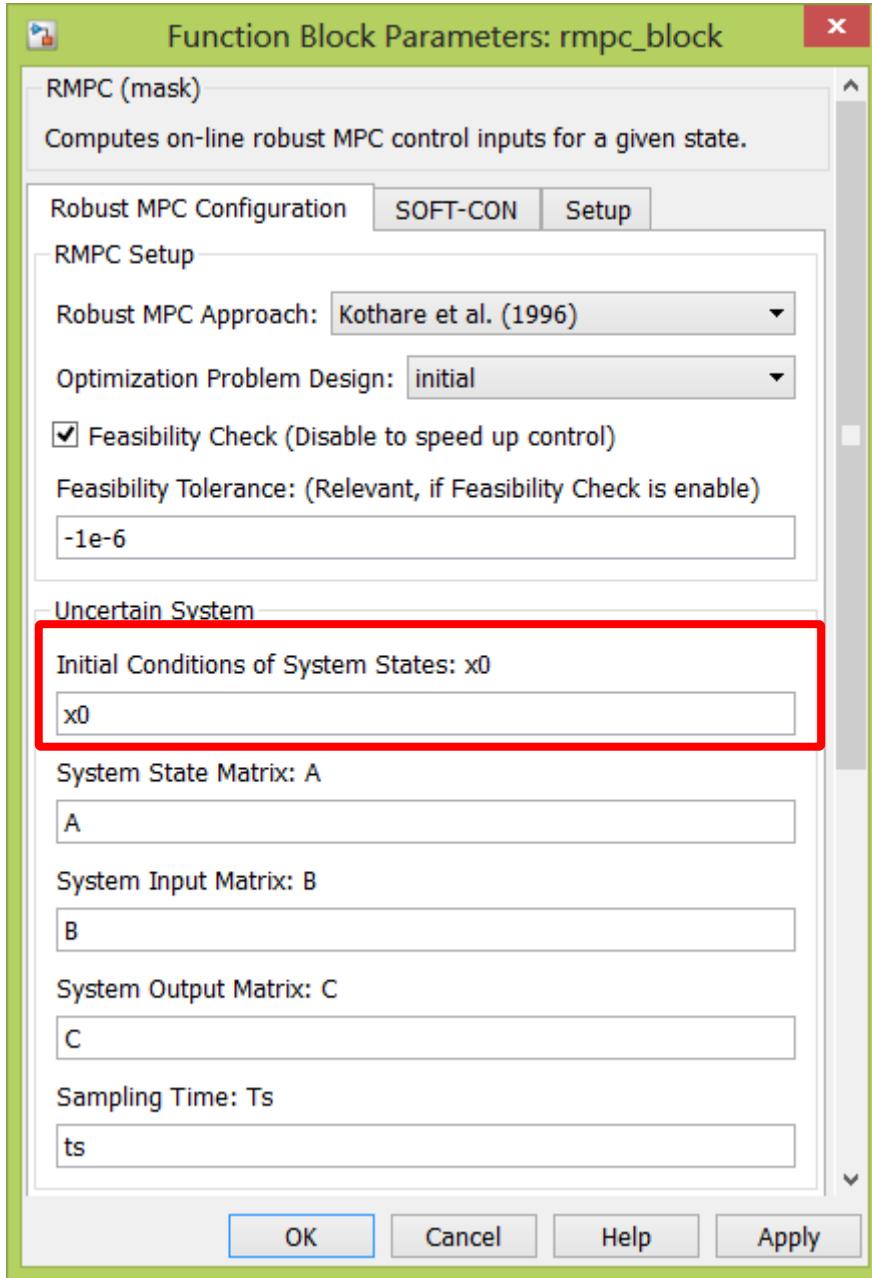
$$\min \gamma$$

$$\text{s.t.: } x \preceq_{\mathcal{K}} 0 \Rightarrow M(x) \preceq \alpha$$



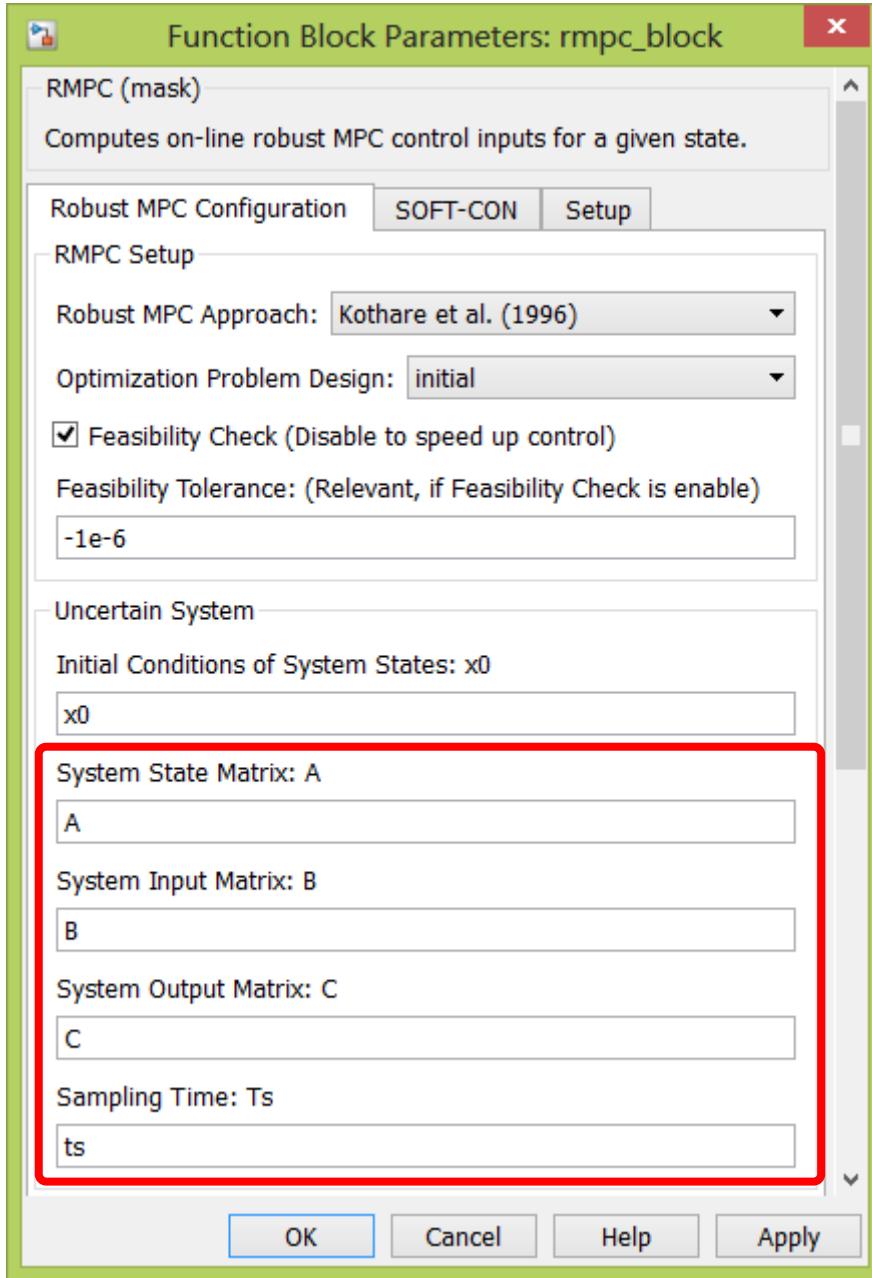
Uncertain System

$$\begin{aligned}x(k+1) &= Ax(k) + Bu(k), \\y(k) &= Cx(k), \quad x(0) = x_0, \\[A, B] &\in \text{convhull}\{[A_v, B_v]\}.\end{aligned}$$



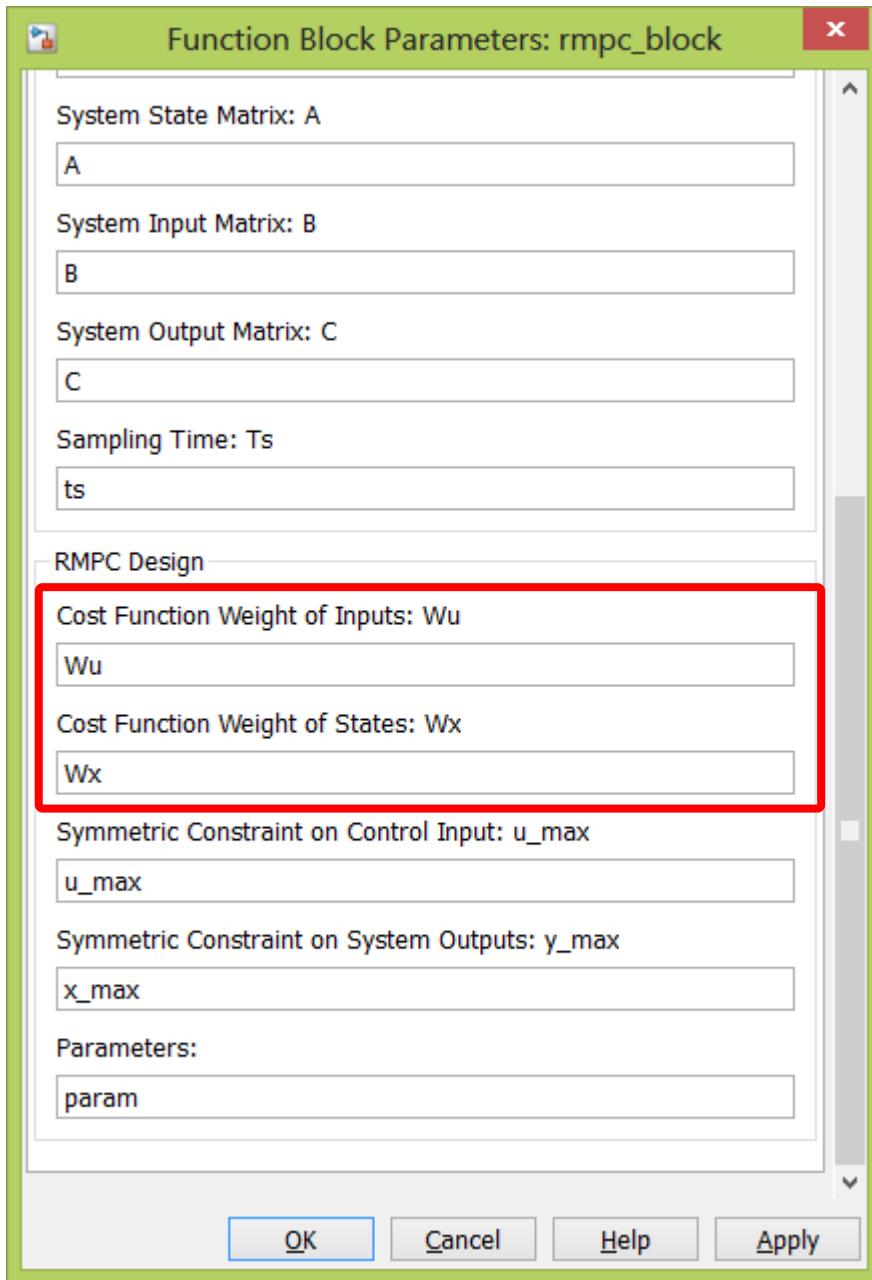
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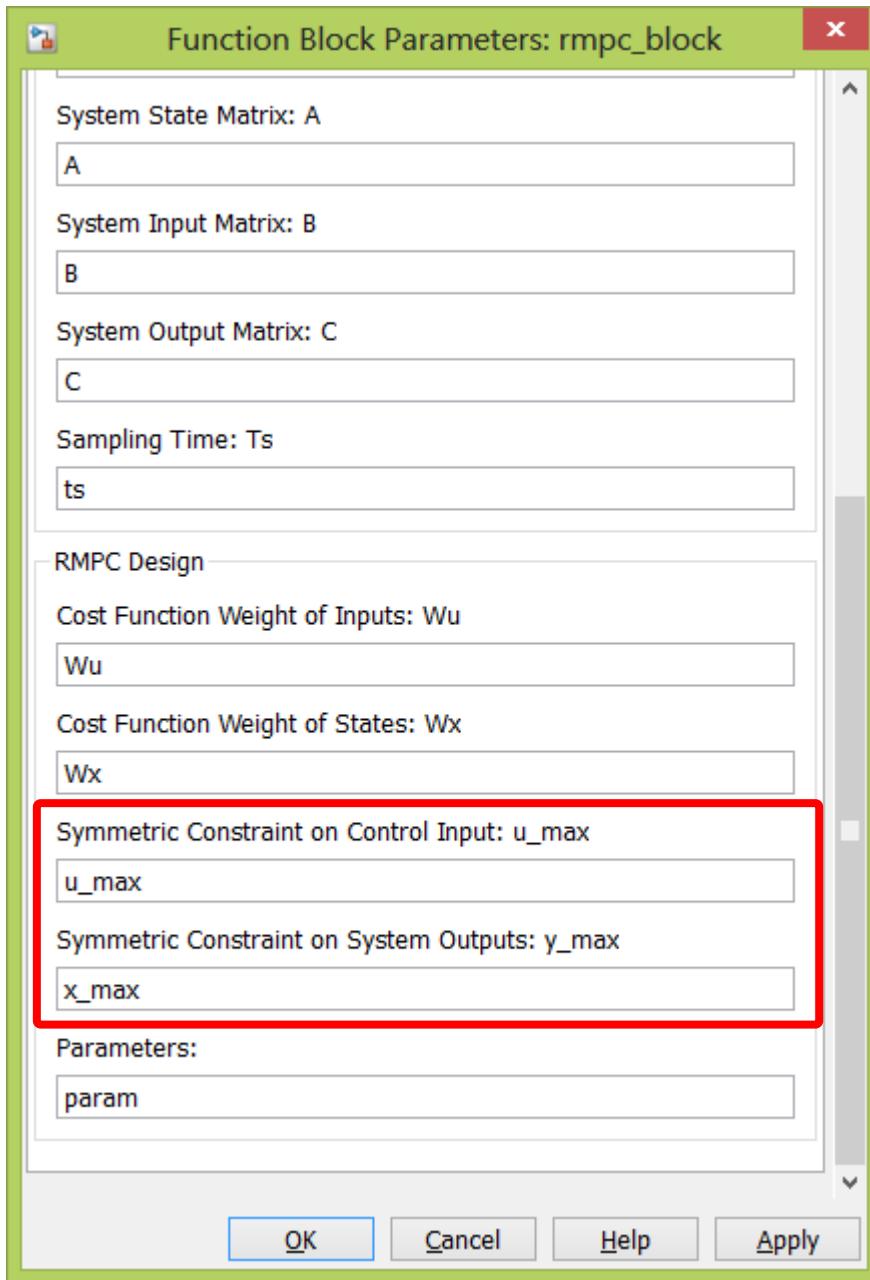
Uncertain System

$$x(k+1) = \mathbf{A} x(k) + \mathbf{B} u(k),$$
$$y(k) = \mathbf{C} x(k), \quad x(0) = x_0,$$
$$[\mathbf{A}, \mathbf{B}] \in \text{convhull}\{[\mathbf{A}_v, \mathbf{B}_v]\}.$$



Quality criterion

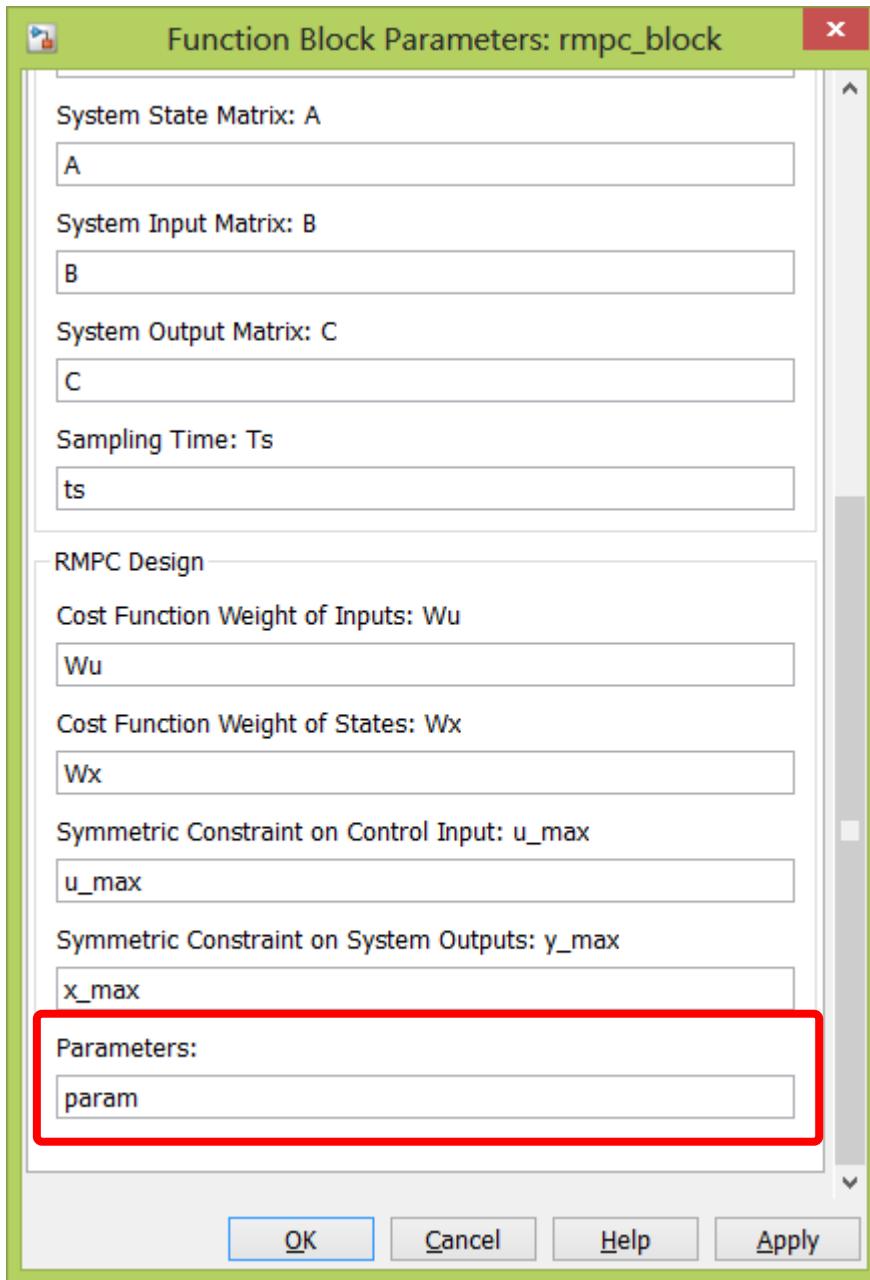
$$J = \sum_{i=0}^{\infty} (\ell(x(k+i), u(k+i))) =$$
$$\sum_{i=0}^{\infty} (x(k+i)^T \mathbf{W}_x x(k+i) +$$
$$u(k+i)^T \mathbf{W}_u u(k+i))$$



Quality criterion

$$\|u(k)\|_2^2 \leq \|u_{\max}\|_2^2$$

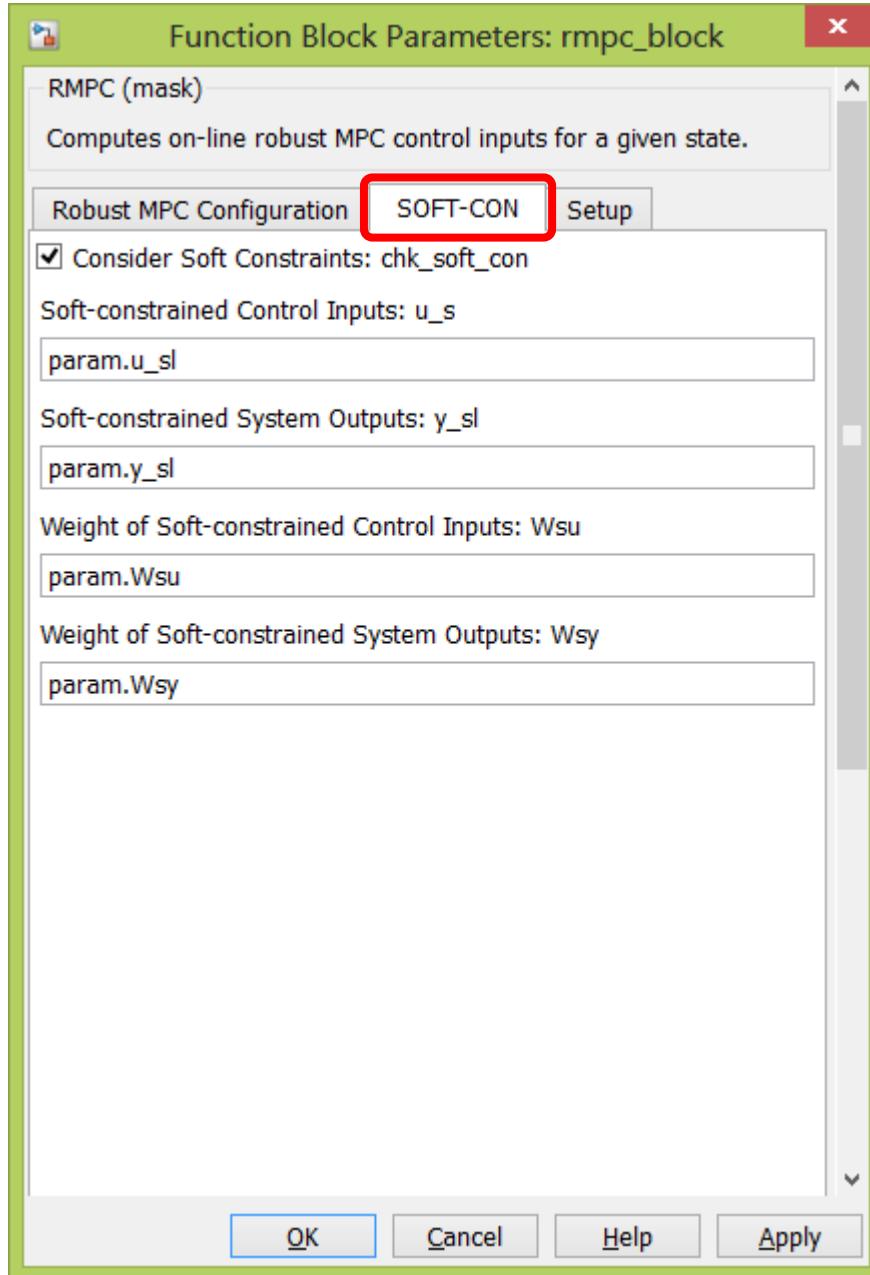
$$\|y(k)\|_2^2 \leq \|y_{\max}\|_2^2$$



Parameters

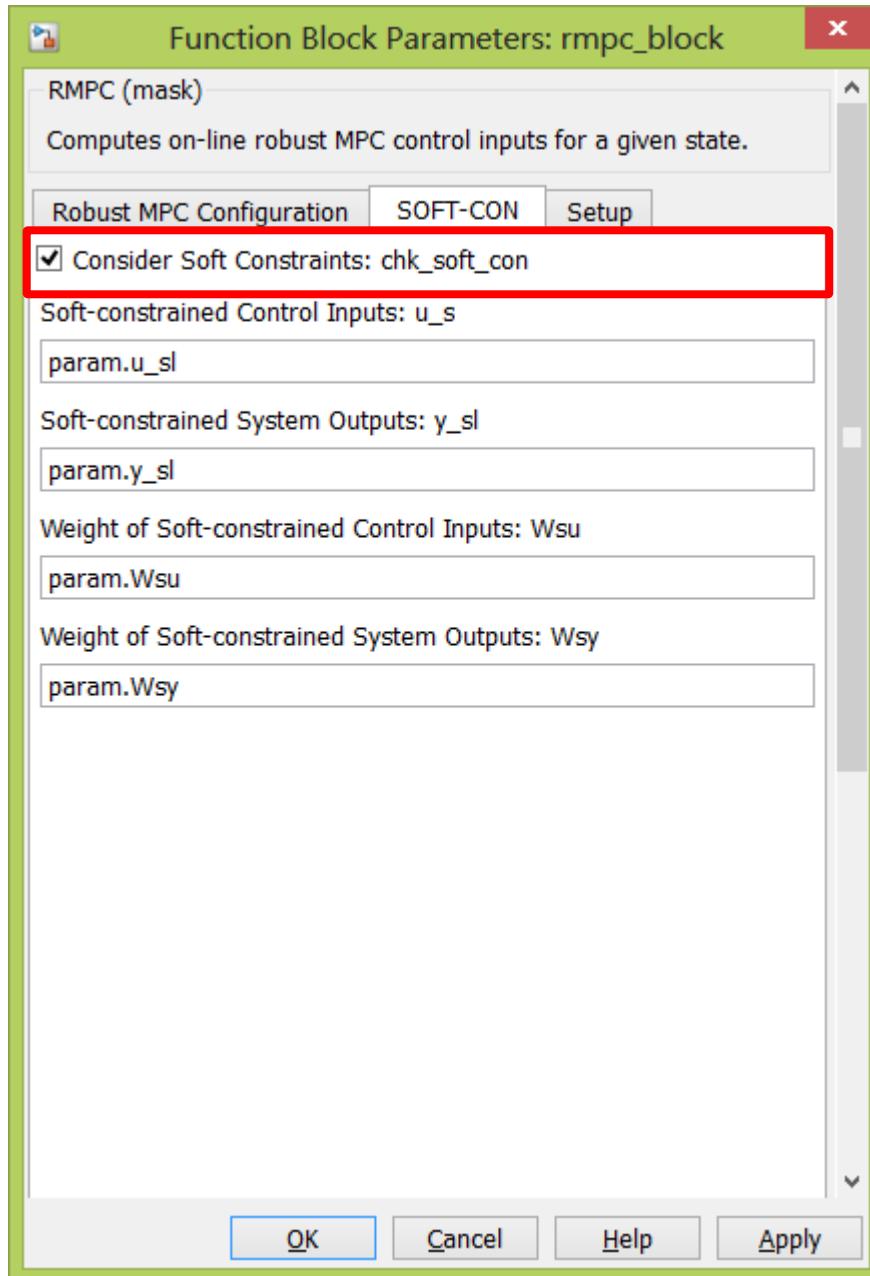
optional RMPC design and tuning parameters

Soft Constraints



Soft Constraints

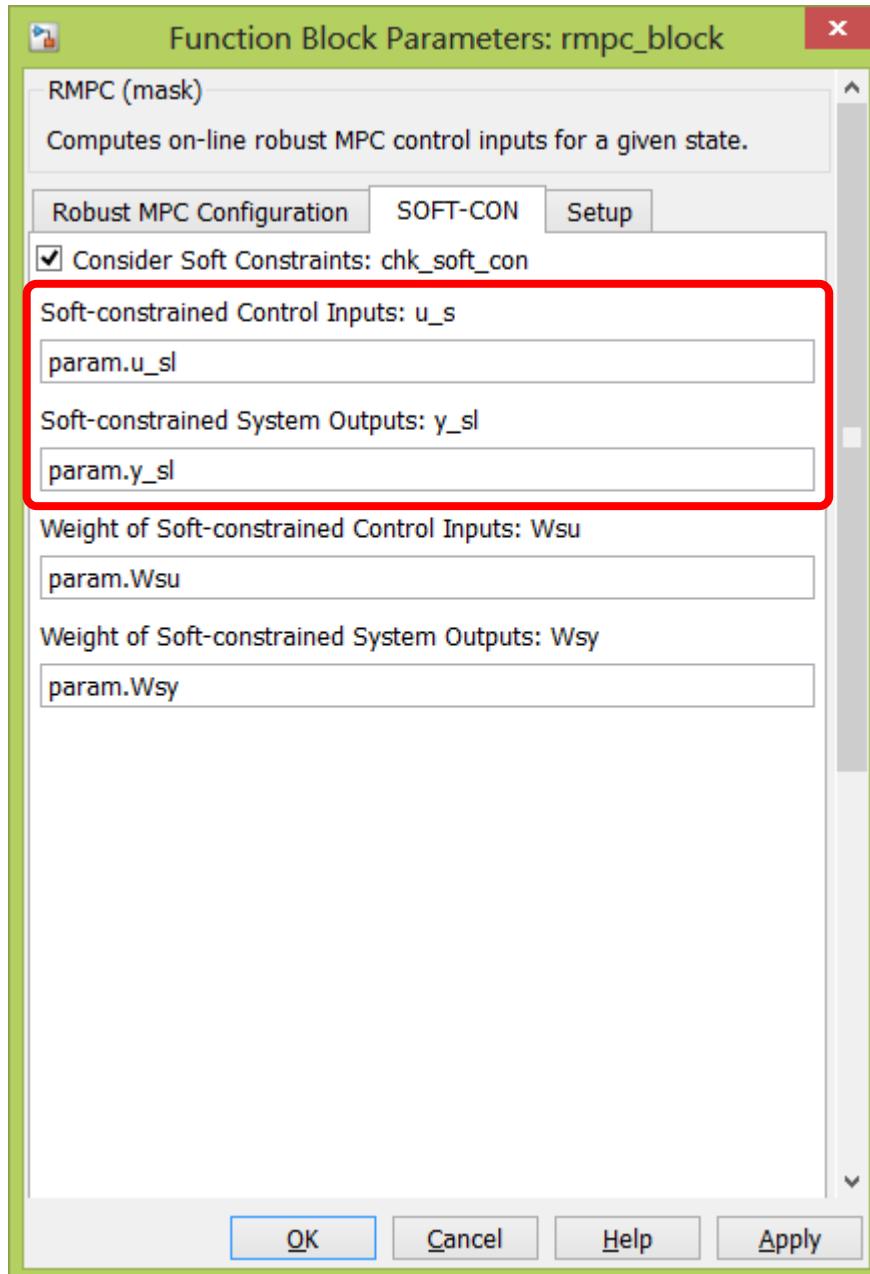
enable

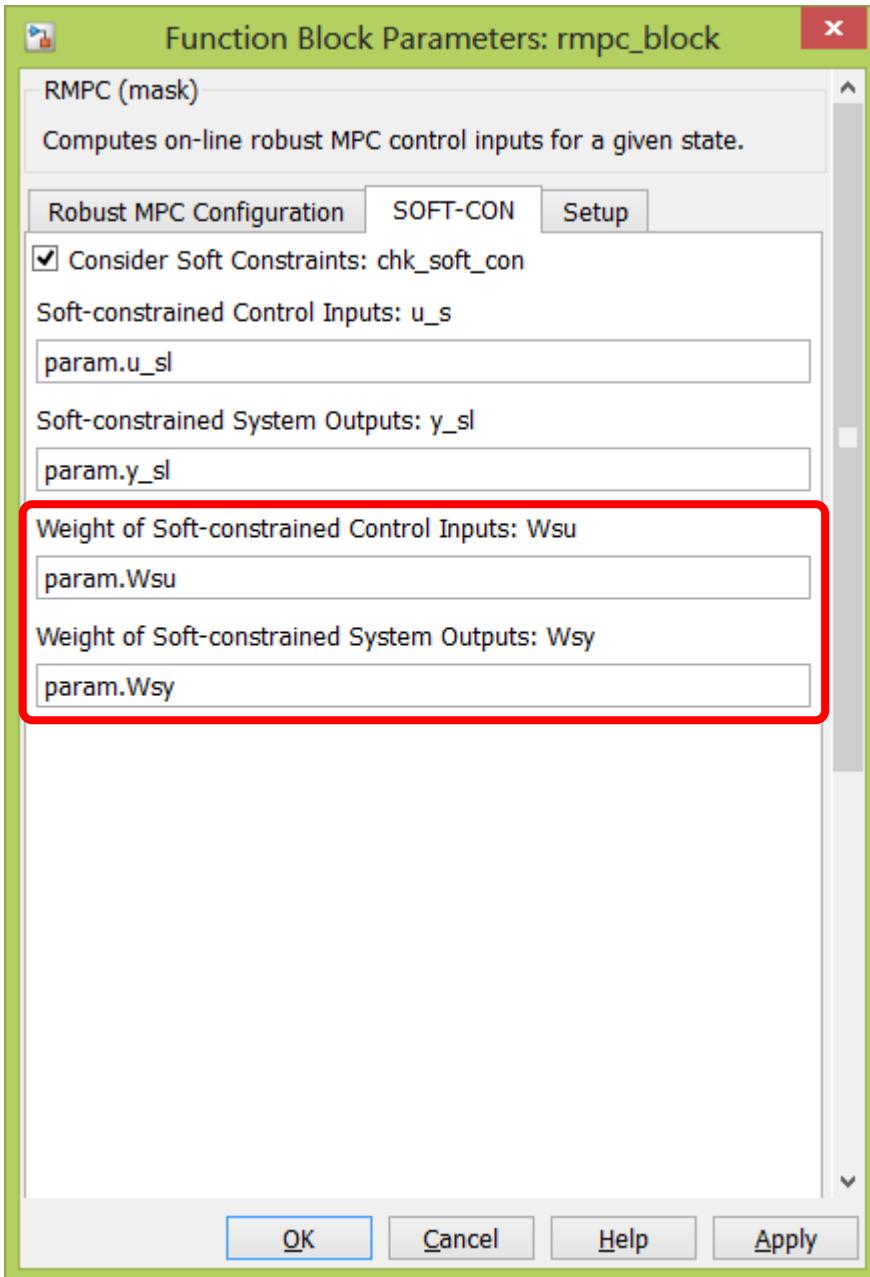


Soft Constraints

$$\|u(k)\|_2^2 \leq \|u_{\text{sl}}\|_2^2 < \|u_{\max}\|_2^2$$

$$\|y(k)\|_2^2 \leq \|y_{\text{sl}}\|_2^2 < \|y_{\max}\|_2^2$$

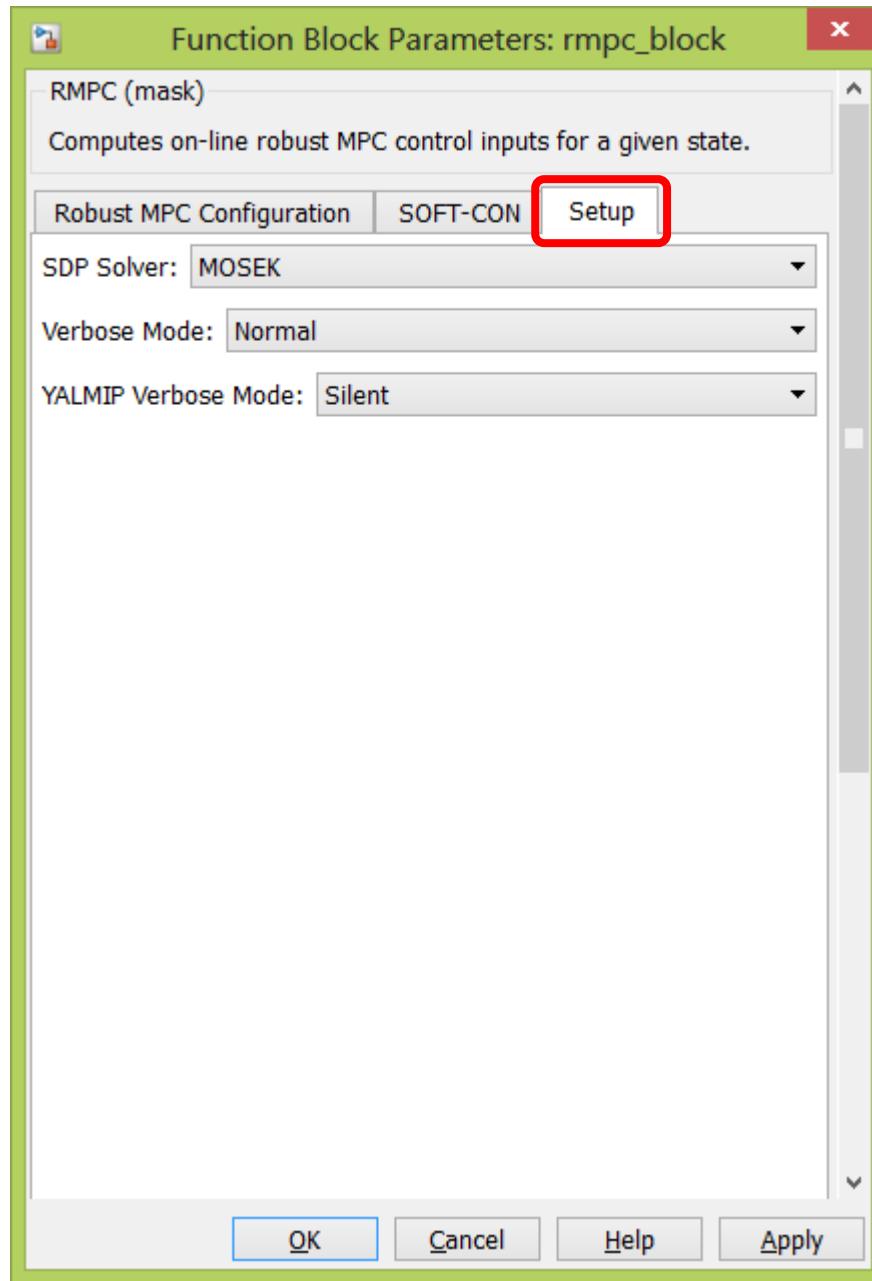




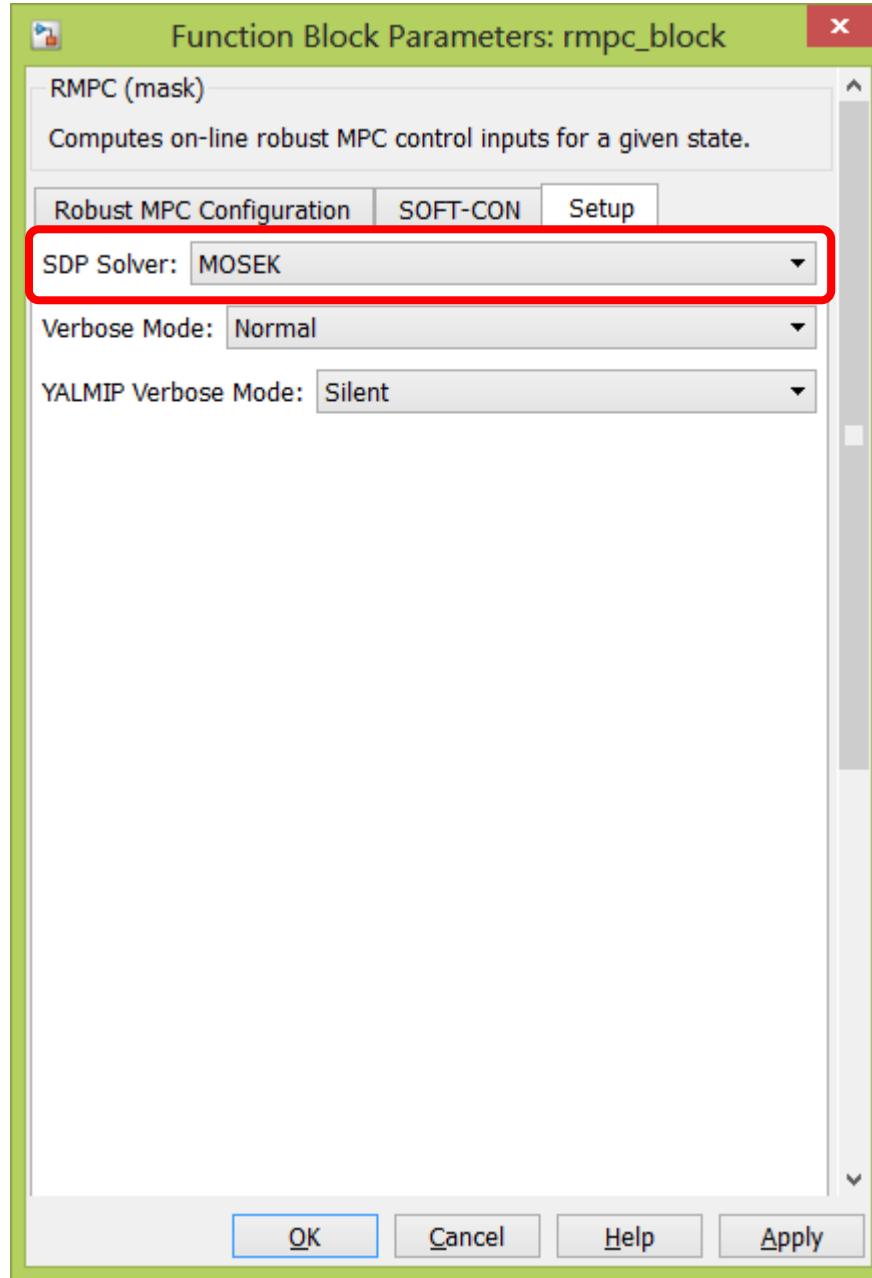
Soft Constraints

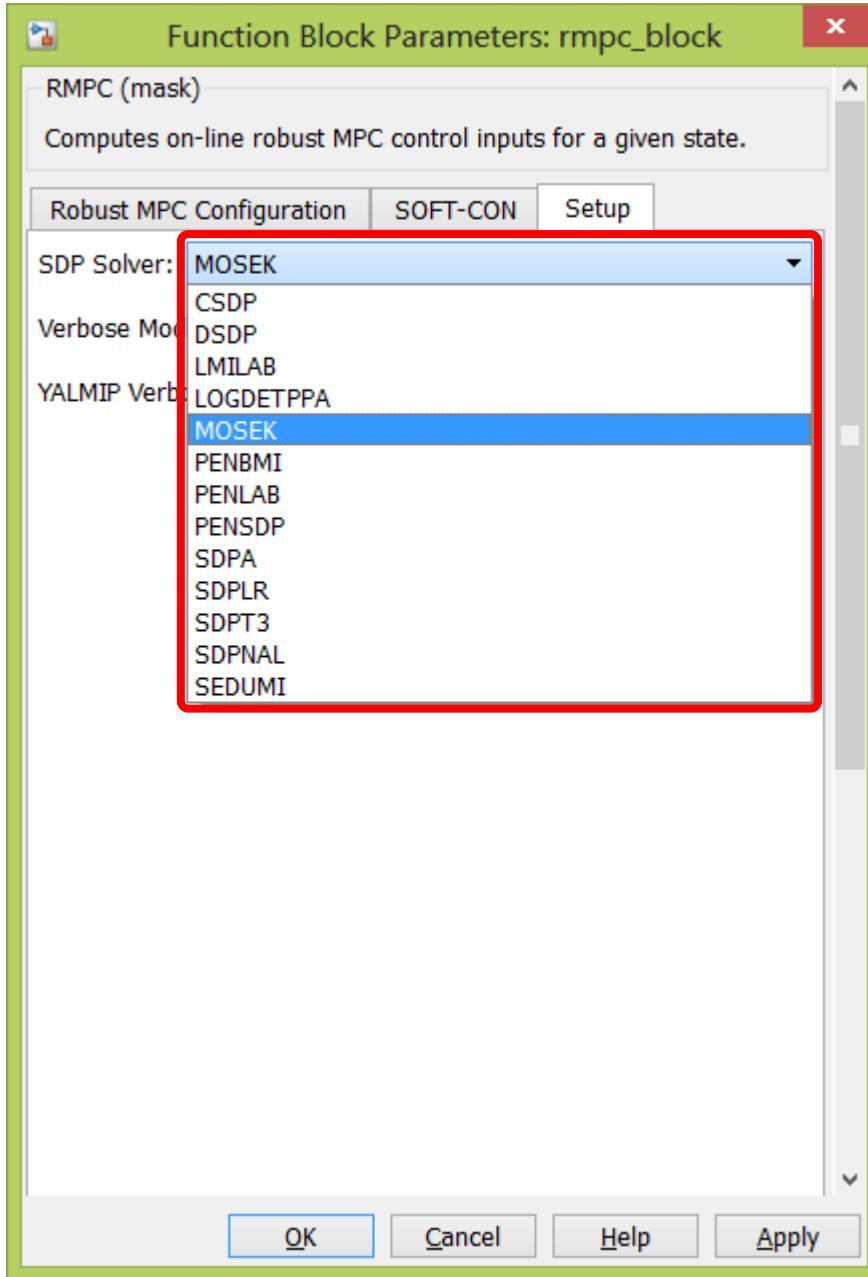
$$\begin{aligned}
 J = \sum_{i=0}^{\infty} (\ell(x(k+i), u(k+i))) = \\
 \sum_{i=0}^{\infty} (x(k+i)^T W_x x(k+i) + \\
 u(k+i)^T W_u u(k+i) + \\
 s_u(k+i)^T \color{red}{W_{su}} s_u(k+i) + \\
 s_y(k+i)^T \color{red}{W_{sy}} s_y(k+i))
 \end{aligned}$$

Setup



SDP Solver

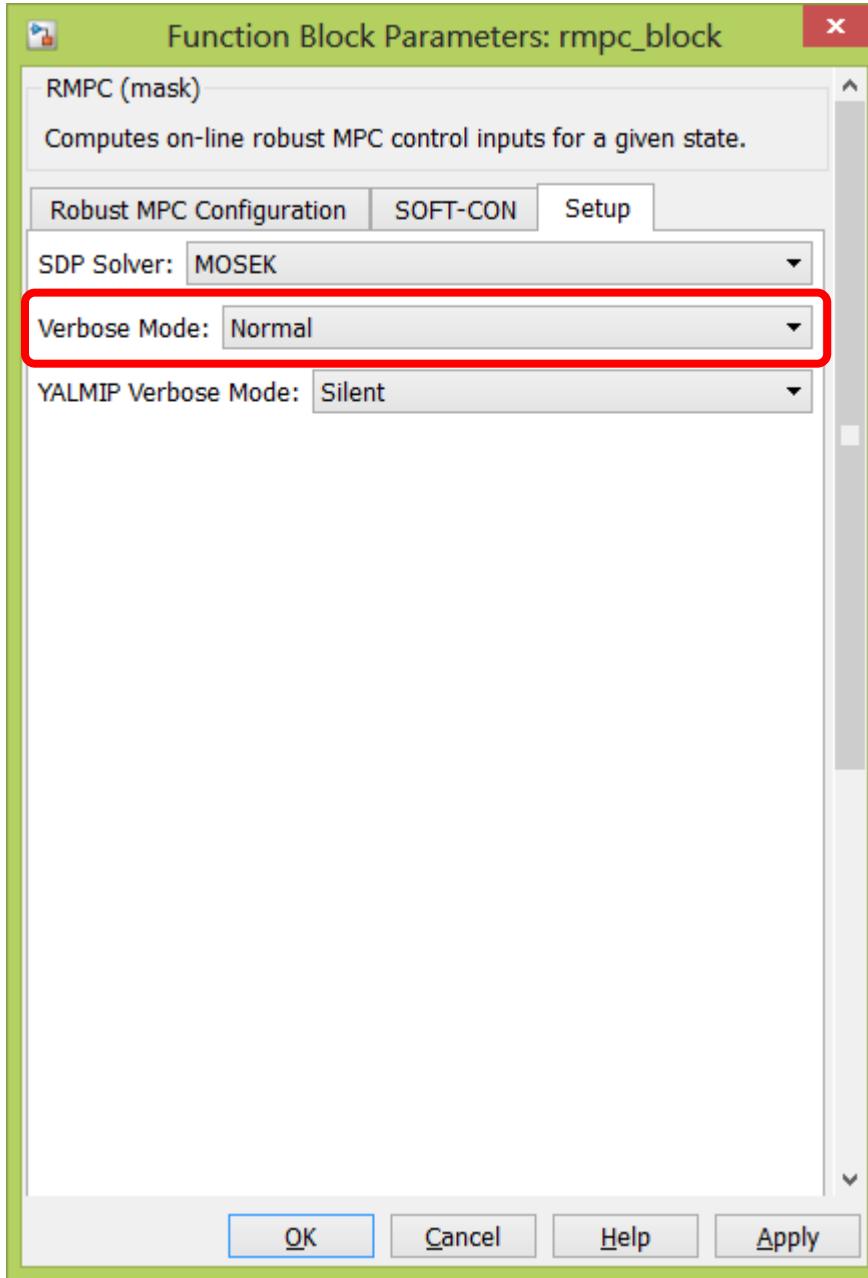


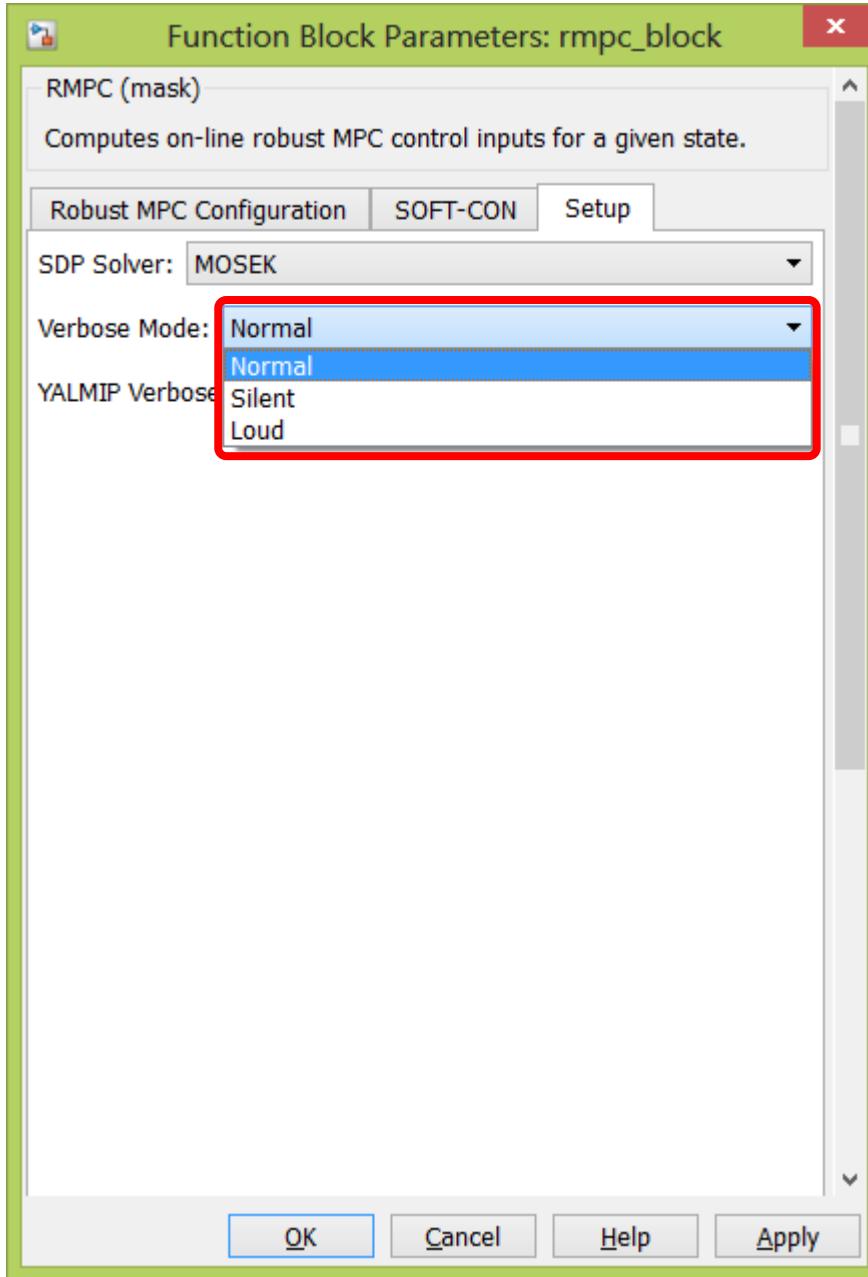


SDP Solver

- CSDP (*free*)
- DSDP (*free*)
- LMILAB (*comercial*)
- LOGDETPPA (*free*)
- **MOSEK (*free for academia*)**
- PENBMI (*comercial*)
- PENLAB (*free*)
- PENSMP (*free for academia*)
- SDPA (*free*)
- SDPLR (*free*)
- SDPT3 (*free*)
- SDPNAL (*free*)
- **SEDUMI (*free*)**

Verbose Mode

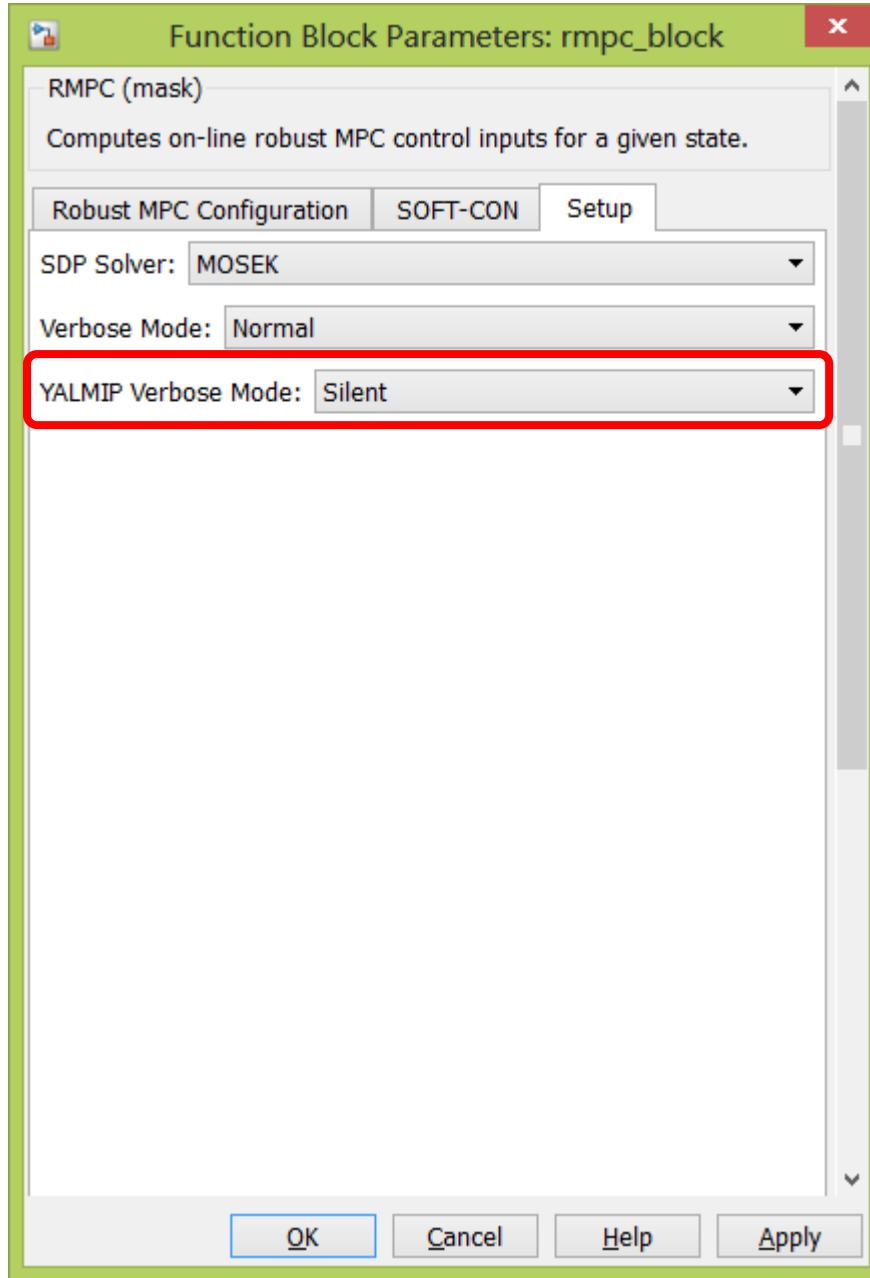


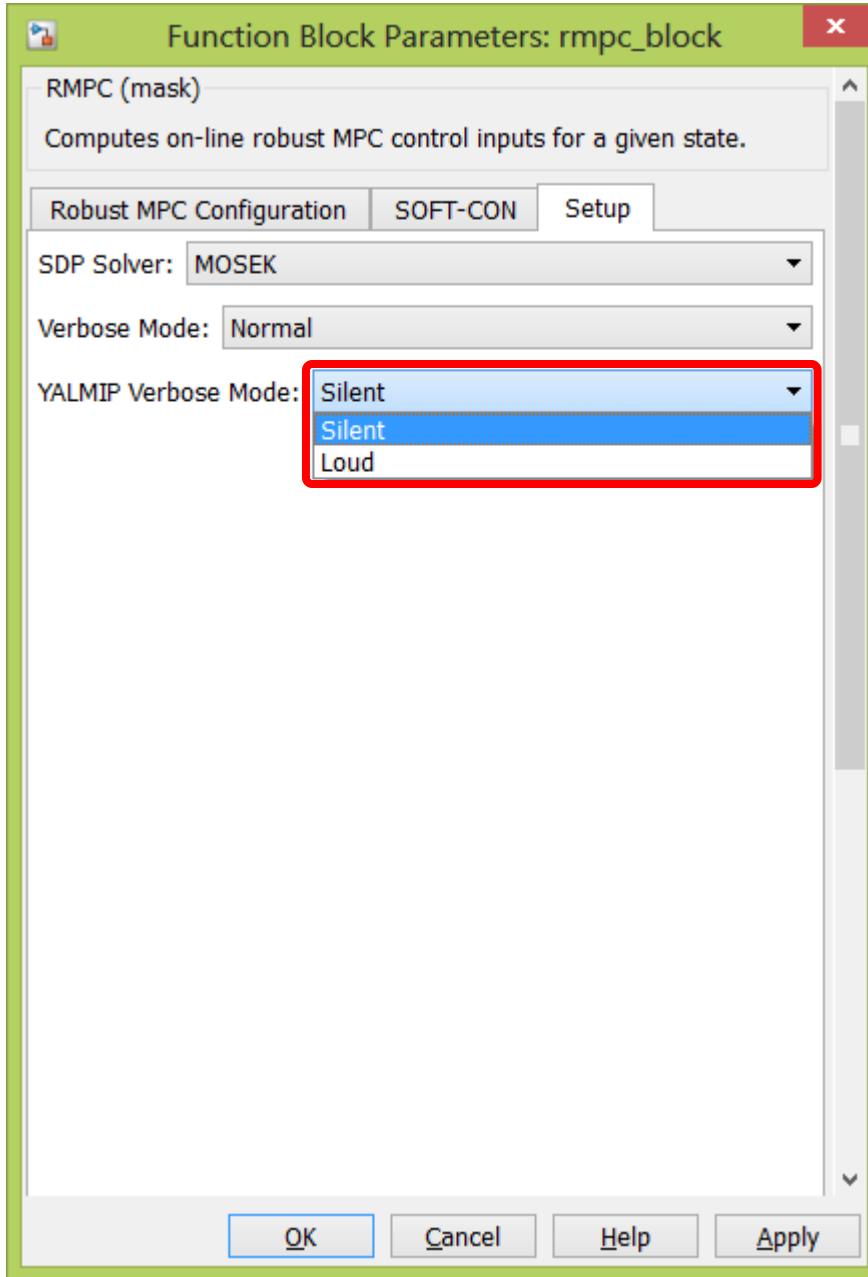


Verbose Mode

- Normal
- Silent
- Loud (*recommended*)

YALMIP Verbose Mode





YALMIP Verbose Mode

- Silent
- Loud

How to use it?

- MATLAB/Simulink: `rmpc_block` (*beginner*)
- MATLAB: CLI (*advanced*)

CLI

```
1: %% System  
2: A{1}=. . .; A{2}=. . .; A{3}=. . .; A{4}=. . .;  
3: B{1}=. . .; B{2}=. . .; B{3}=. . .; B{4}=. . .;  
4: C{1}=. . .; C{2}=. . .; C{3}=. . .; C{4}=. . .;  
5: ts=0.5; % Sampling time  
6: x0=. . .; % Initial conditions
```

CLI

```
1: %% System  
2: A{1}=. . . ; A{2}=. . . ; A{3}=. . . ; A{4}=. . . ;  
3: B{1}=. . . ; B{2}=. . . ; B{3}=. . . ; B{4}=. . . ;  
4: C{1}=. . . ; C{2}=. . . ; C{3}=. . . ; C{4}=. . . ;  
5: ts=0.5; % Sampling time  
6: x0=. . . ; % Initial conditions
```

$$x(k+1) = \textcolor{red}{A} x(k) + \textcolor{red}{B} u(k),$$

$$y(k) = \textcolor{red}{C} x(k), \quad x(0) = x_0,$$

$$[A, B] \in \text{convhull}\{[A_v, B_v]\}.$$

CLI

```
1: %% System  
2: A{1}=. . . ; A{2}=. . . ; A{3}=. . . ; A{4}=. . . ;  
3: B{1}=. . . ; B{2}=. . . ; B{3}=. . . ; B{4}=. . . ;  
4: C{1}=. . . ; C{2}=. . . ; C{3}=. . . ; C{4}=. . . ;  
5: ts=0.5; % Sampling time  
6: x0=. . . ; % Initial conditions
```

$$x(k+1) = Ax(k) + Bu(k),$$

$$y(k) = Cx(k), \quad x(0) = x_0,$$

$$[A, B] \in \text{convhull}\{[A_v, B_v]\}.$$

CLI

```
1: %% Symmetric constraints  
2: u_max=...; % Input constraints  
3: x_max=...; % State constraints
```

$$\|u(k)\|_2^2 \leq \|u_{\max}\|_2^2$$

$$\|x(k)\|_2^2 \leq \|x_{\max}\|_2^2$$

CLI

```
1: %% RMPC design
2: Wx=...; Wu=...;           % Weight matrices
3: [method,kwd] = mup_cli_rmpc_method; % Method
4: mup_sdp                   % SDP formulation
5: mup_rmpc_opt              % RMPC design
```

CLI

```
1: %% RMPC design  
2: Wx=...; Wu=...;          % Weight matrices  
3: [method,kwd] = mup_cli_rmpc_method; % Method  
4: mup_sdp                  % SDP formulation  
5: mup_rmpc_opt             % RMPC design
```

$$J(k) = \sum_{i=0}^{\infty} (x(k+i)^T \mathbf{W_x} x(k+i) + u(k+i)^T \mathbf{W_u} u(k+i))$$

CLI

```
1: %% RMPC design
2: Wx=...; Wu=...;           % Weight matrices
3: [method,kwd] = mup_cli_rmpc_method; % Method
4: mup_sdp                   % SDP formulation
5: mup_rmpc_opt              % RMPC design
```

CLI

```
1: %% RMPC design
2: Wx=...; Wu=...;           % Weight matrices
3: [method,kwd] = mup_cli_rmpc_method; % Method
4: mup_sdp                   % SDP formulation
5: mup_rmpc_opt              % RMPC design
```

List of available methods:

- 1: Cao et al. (2005)
- 2: Cao et al. (2005) - Soft-Con
- 3: Cuzzola et al. (2002)

CLI

```
1: %% RMPC design
2: Wx=...; Wu=...;           % Weight matrices
3: [method,kwd] = mup_cli_rmpc_method; % Method
4: mup_sdp                  % SDP formulation
5: mup_rmpc_opt              % RMPC design
```

$$\min \gamma,$$

$$\text{s.t.: } x \preceq_{\mathcal{K}} 0 \Rightarrow M(x) \prec 0$$

CLI

```
1: %% RMPC design
2: Wx=...; Wu=...;           % Weight matrices
3: [method,kwd] = mup_cli_rmpc_method; % Method
4: mup_sdp                   % SDP formulation
5: mup_rmpc_opt             % RMPC design
```

$$u(k) = F(x(k)) x(k)$$

CLI

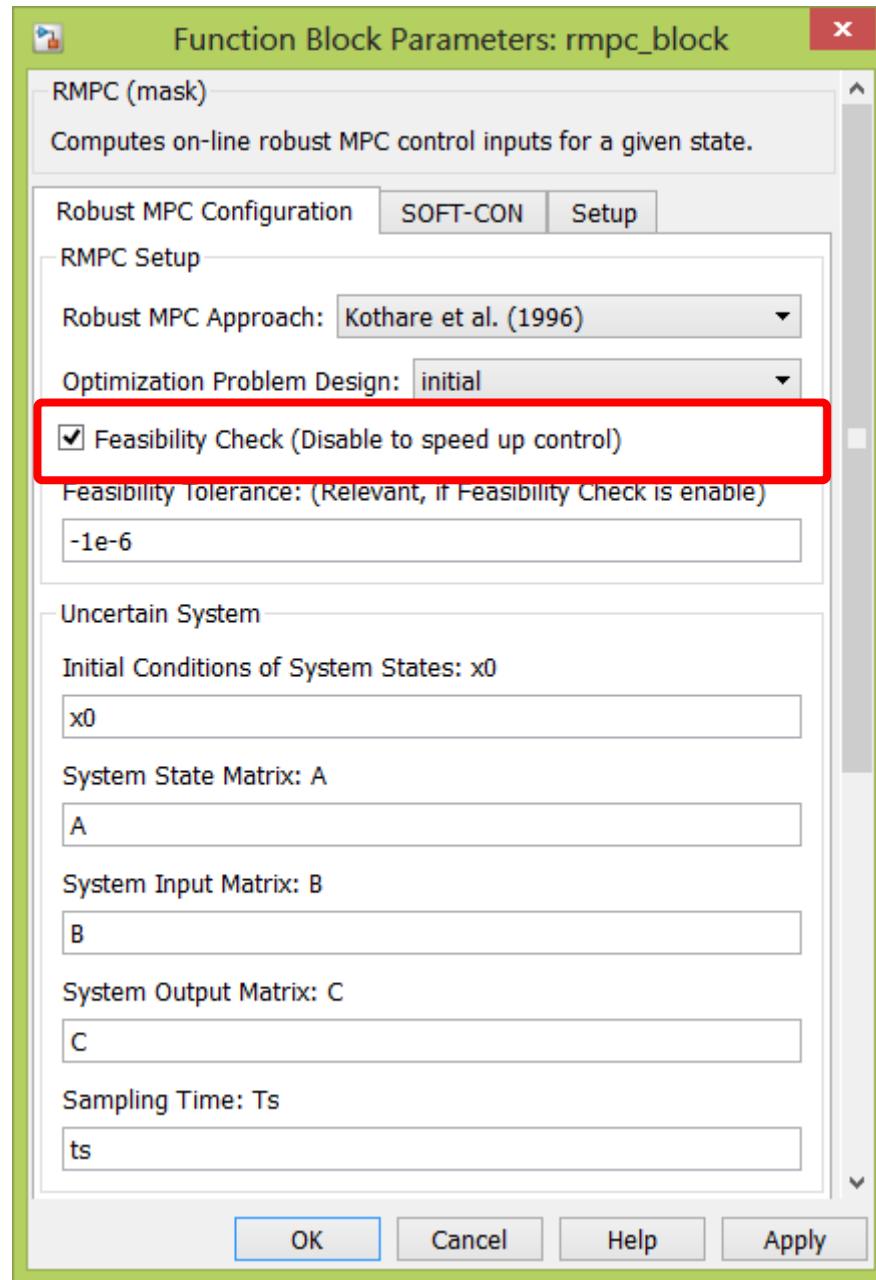
```
1: %% Closed-loop control in k-th step  
2: u = mup_rmpc{x,design}; % RMPC control law
```

CLI

```
1: %% Closed-loop control in k-th step  
2: u = mup_rmpc{x,design}; % RMPC control law
```

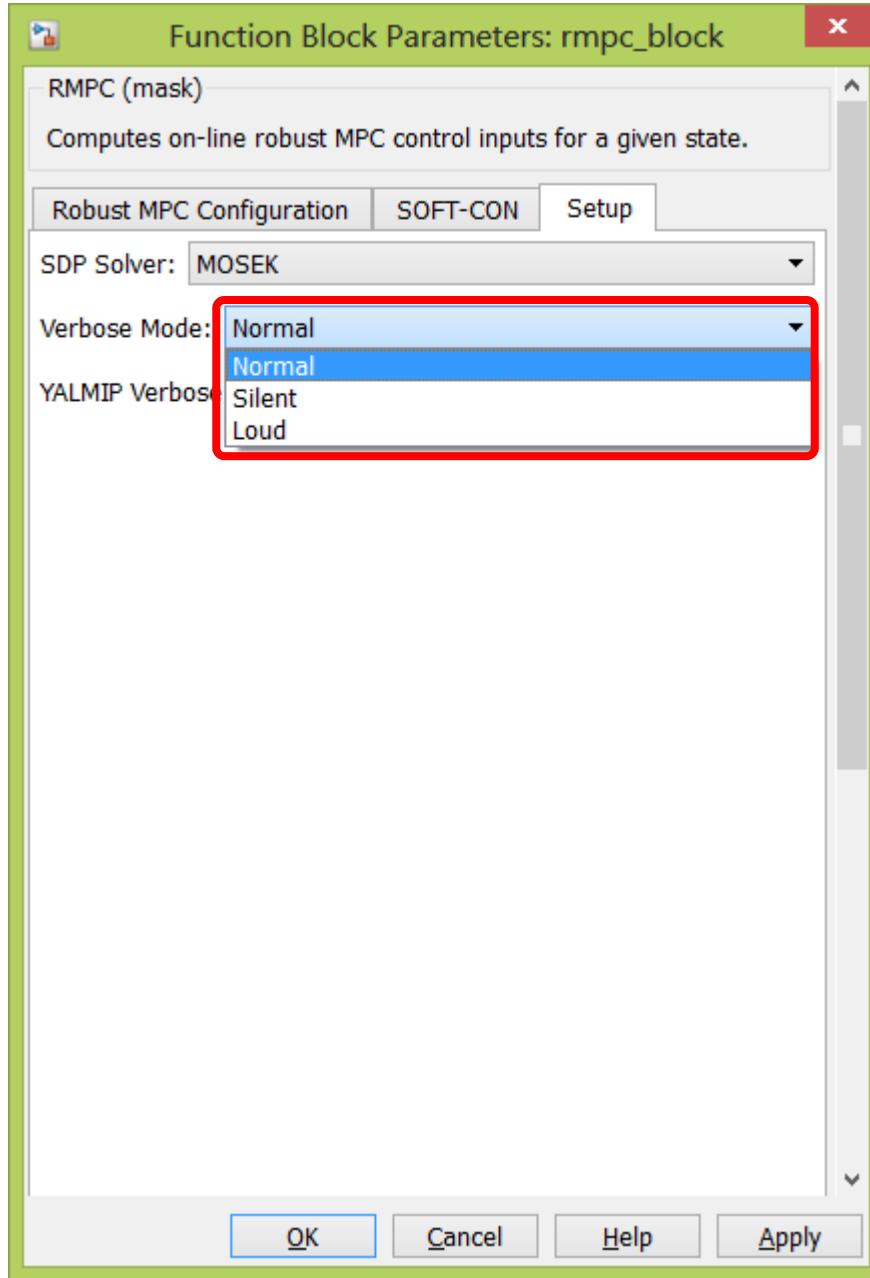
$$u(k) = F(x(k)) x(k)$$

Feasibility Check



Feasibility Check

enable



Verbose Mode

- **Normal**
- **Silent**
- **Loud (*recommended*)**

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

MUP:BLOCK:RMPC: Valid. (0.12)

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Terminated. (0.03 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

MUP:BLOCK:RMPC: Valid. (0.12)

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Terminated. (0.03 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

FEAS_CHK: VTX:2 | I:2: Stability condition violated!

FEAS_CHK: VTX:1 | I:1: State constraints violated!

FEAS_CHK: VTX:1 | I:2: State constraints violated!

FEAS_CHK: VTX:2 | I:1: State constraints violated!

FEAS_CHK: VTX:2 | I:2: State constraints violated!

FEAS_CHK: VTX:3 | I:1: State constraints violated!

FEAS_CHK: VTX:3 | I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Termined. (0.05 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

FEAS_CHK: VTX:2 | I:2: Stability condition violated!

FEAS_CHK: VTX:1 | I:1: State constraints violated!

FEAS_CHK: VTX:1 | I:2: State constraints violated!

FEAS_CHK: VTX:2 | I:1: State constraints violated!

FEAS_CHK: VTX:2 | I:2: State constraints violated!

FEAS_CHK: VTX:3 | I:1: State constraints violated!

FEAS_CHK: VTX:3 | I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Termined. (0.05 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

FEAS_CHK: VTX:2 | I:2: Stability condition violated!

FEAS_CHK: VTX:1 | I:1: State constraints violated!

FEAS_CHK: VTX:1 | I:2: State constraints violated!

FEAS_CHK: VTX:2 | I:1: State constraints violated!

FEAS_CHK: VTX:2 | I:2: State constraints violated!

FEAS_CHK: VTX:3 | I:1: State constraints violated!

FEAS_CHK: VTX:3 | I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Termined. (0.05 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

FEAS_CHK: VTX:2 | I:2: Stability condition violated!

FEAS_CHK: VTX:1 | I:1: State constraints violated!

FEAS_CHK: VTX:1 | I:2: State constraints violated!

FEAS_CHK: VTX:2 | I:1: State constraints violated!

FEAS_CHK: VTX:2 | I:2: State constraints violated!

FEAS_CHK: VTX:3 | I:1: State constraints violated!

FEAS_CHK: VTX:3 | I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Terminated. (0.05 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

FEAS_CHK: VTX:2 | I:2: Stability condition violated!

FEAS_CHK: VTX:1 | I:1: State constraints violated!

FEAS_CHK: VTX:1 | I:2: State constraints violated!

FEAS_CHK: VTX:2 | I:1: State constraints violated!

FEAS_CHK: VTX:2 | I:2: State constraints violated!

FEAS_CHK: VTX:3 | I:1: State constraints violated!

FEAS_CHK: VTX:3 | I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Terminated. (0.05 sec)

Feasibility Check /Normal

MUP:BLOCK:RMPC: FEASIBILITY_CHECK initialized...

FEAS_CHK: VTX:2|I:1: Stability condition violated!

FEAS_CHK: VTX:2|I:2: Stability condition violated!

FEAS_CHK: VTX:1|I:1: State constraints violated!

FEAS_CHK: VTX:1|I:2: State constraints violated!

FEAS_CHK: VTX:2|I:1: State constraints violated!

FEAS_CHK: VTX:2|I:2: State constraints violated!

FEAS_CHK: VTX:3|I:1: State constraints violated!

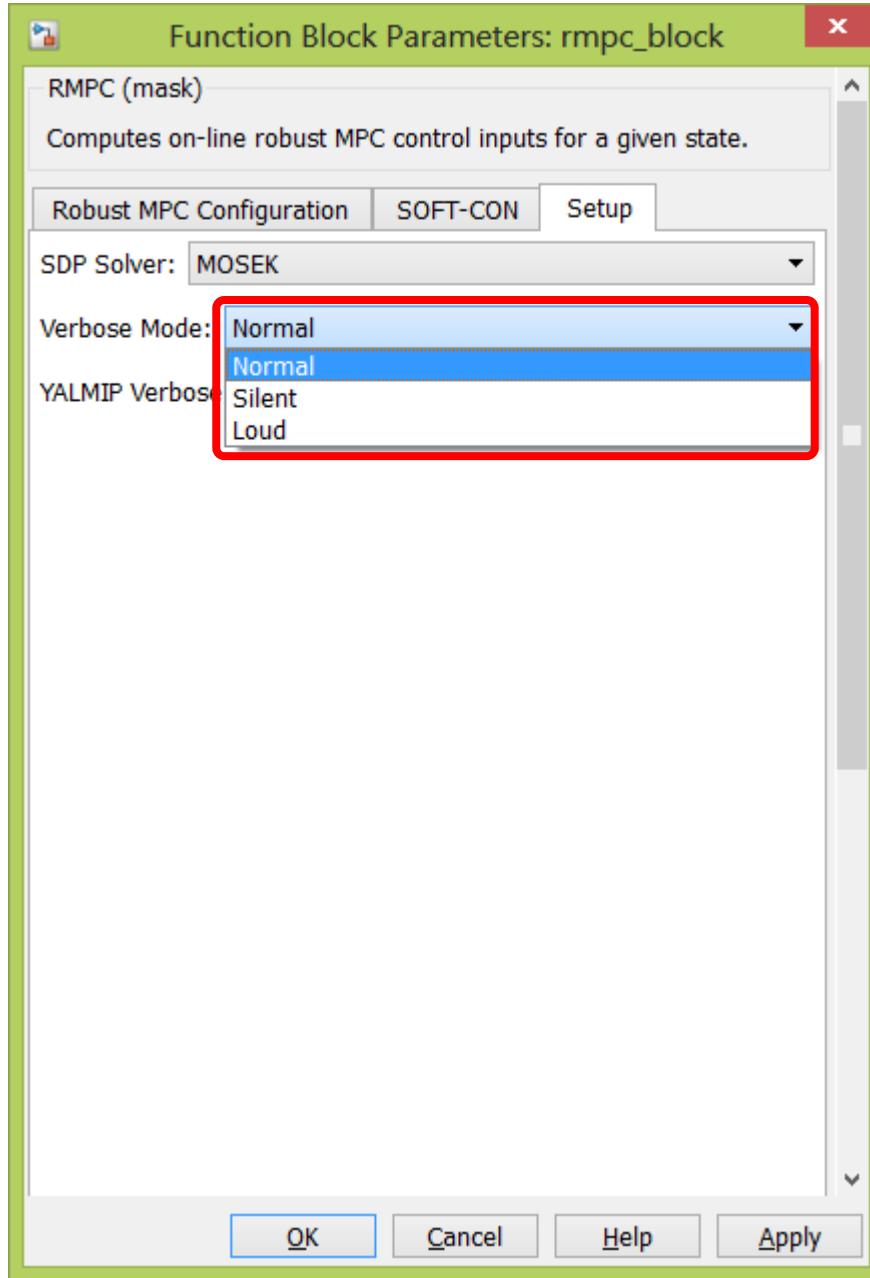
FEAS_CHK: VTX:3|I:2: State constraints violated!

FEAS_CHK: Failed! (8 infeasible constraint(s) found) (0.03)

MUP:BLOCK:RMPC: Feasibility check failed!

MUP:BLOCK:RMPC: Instead of Robust MPC has been designed LQ controller

MUP:BLOCK:RMPC: FEASIBILITY_CHECK: Termined. (0.05 sec)



Verbose Mode

- Normal
- Silent
- Loud (*recommended*)

Feasibility Check / Loud

FEAS_CHK: Inverse Lyapunov matrix MIN_EIG = 3.275738e+00.
FEAS_CHK: I:1: Inverse SDLF matrix W MIN_EIG = 3.115516e-01.
FEAS_CHK: I:2: Inverse SDLF matrix W MIN_EIG = 3.115516e-01.
FEAS_CHK: I:1: Inverse SDLF matrix Q MIN_EIG = 1.482989e-06.
FEAS_CHK: I:2: Inverse SDLF matrix Q MIN_EIG = 1.480064e-06.
FEAS_CHK: I:1: Invariant ellipsoid matrix MIN_EIG = 1.566609e-08.
FEAS_CHK: I:2: Invariant ellipsoid matrix MIN_EIG = 1.439442e-08.
FEAS_CHK: VTX:1 | I:1: Stability condition matrix MIN_EIG = 8.704402e-06.
FEAS_CHK: VTX:1 | I:2: Stability condition matrix MIN_EIG = 8.705740e-06.
FEAS_CHK: VTX:2 | I:1: Stability condition matrix MIN_EIG = -5.363964e-06.
FEAS_CHK: VTX:2 | I:1: Stability condition violated!
... (*shortened*)

Feasibility Check / Loud

FEAS_CHK: Inverse Lyapunov matrix MIN_EIG = 3.275738e+00.

FEAS_CHK: I:1: Inverse SSDLF matrix W MIN_EIG = 3.115516e-01.

FEAS_CHK: I:2: Inverse SSDLF matrix W MIN_EIG = 3.115516e-01.

FEAS_CHK: I:1: Inverse SSDLF matrix Q MIN_EIG = 1.482989e-06.

FEAS_CHK: I:2: Inverse SSDLF matrix Q MIN_EIG = 1.480064e-06.

FEAS_CHK: I:1: Invariant ellipsoid matrix MIN_EIG = 1.566609e-08.

FEAS_CHK: I:2: Invariant ellipsoid matrix MIN_EIG = 1.439442e-08.

FEAS_CHK: VTX:1 | I:1: Stability condition matrix MIN_EIG = 8.704402e-06.

FEAS_CHK: VTX:1 | I:2: Stability condition matrix MIN_EIG = 8.705740e-06.

FEAS_CHK: VTX:2 | I:1: Stability condition matrix MIN_EIG = -5.363964e-06.

FEAS_CHK: VTX:2 | I:1: Stability condition violated!

... (shortened)

Feasibility Check / Loud

FEAS_CHK: Inverse Lyapunov matrix MIN_EIG = 3.275738e+00.
FEAS_CHK: I:1: Inverse SSDLF matrix W MIN_EIG = 3.115516e-01.
FEAS_CHK: I:2: Inverse SSDLF matrix W MIN_EIG = 3.115516e-01.
FEAS_CHK: I:1: Inverse SSDLF matrix Q MIN_EIG = 1.482989e-06.
FEAS_CHK: I:2: Inverse SSDLF matrix Q MIN_EIG = 1.480064e-06.
FEAS_CHK: I:1: Invariant ellipsoid matrix MIN_EIG = 1.566609e-08.
FEAS_CHK: I:2: Invariant ellipsoid matrix MIN_EIG = 1.439442e-08.
FEAS_CHK: VTX:1 | I:1: Stability condition matrix MIN_EIG = 8.704402e-06.
FEAS_CHK: VTX:1 | I:2: Stability condition matrix MIN_EIG = 8.705740e-06.
FEAS_CHK: VTX:2 | I:1: Stability condition matrix MIN_EIG = -5.363964e-06.
FEAS_CHK: VTX:2 | I:1: Stability condition violated!
... (shortened)

Feasibility Check / Loud

FEAS_CHK: Inverse Lyapunov matrix MIN_EIG = 3.275738e+00.

FEAS_CHK: I:1: Inverse SDLF matrix W MIN_EIG = 3.115516e-01.

FEAS_CHK: I:2: Inverse SDLF matrix W MIN_EIG = 3.115516e-01.

FEAS_CHK: I:1: Inverse SDLF matrix Q MIN_EIG = 1.482989e-06.

FEAS_CHK: I:2: Inverse SDLF matrix Q MIN_EIG = 1.480064e-06.

FEAS_CHK: I:1: Invariant ellipsoid matrix MIN_EIG = 1.566609e-08.

FEAS_CHK: I:2: Invariant ellipsoid matrix MIN_EIG = 1.439442e-08.

FEAS_CHK: VTX:1 | I:1: Stability condition matrix MIN_EIG = 8.704402e-06.

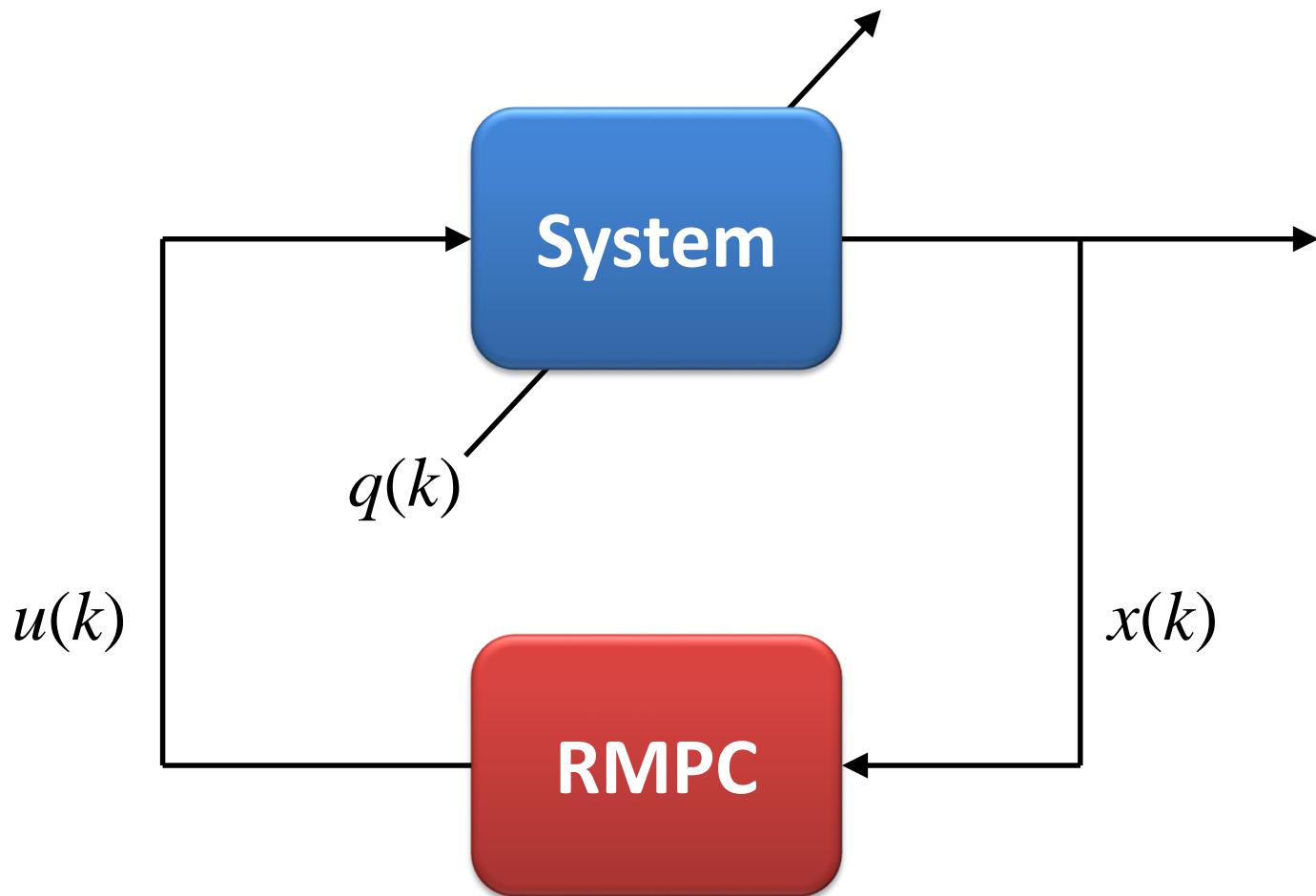
FEAS_CHK: VTX:1 | I:2: Stability condition matrix MIN_EIG = 8.705740e-06.

FEAS_CHK: VTX:2 | I:1: Stability condition matrix MIN_EIG = -5.363964e-06.

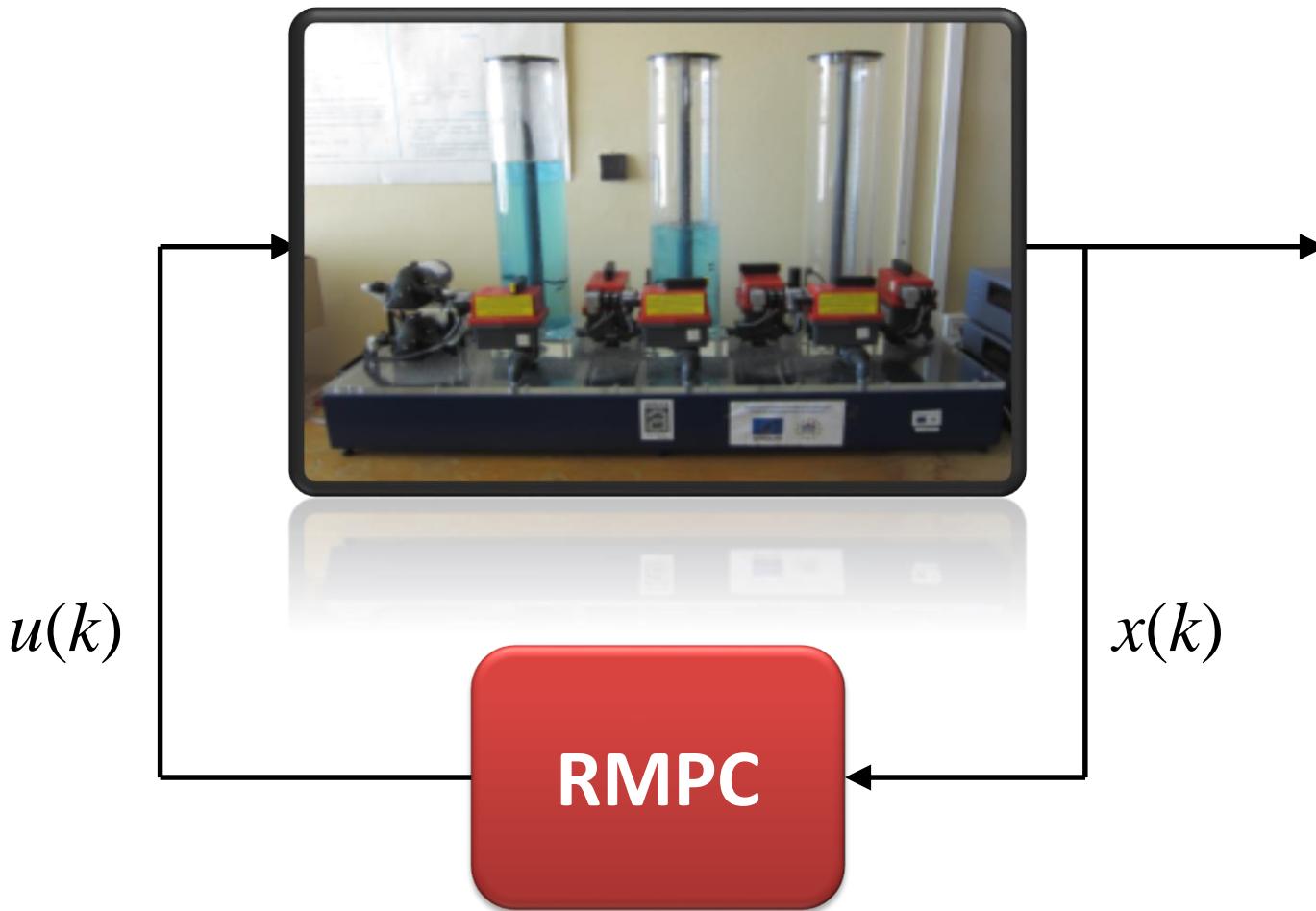
FEAS_CHK: VTX:2 | I:1: Stability condition violated!

... (shortened)

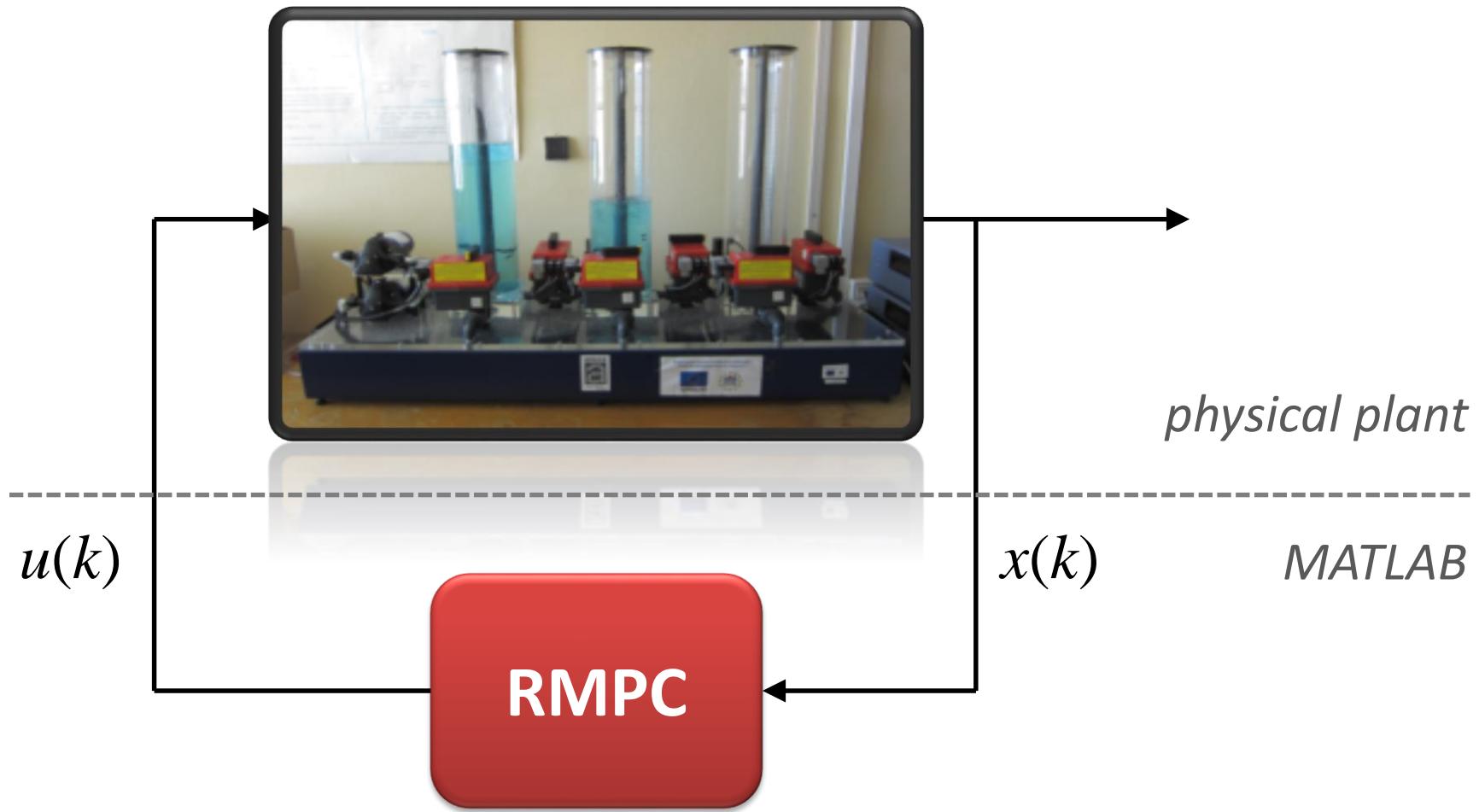
Case Study



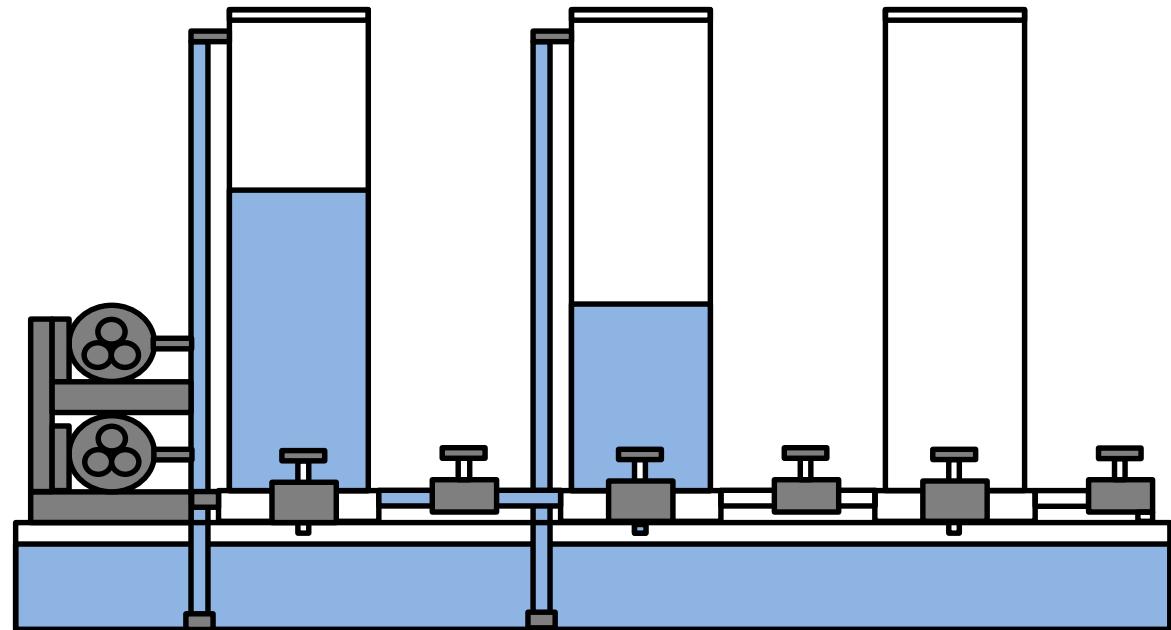
Case Study



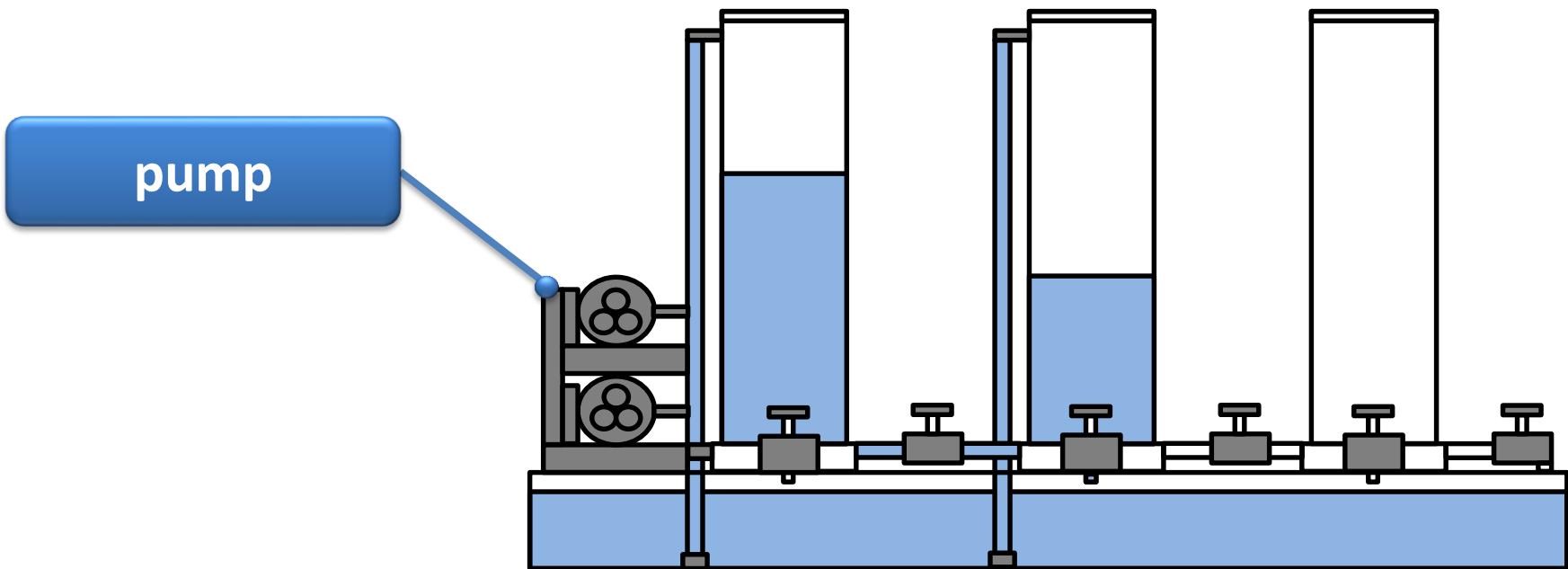
Case Study



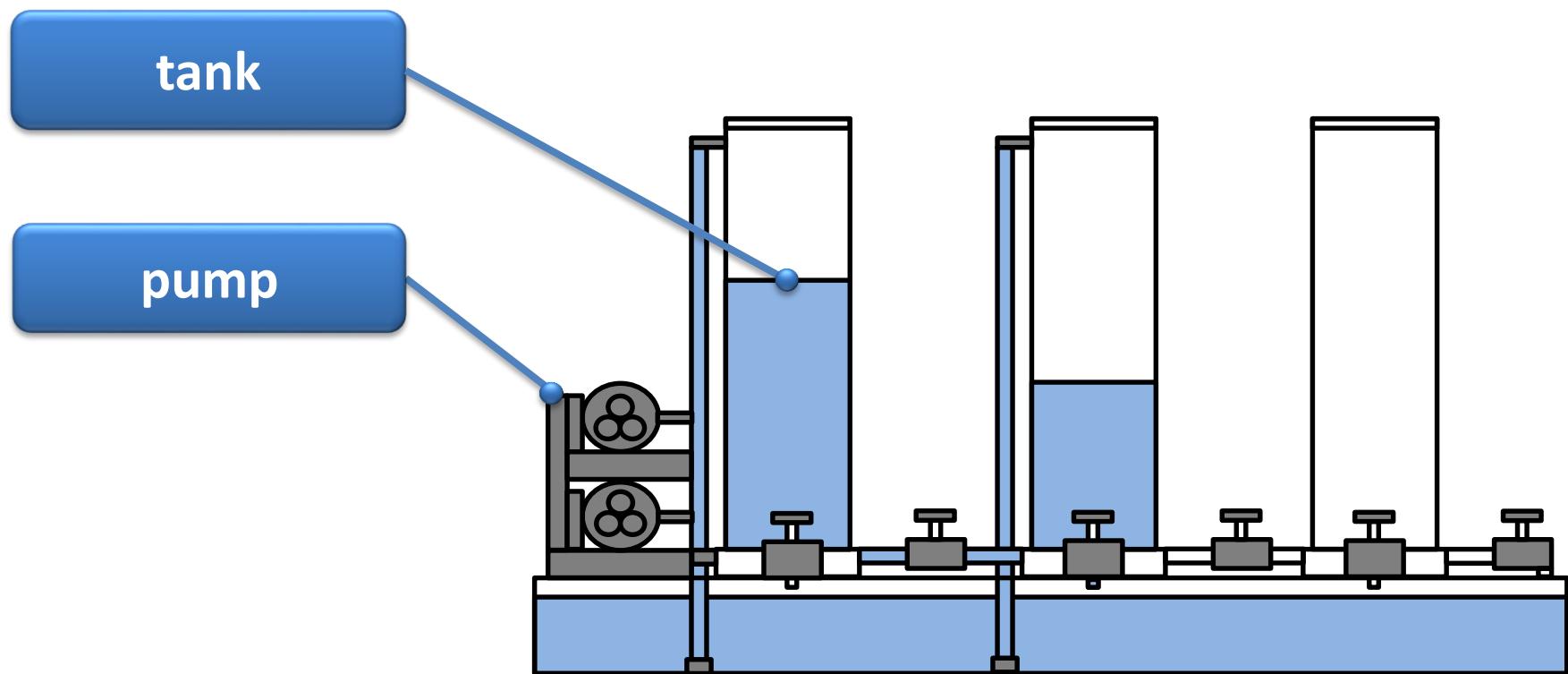
Liquid Tanks



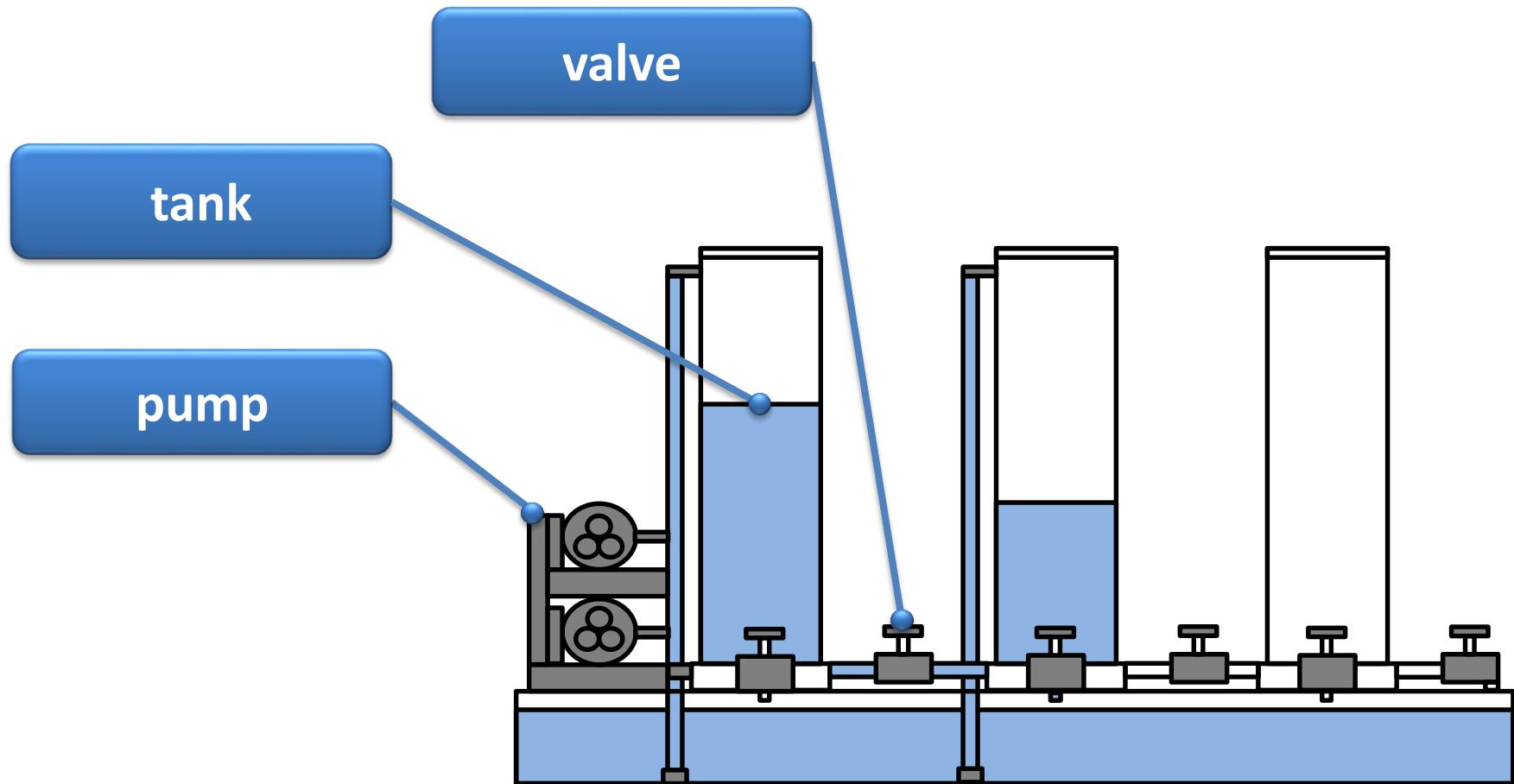
Liquid Tanks



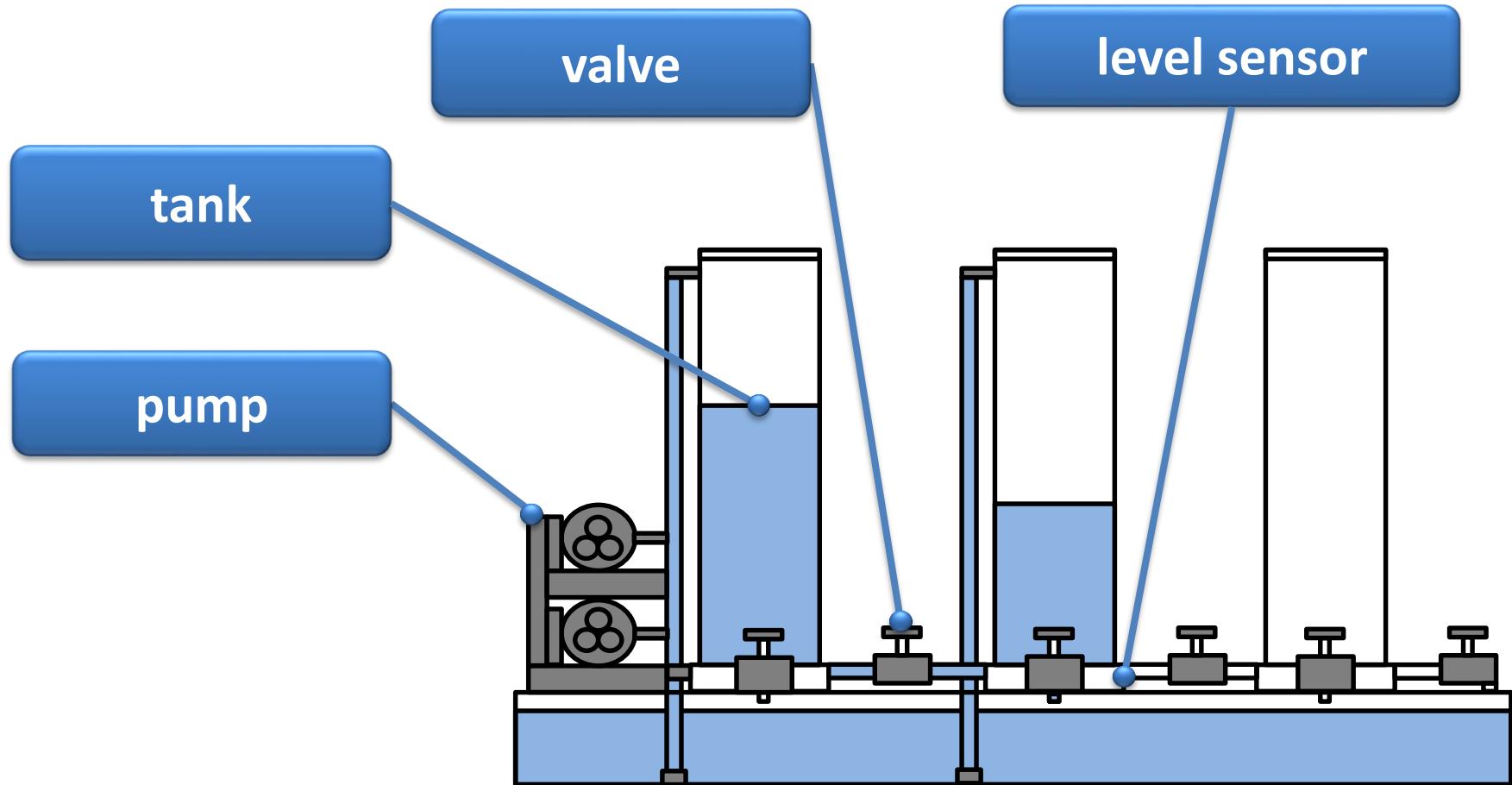
Liquid Tanks



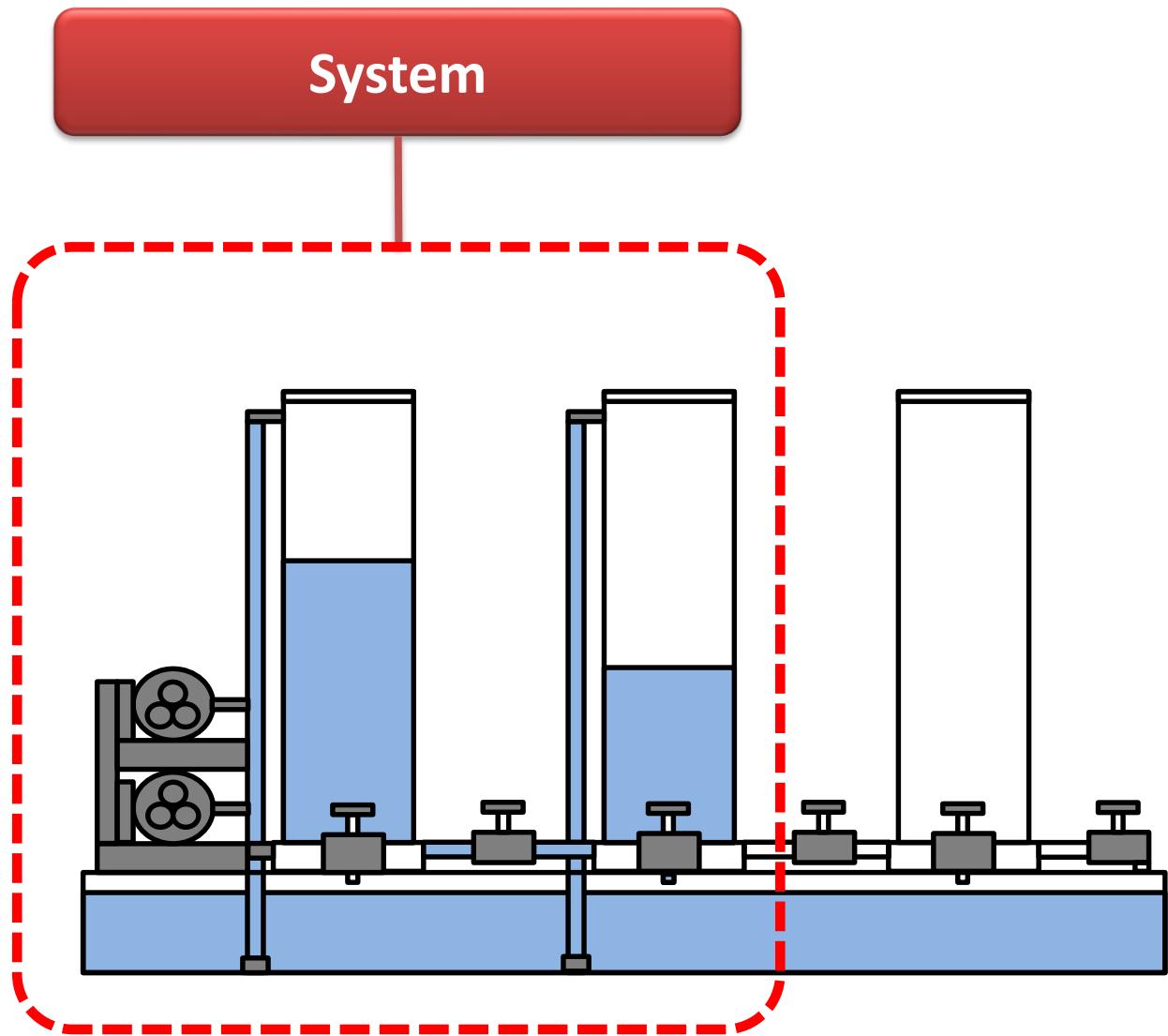
Liquid Tanks



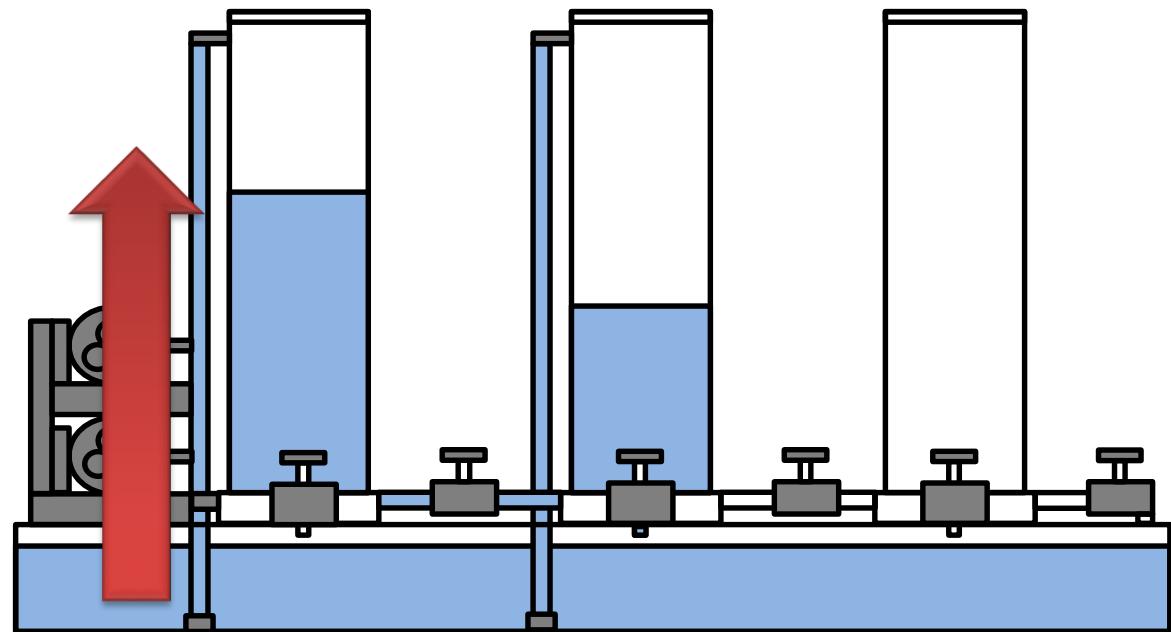
Liquid Tanks



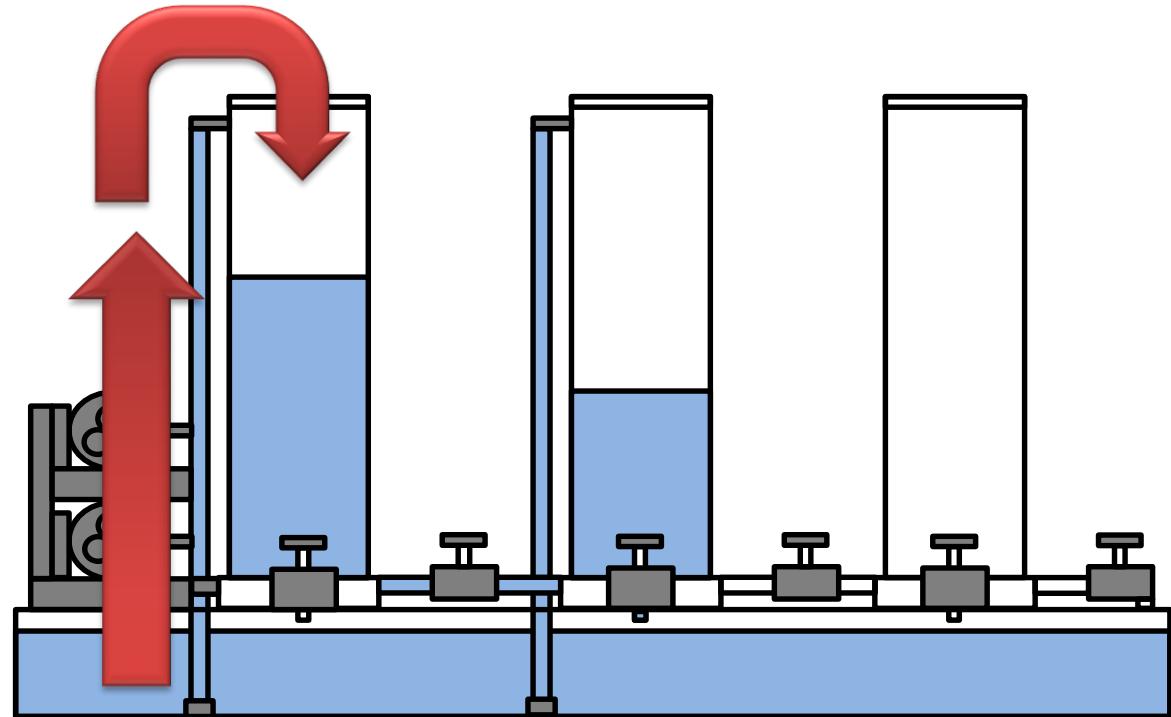
Liquid Tanks



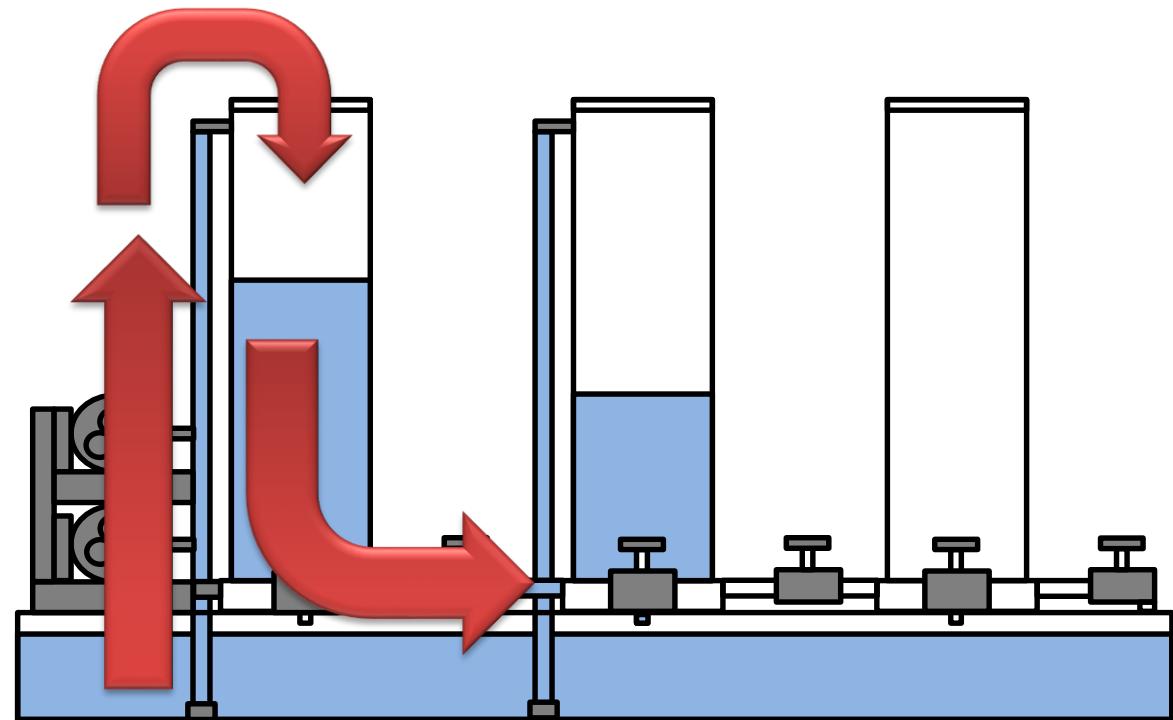
Liquid Tanks



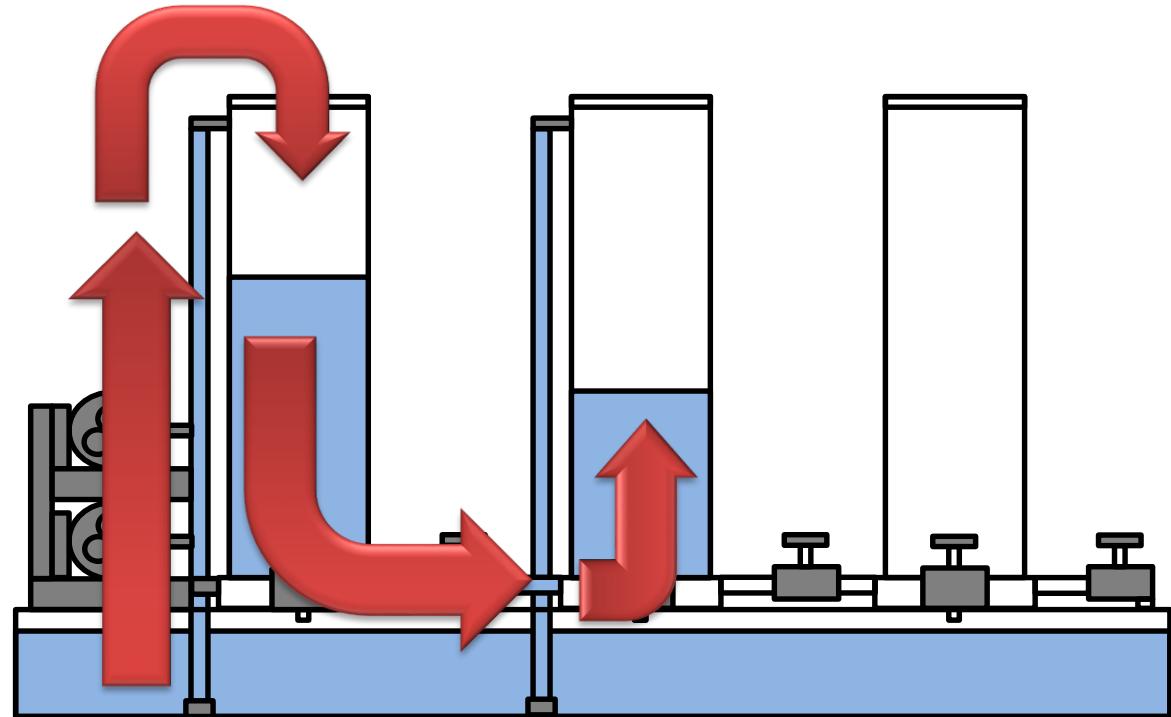
Liquid Tanks



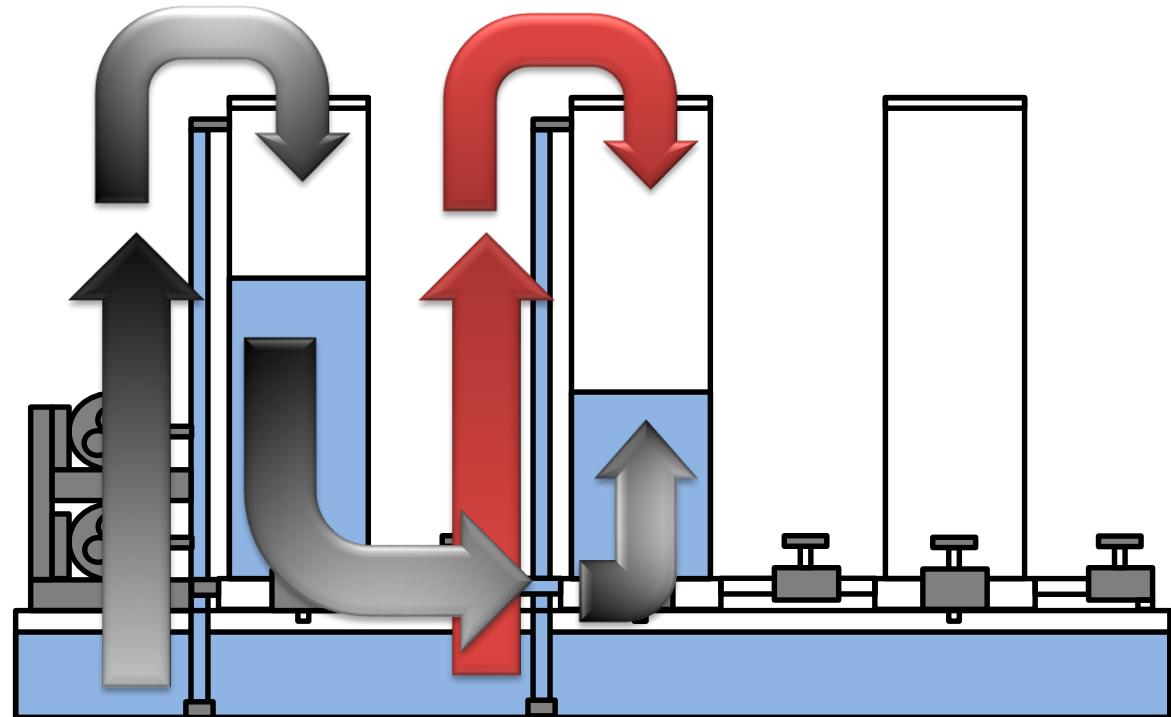
Liquid Tanks



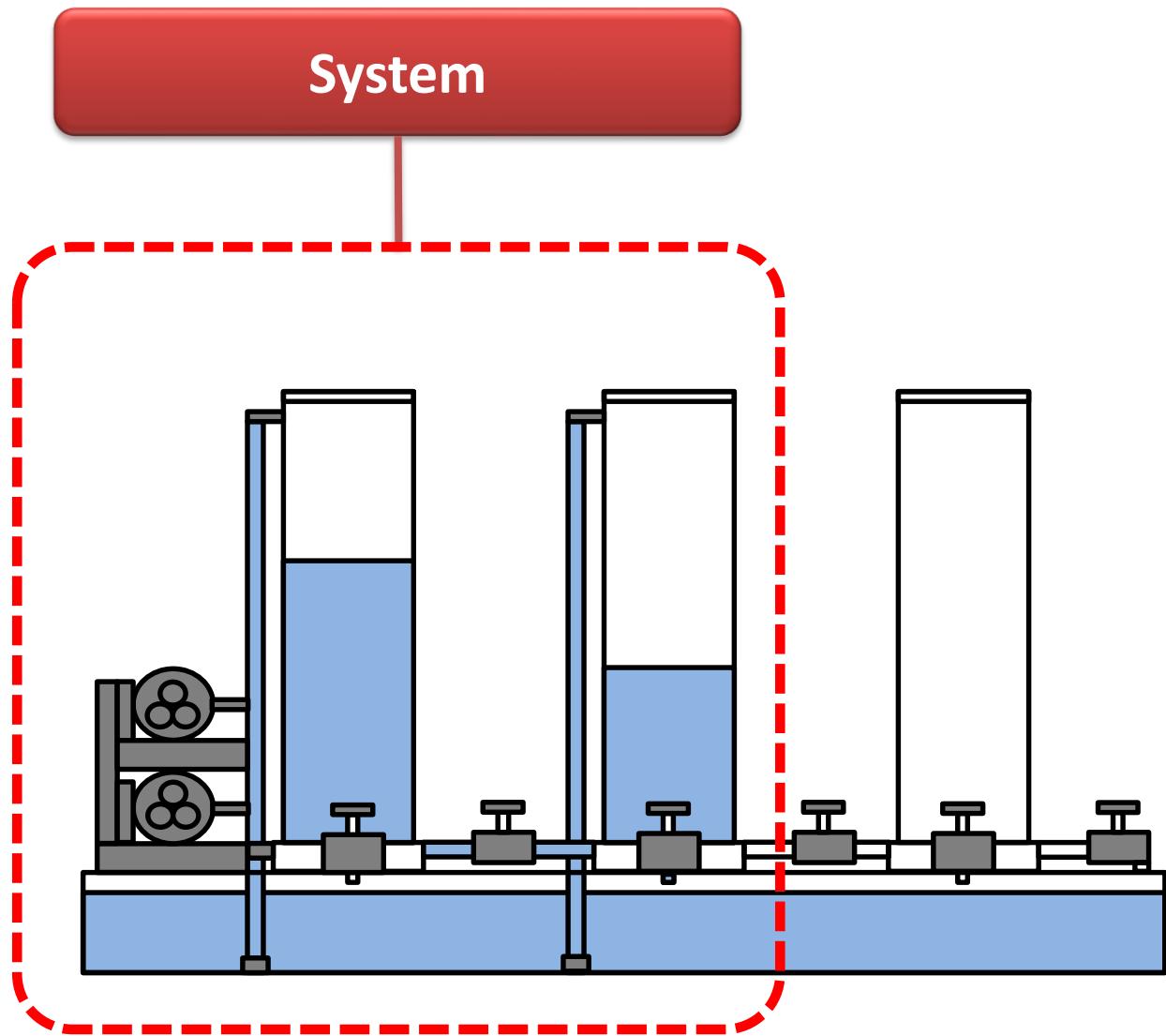
Liquid Tanks



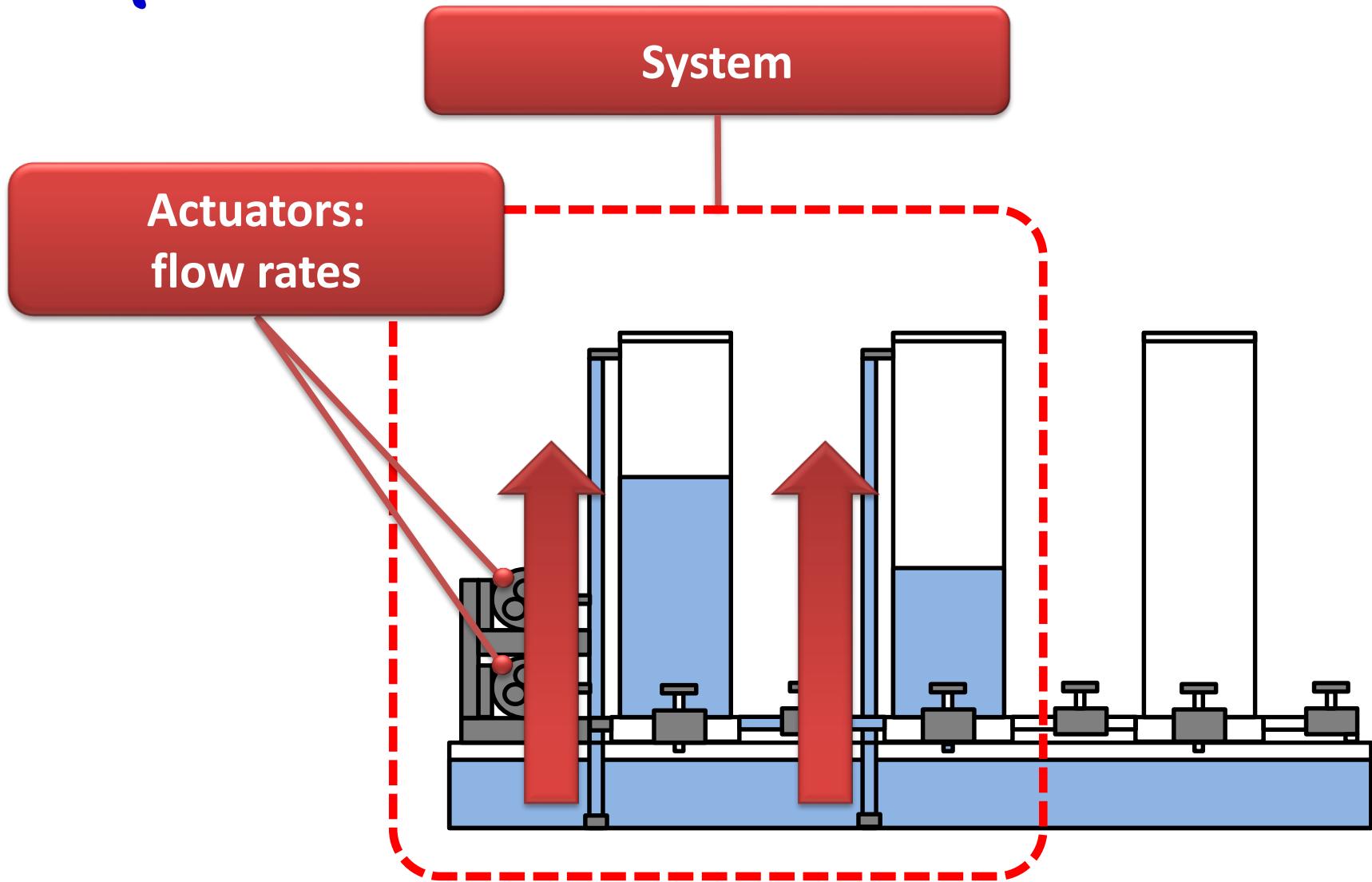
Liquid Tanks



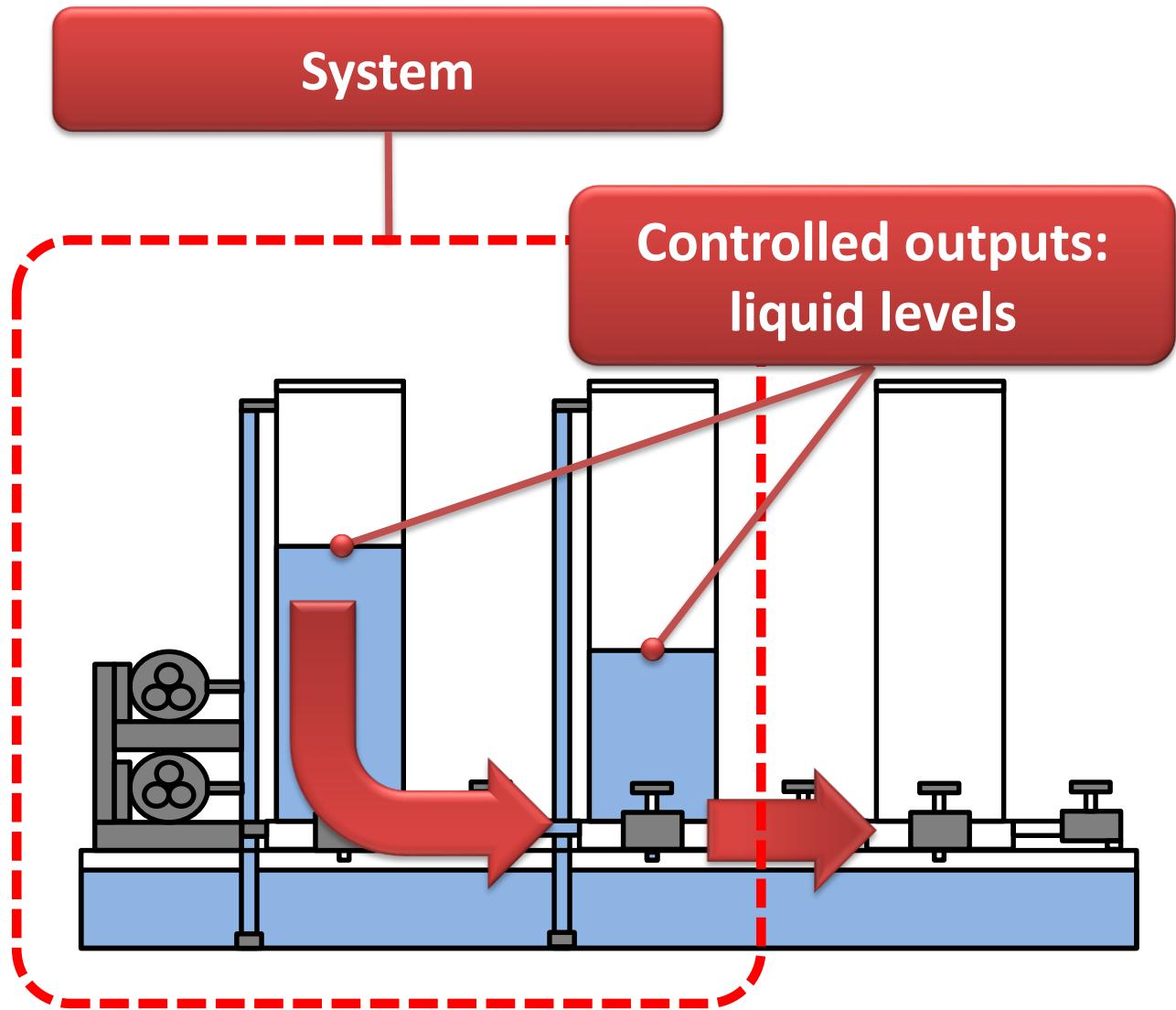
Liquid Tanks



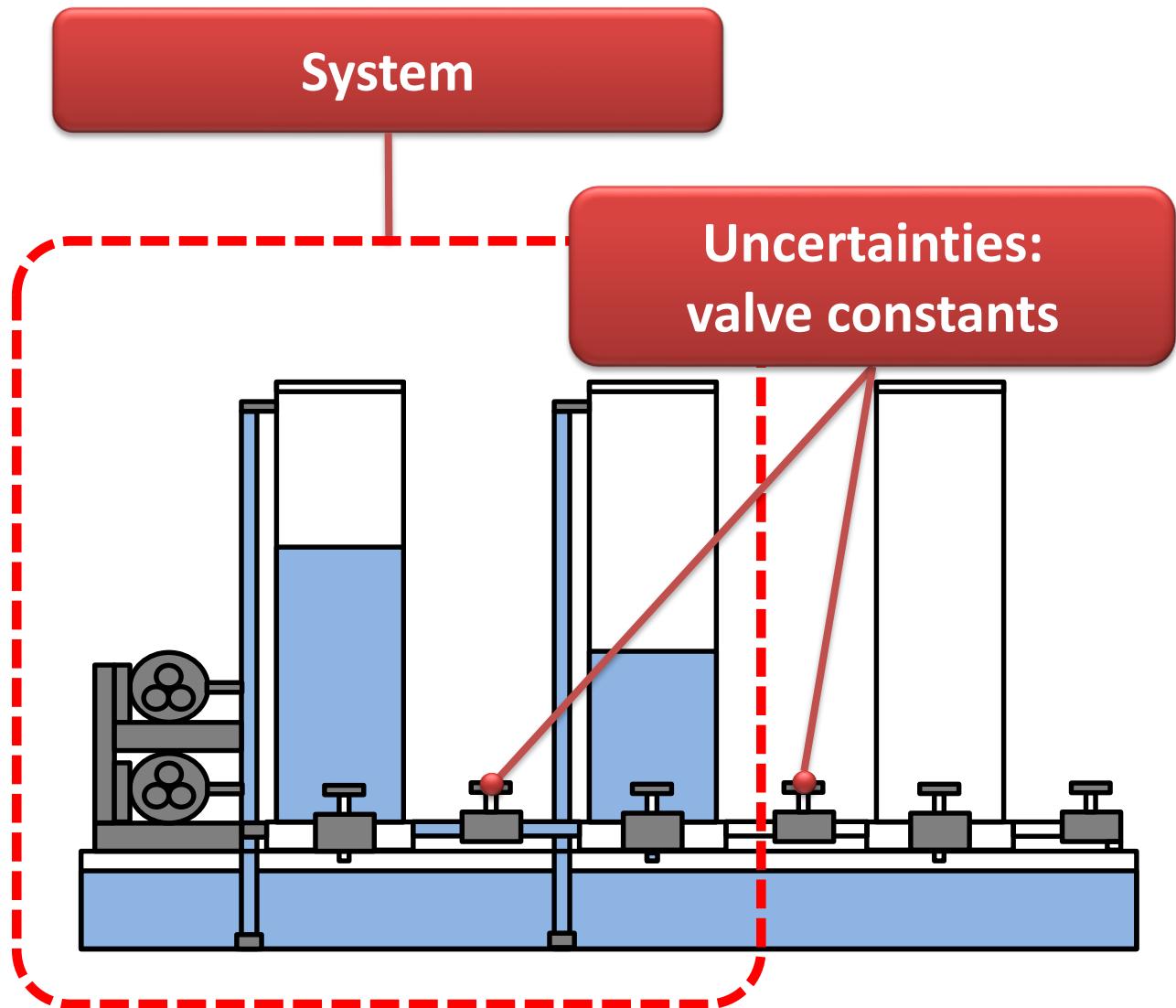
Liquid Tanks



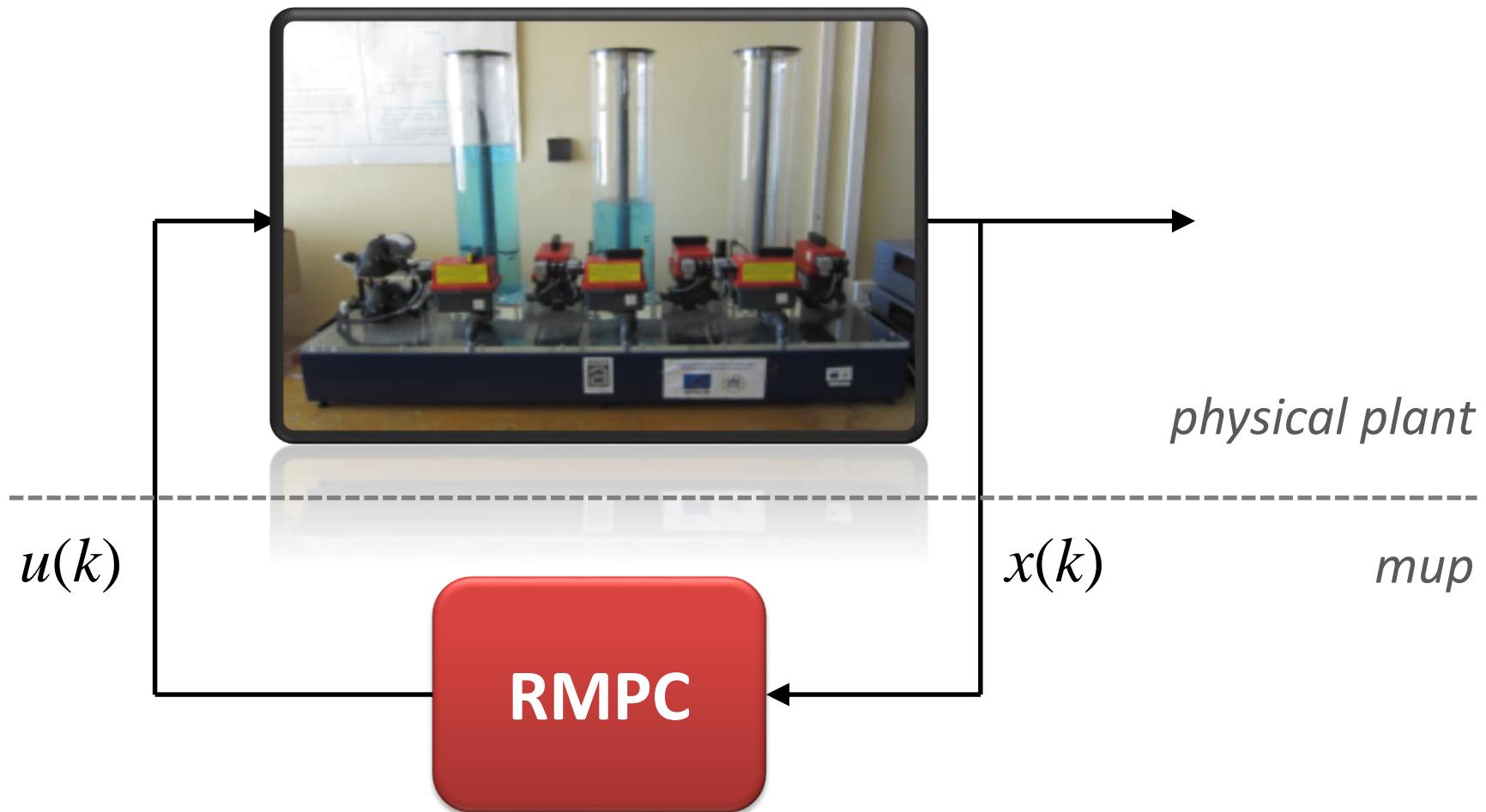
Liquid Tanks



Liquid Tanks



Case Study



Control Performance

Approach	Lyapunov function	$\text{diag}(W_x)$	$\text{diag}(W_u)$
RMPC ₁	single LF★	[500, 50]	[1, 0.1]
RMPC ₂	single LF★	[5, 5]	[1, 1]
RMPC ₃	PDLF★★	[5000, 50]	[0.1, 1]
RMPC ₄	PDLF★★	[5000, 50]	[1, 1]

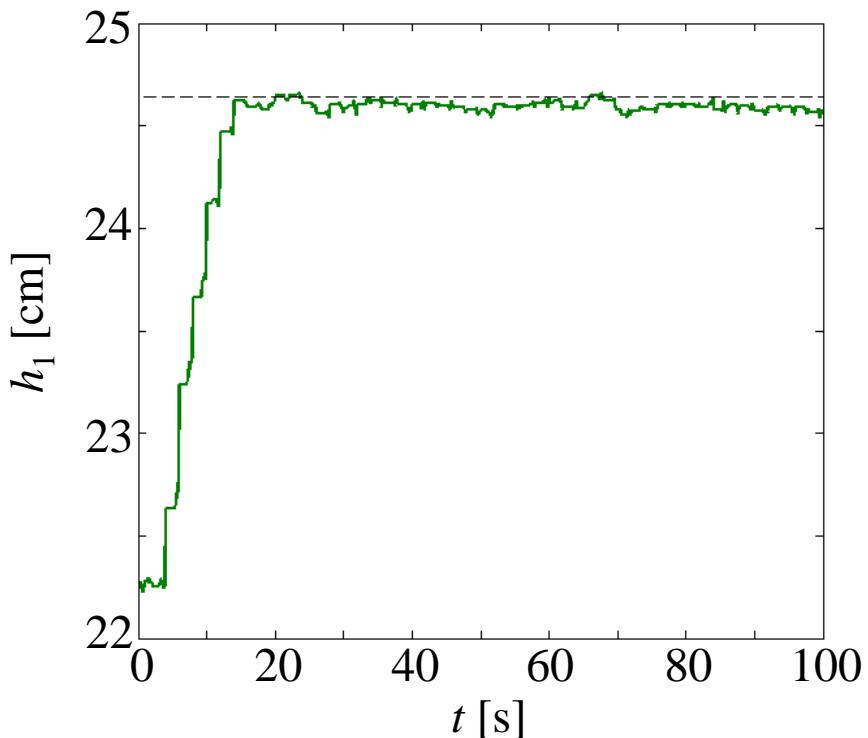
$$J = \sum_{t=0}^{\infty} (h - h_{\text{ref}})^\top W_x (h - h_{\text{ref}}) + (q - q_{\text{ref}})^\top W_u (q - q_{\text{ref}}))$$

*Kothare *et al.*, *Automatica*, 1996; ★★Cuzzola *et al.*, *Automatica*, 2002

Control Performance

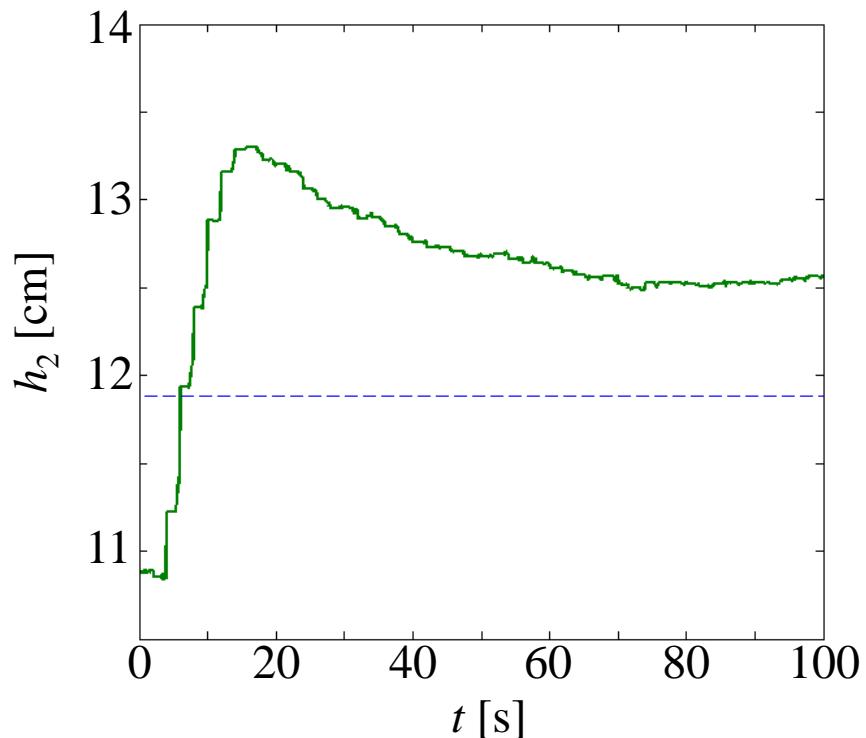
Liquid level in Tank #1

h_1 [cm]



Liquid level in Tank #2

h_2 [cm]

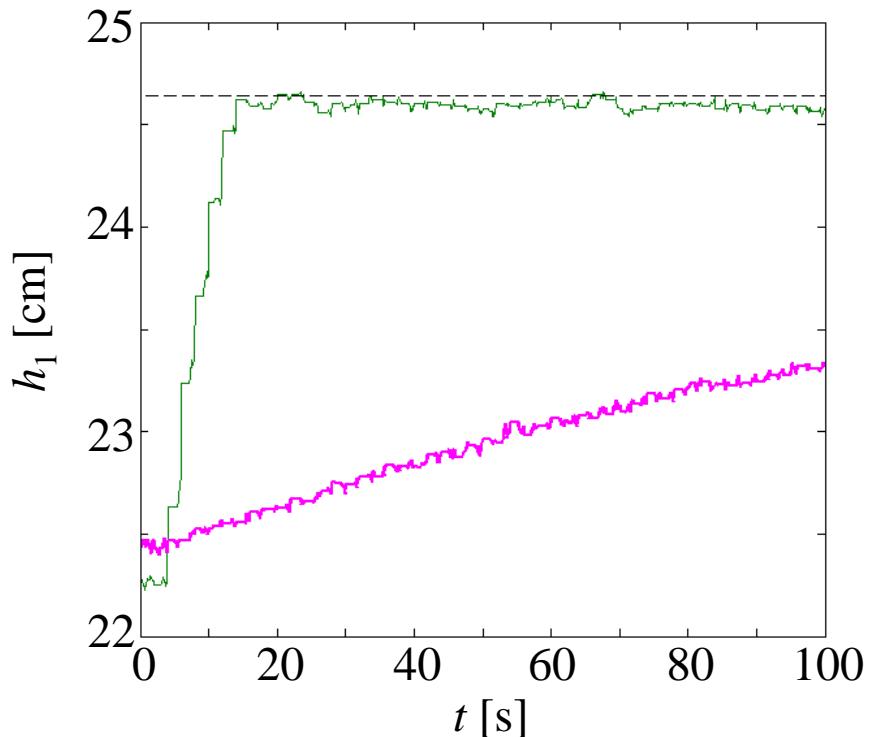


RMPC₁

Control Performance

Liquid level in Tank #1

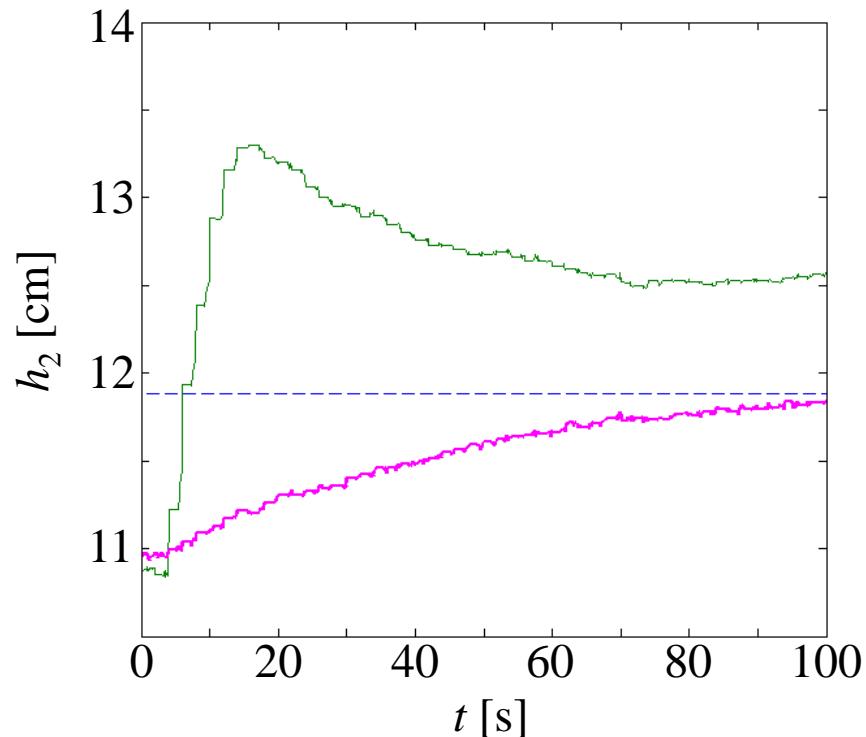
h_1 [cm]



RMPC₁

Liquid level in Tank #2

h_2 [cm]

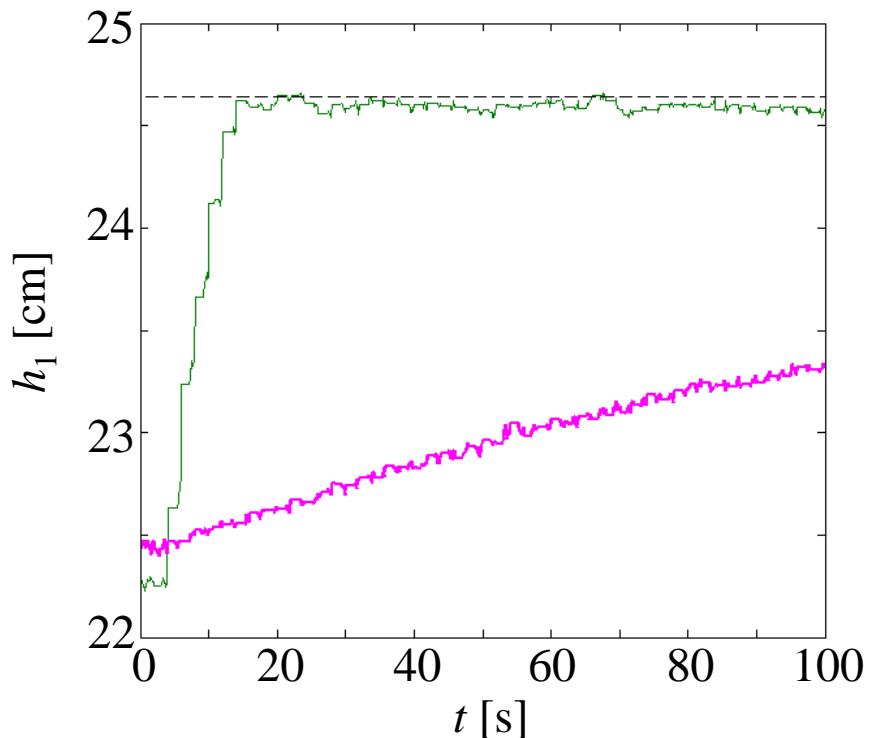


RMPC₂

Control Performance

Liquid level in Tank #1

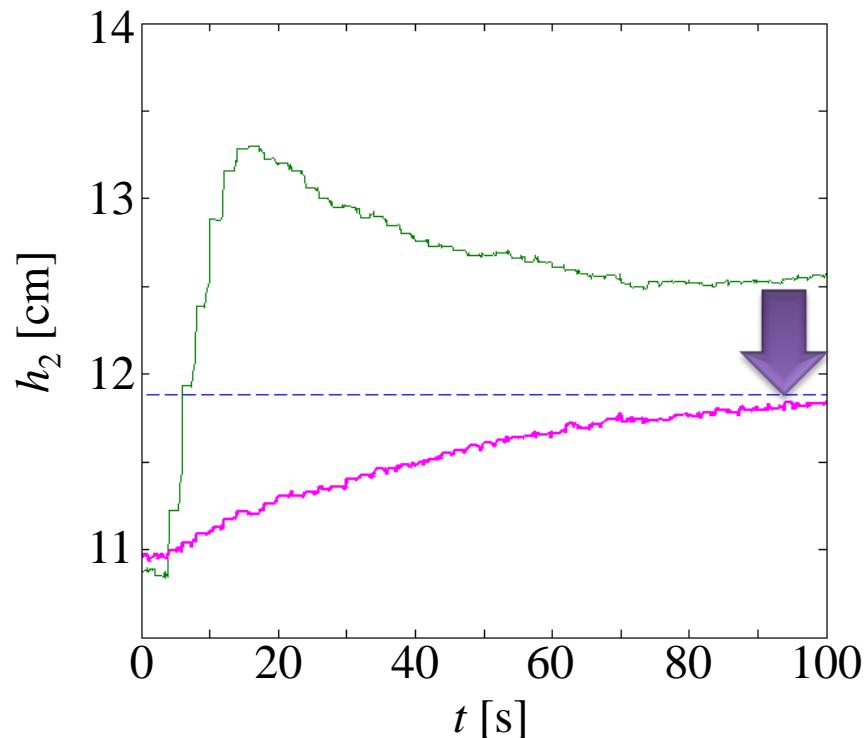
h_1 [cm]



RMPC₁

Liquid level in Tank #2

h_2 [cm]

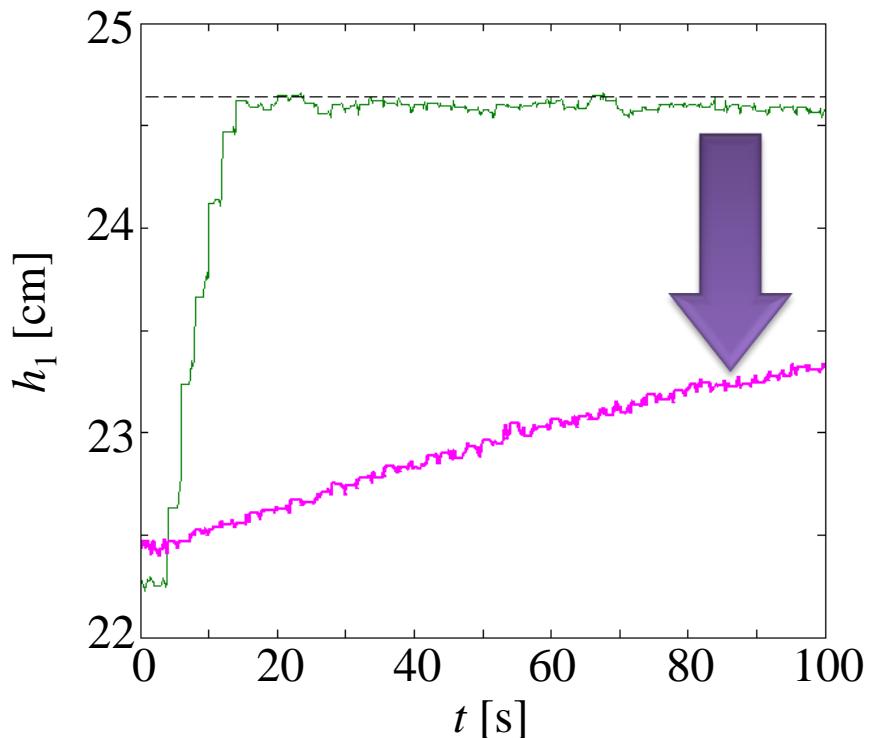


RMPC₂

Control Performance

Liquid level in Tank #1

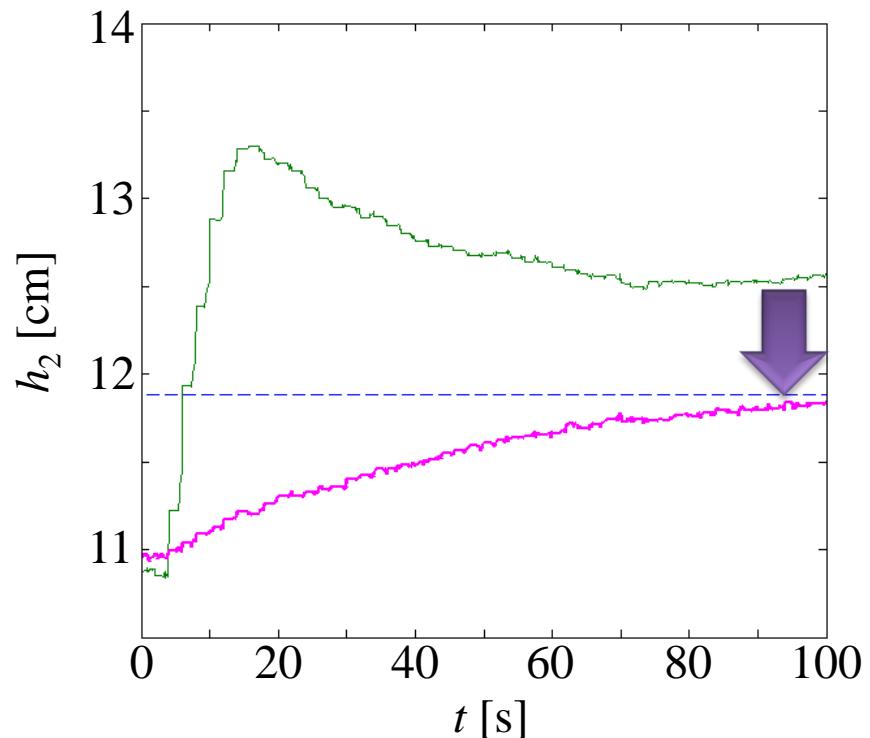
h_1 [cm]



RMPC_1

Liquid level in Tank #2

h_2 [cm]

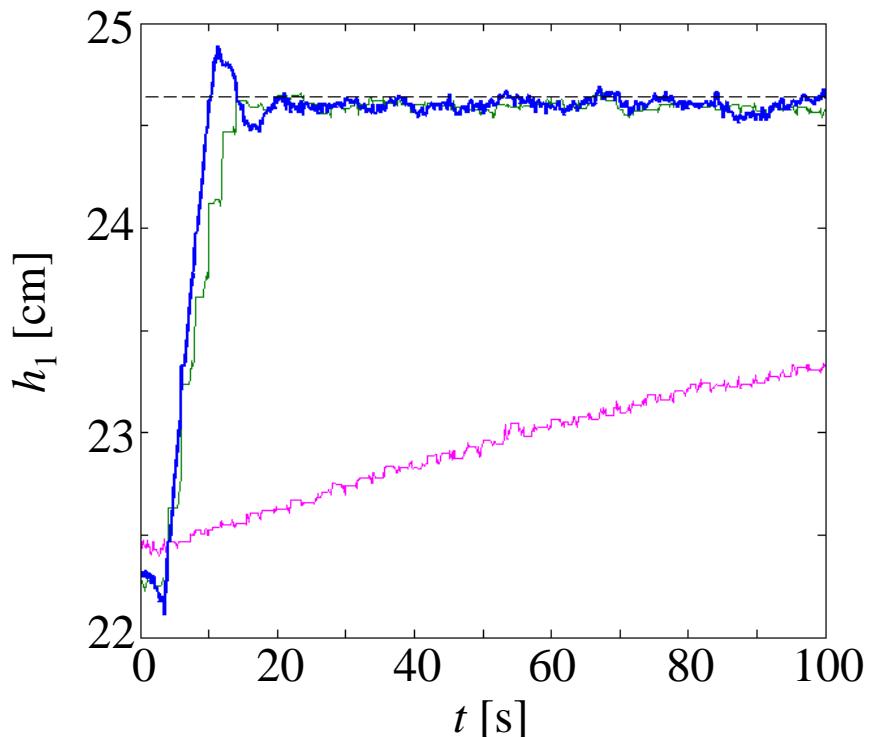


RMPC_2

Control Performance

Liquid level in Tank #1

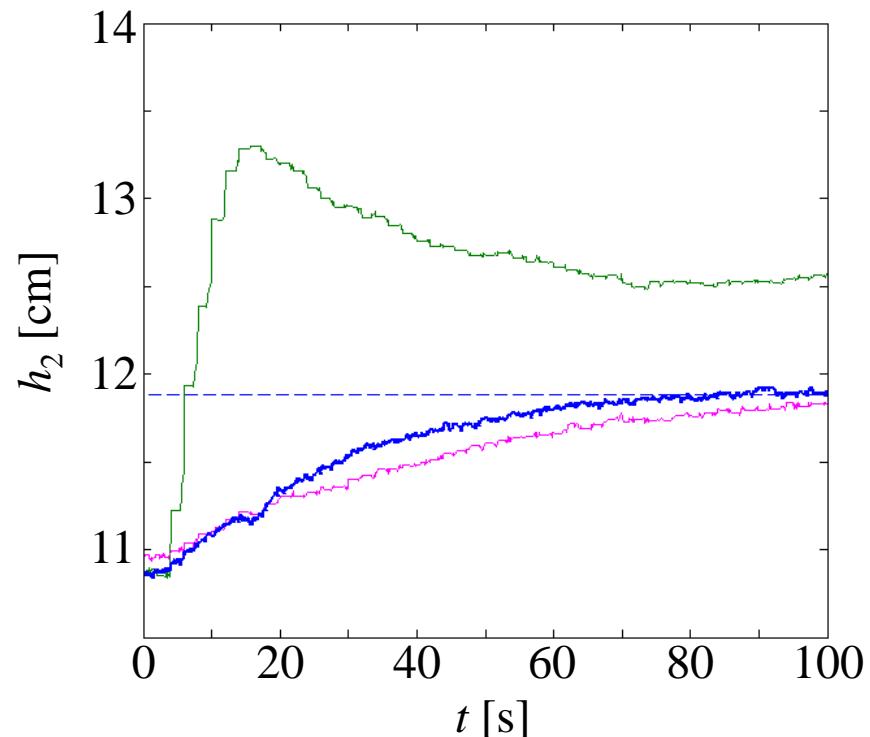
h_1 [cm]



RMPC_1

Liquid level in Tank #2

h_2 [cm]

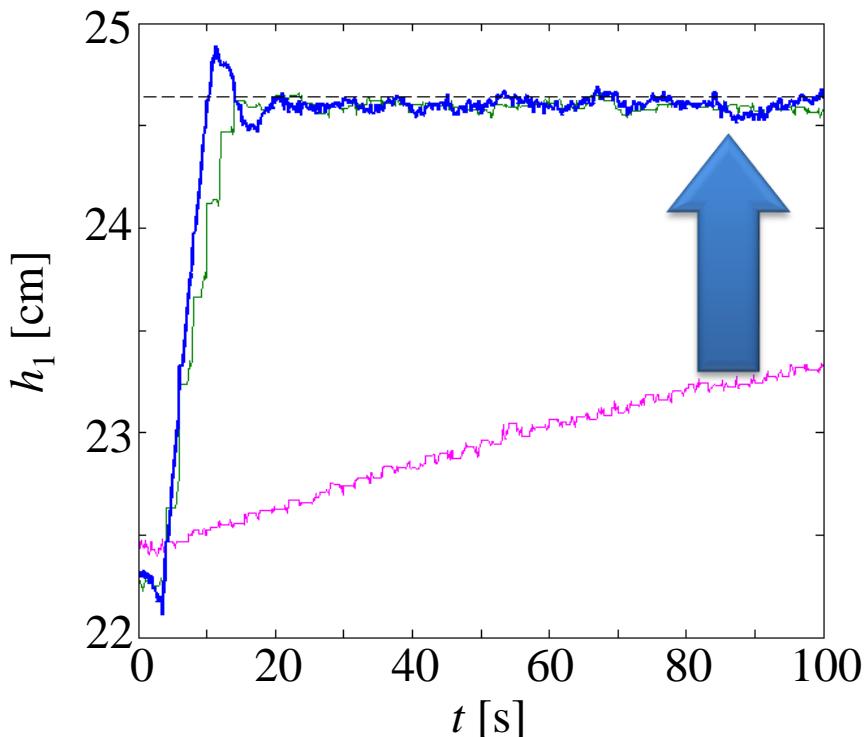


RMPC_3

Control Performance

Liquid level in Tank #1

h_1 [cm]

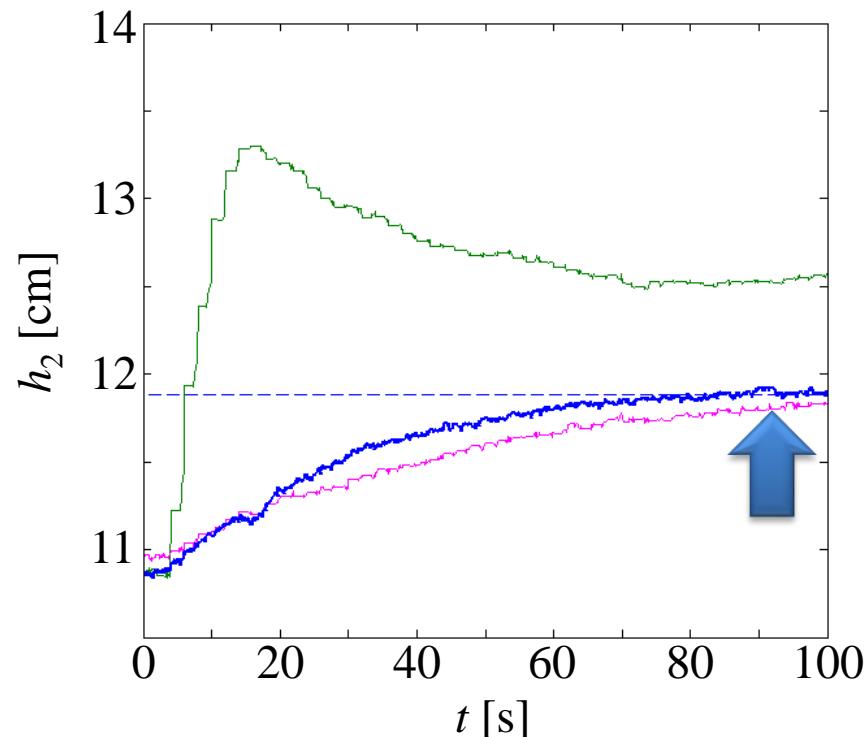


RMPC_1

RMPC_2

Liquid level in Tank #2

h_2 [cm]

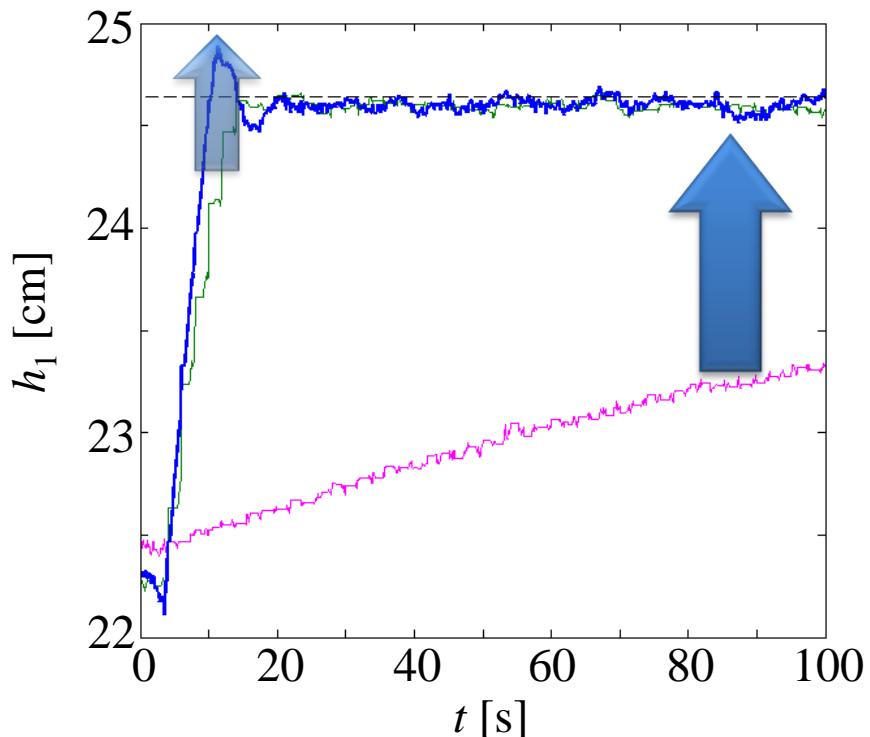


RMPC_3

Control Performance

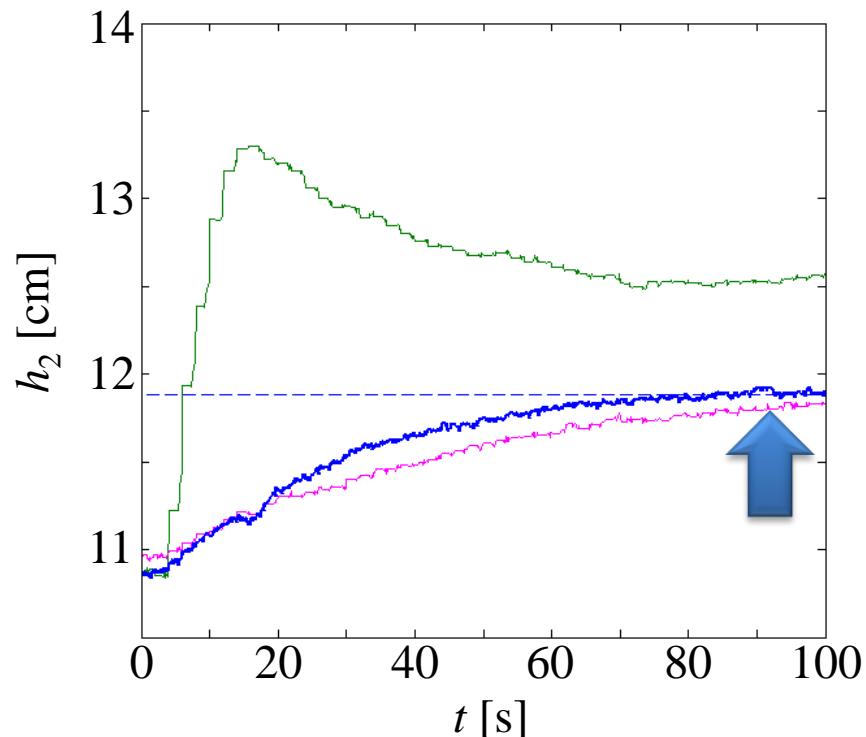
Liquid level in Tank #1

h_1 [cm]



Liquid level in Tank #2

h_2 [cm]



RMPC₁

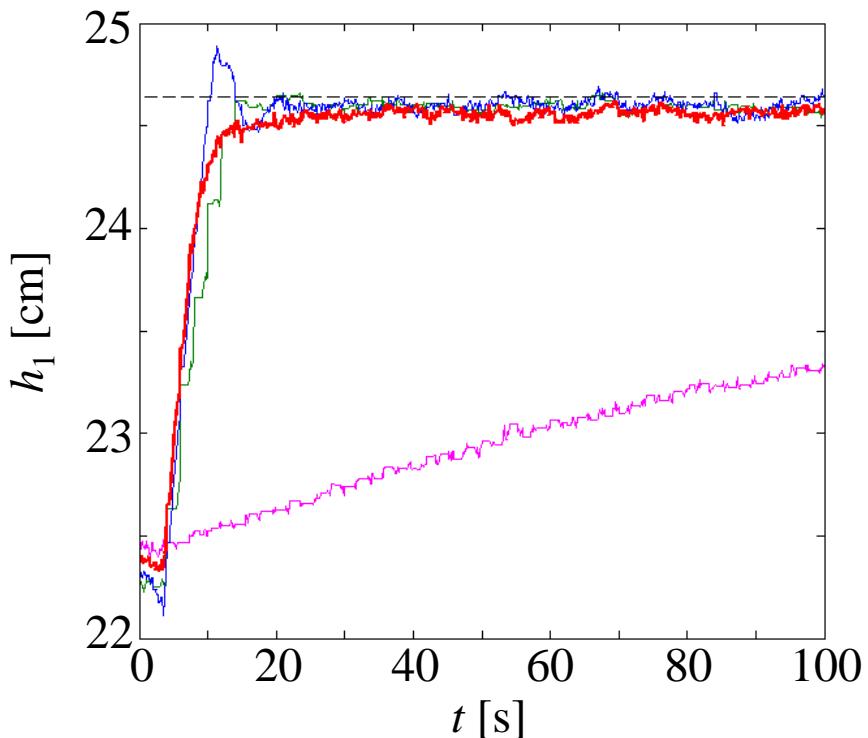
RMPC₂

RMPC₃

Control Performance

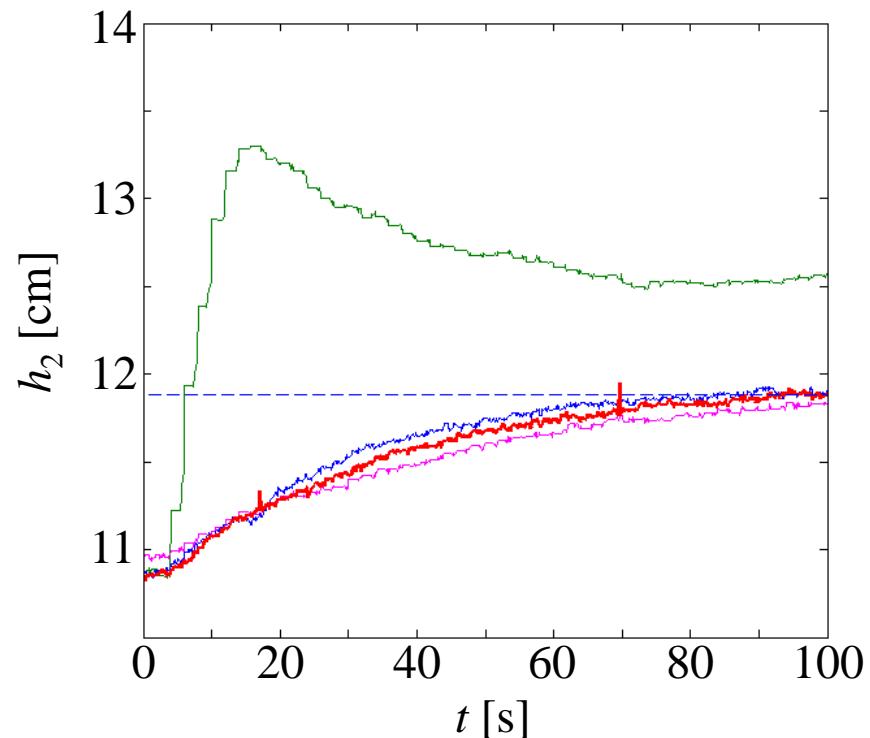
Liquid level in Tank #1

h_1 [cm]



Liquid level in Tank #2

h_2 [cm]



RMPC₁

RMPC₂

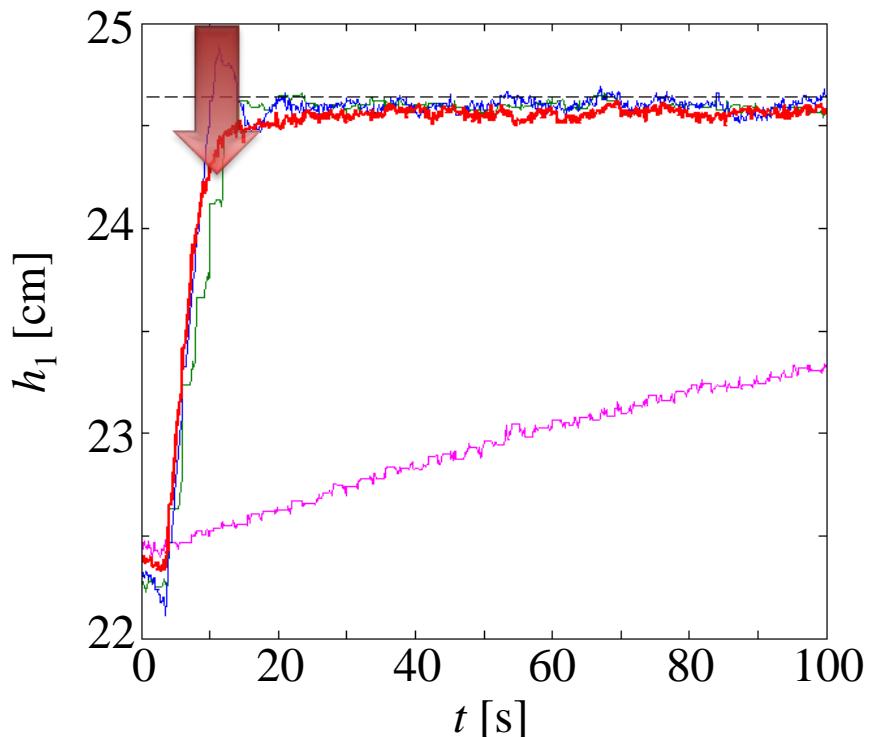
RMPC₃

RMPC₄

Control Performance

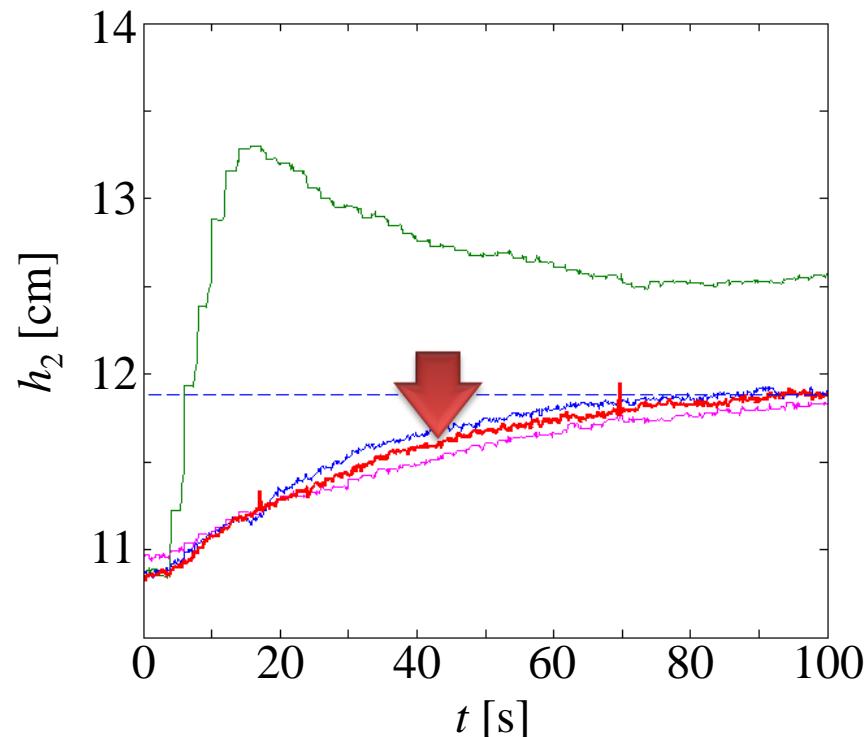
Liquid level in Tank #1

h_1 [cm]



Liquid level in Tank #2

h_2 [cm]



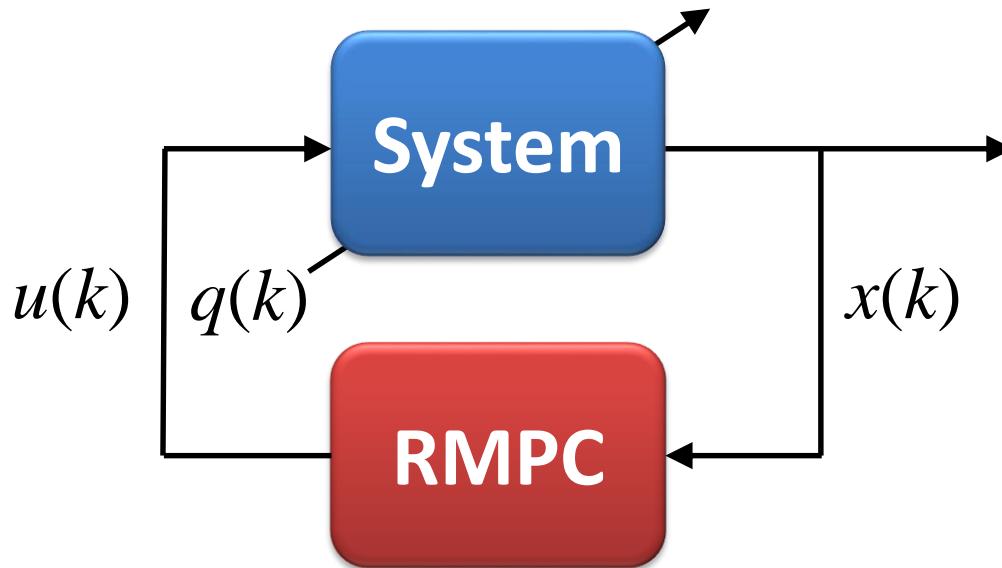
RMPC₁

RMPC₂

RMPC₃

RMPC₄

Toolbox



MATLAB toolbox for on-line RMPC design by LMIs

- speed up RMPC design and tuning
- advanced RMPC design analysis
- user-friendly tool

References

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